



AISSMS **INSTITUTE OF INFORMATION TECHNOLOGY** **(IOIT)**

ADDING VALUE TO ENGINEERING

An Autonomous Institute Affiliated to Savitribai Phule Pune University
Approved by AICTE, New Delhi and Recognised by Govt. of Maharashtra
Accredited by NAAC with "A+" Grade | NBA - 5 UG Programmes



EXAMINATION CIRCULAR NO. 300 OF 2025

MASTER OF TECHNOLOGY (2024/2025 PATTERN)

Endsem Examination Jan - 2026

INSTRUCTIONS FOR CANDIDATES

- Candidates are required to be present at the examination center, THIRTY MINUTES before the stipulated time.
- Candidates are forbidden from taking any material into the examination hall that can be treated as a malpractice.
- Candidates are requested to see the Notice Board at their center of examination regularly for changes if any that may be notified later in the program.
- No request shall be granted for change in time or date for the College Examination on any ground.
- Candidates are requested to note the Day, Date and Time of Paper.
- The exchanges of side-rules, drawing instruments of other materials used in the examination hall is not permitted at the time of examination. Candidates must bring their own instruments and will not be allowed to borrow from each other under any circumstances.
- Use of non-programmable battery operated electronic pocket size Calculator is allowed. The exchange of Calculators is not allowed.
- Electronics Devices such as mobile phone, smart watch, bluetooth etc. are not allowed at the time of Examination.
- All the students should be in college uniform and should carry their I-Card at the time of examination.
- The written examination will be conducted in the following order.



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F.Y M TECH (ELECTRICAL 2024/2025 Pattern)

(Power Electronics & Drives)

SEMESTER-I & II

Time - 10.00 am To 12.30 pm

Day & Date	Coure name	Course Code
12-01-2026 Monday	Applied Mathematics for Power Electronics Systems	PEPCC901
13-01-2026 Tuesday	Elective- I A) Power Electronics for Renewable Energy Sys. B) Control Des.Tech. for Power Electronics Sys. C) Power Electronics & FACTS Devices	PEPEC905
16-01-2026 Friday	AC and DC Drives	PEPCC1001
17-01-2026 Saturday	Analysis and Design of Inverters	PEPCC1002
19-01-2026 Monday	Elective- II A) Emerging Trends in Power Conv.Tech. B) Distributed Generation & Micro Grid C) Embedded Systems D) Electric Vehicles & Power Management	PEPEC1006
20-01-2026 Tuesday	Research Methodology	PEELC906



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F.Y M TECH (ELECTRONICS & TELECOMM. 2024/2025 Pattern)

(VLSI & Embedded Systems)

SEMESTER-I & II

Time – 10.00 am To 12.30 pm

Day & Date	Coure name	Course Code
12-01-2026 Monday	Reconfigurable Computing	VLPCC902
13-01-2026 Tuesday	Elective I A) Real Time Operating System B) MOS Device Modeling & Characterization C) Embedded Product Design D) Embedded Automotive System	VLPEC905
16-01-2026 Friday	System on Chip	VLPCC1002
17-01-2026 Saturday	ASIC Design	VLPCC1004
19-01-2026 Monday	Elective- II A) Mixed Signal IC Design B) High Speed ICs C) VLSI Signal Processing D) Memory Technologies	VLPEC1006
20-01-2026 Tuesday	Research Methodology	VLELC906

Date : 18/12/2025



(Signature)

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