# Savitribai Phule Pune University

# **Faculty of Science and Technology**



**Syllabus for** 

**B.E** (Electronics & Telecommunication Engineering)

# (Course 2019)

(w.e.f. June 2022)

	Savitriba B.E. (Electronic (With ef	:s &	Tel	ecom	mu	nica	tion)	2019		ourse	2			
			Se	meste	r-VI	I								
Course		5	each Schei urs/V	0		amiı	nation Ma		me	and		Cre	dit	
Code	Course Name	Theory	Practical	Tutorial	In-Sem	End-Sem	TW	PR	OR	Total	HT	PR	TUT	Total
404181	Radiation & Microwave Theory	03	-	-	30	70	-	-	-	100	03	-	-	03
404182	VLSI Design and Technology	03	-	-	30	70	-	-	-	100	03	-	-	03
404183	Cloud Computing	03	-	-	30	70	-	-	-	100	03	-	-	03
404184	Elective - 3	03	-	-	30	70	-	-	-	100	03	-	-	03
404185	Elective - 4	03	-	-	30	70	-	-	-	100	03	-	-	03
404186	Lab Practice - 1 (RMT & Cloud Computing)	-	04	-	-	-	25	-	50	75	-	02	-	02
404187	Lab Practice - 2 (VLSI Design & Elective -3)	-	04	-	-	-	25	50	-	75	-	02	-	02
404188	Project Stage - I	-	02	-	-	-	50	-	-	50	-	01	-	01
404189	Mandatory Audit Course 7	-	-	-	-	-	-	-	-	-	I	-	-	-
	Total	15	10	-	150	350	100	50	50	700	-	-	-	-
		1	1	1	1	То	tal Cr	edits	ı	1	15	05	-	20

Elective - 3	Elective - 4
1. Speech Processing	1. Data Mining
2. PLC SCADA & Automation	2. Electronic Product Development
3. JAVA Script	3. Deep Learning
4. Embedded & RTOS	4. Low Power CMOS
5. Modernized IoT	5. Smart Antennas

	Mandatory Audit Course - 7
1.	Management Information System
2.	Patent Search & Analysis
3.	Knowledge Management
4.	Energy Economics & Policy
5.	Educational Leadership
6.	Human Resource Development

	Savitril B.E. (Electron (With o	ics &	Tel fron	lecon	nmu lemi	nica c Ye	ntion)	201	9 Co	ourse	2			
Course	Course Teaching Teaching Examination Scheme and Marks Credit													
Code	Course Name	Theory	Practical	Tutorial	In-Sem	End-Sem	ΜT	PR	OR	Total	HT	PR	TUT	Total
404190	Fiber Optic Communication	03	-	-	30	70	-	-	-	100	03	-	-	03
404191	Elective - 5	03	-	-	30	70	-	-	-	100	03	-	-	03
404192	Elective - 6	03	-	-	30	70	-	-	-	100	03	-	-	03
404193	Innovation & Entrepreneurship	-	-	02	-	-	50	-	-	50	-	-	02	02
404194	Digital Business Management	-	-	02	-	-	50	-	-	50	-	-	02	02
404195	Fiber Optic Lab	-	02	-	-	-	25	-	50	75	-	01	-	01
404196	Lab Practice - 3 (Elective - 5)	-	02	-	-	-	25	50	-	75	-	01	-	01
404197	Project Stage - II	-	10	-	-	-	100	-	50	150	-	05	-	05
	Total	09	14	04	90	210	250	50	100	700	-	-	-	-
					1	То	otal Cr	edite	5	1	09	07	04	20

Elective - 5	Elective - 6
1. Biomedical Signal Processing	1. System on Chip
2. Industrial Drives & Automation	2. Nano Electronics
3. Android Development	3. Remote Sensing
4. Embedded System Design	4. Digital Marketing
5. Mobile Computing	5. Open Elective

	Program Outcomes (PO's)
Engi	neering Graduates will be able to:
1.	<b>Engineering knowledge</b> : Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization to the solution of complex engineering problems.
2.	<b>Problem analysis</b> : Identify, formulate, review research literature, and analyze complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.
3.	<b>Design/development of solutions</b> : Design solutions for complex engineering problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal, and environmental considerations.
4.	<b>Conduct investigations of complex problems</b> : Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of theinformation to provide valid conclusions.
5.	<b>Modern tool usage</b> : Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modeling to complex engineering activities with an understanding of the limitations.
6.	<b>The engineer and society</b> : Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice.
7.	<b>Environment and sustainability</b> : Understand the impact of the professional engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and needfor sustainable development.
8.	<b>Ethics</b> : Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice.
9.	<b>Individual and team work</b> : Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings.
10.	<b>Communication</b> : Communicate effectively on complex engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.
11.	<b>Project management and finance</b> : Demonstrate knowledge and understanding of the engineering and management principles and apply these to one's own work, as a member and leader in a team, to manage projects and in multidisciplinary environments.
12.	<b>Life-long learning</b> : Recognize the need for, and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.

# **SEMESTER - VII**

	Savitribai Phule	Pune University					
Four	th Year of <mark>E &amp; Tc Er</mark>	ngineering (2019 Course	2)				
4	04181: Radiation and	d Microwave Theory					
Teaching Scheme:   Credit   Examination Scheme:							
Theory: 03 Hrs. / We	ek 03	In-Sem (Theory):	30 Marks				
		End Sem (Theory):	70 Marks				
Prerequisite Courses, if a	ny:						
1. Electromagnetic Fi	eld Theory						
Companion Course, if an	y:						
1. Lab Practice – 1							
Course Objectives:							
distinguish various p CO2: Identify various m striplines and identif CO3: Explore constructio CO4: Explore constructio CO5: Analyze the structu microwave solid stat CO6: Know the various m	entals of electromagnetic performance parameters of odes in the waveguide. Of applications of the same in and working of principle in and working of principle re, characteristics, operati- te active devices.	c to derive free space propa f antenna. Compare: coaxial line, rectar e. es passive microwave devices es active microwave devices/ ion, equivalent circuits and ap e set ups of microwave measur ntal sustainability.	ngular waveguides & s/components. components. pplications of various				
Unit I	Fundamental T	heory of Radiation and	6 Hrs.				
	Radiat	ting Elements					
radiation mechanism and ty	ypes of antenna, performan	Friis transmission equation, Ince parameters such as radiation polarization, input impedance	on pattern, directivity				

Unit II	Transmission Lines and Waveguides6 Hrs.					
advantages and application of waveguides, compari- propagation in waveguide excitation in waveguides, of cavities, Striplines: Str	aves, short history of microwave engineering, frequency lons of microwaves (overall applications). Introduction to wave g son of waveguides and co-axial cables, Rectangular waveg es, cut off frequency, dominant mode, waveguide characteristics , coupling methods (probe, slot, loop), application of re-entrant ouctural details, types and applications.	uides, advantages uides, modes of s and parameters, cavities, coupling				
Mapping of Course Outcomes for Unit II						
Unit III	Passive Microwave Components	6 Hrs.				
plane, H-plane and ma	principle and scattering analysis of passive microwave compo- gic tee. Ferrite composition, characteristics and Faraday re- principle and scattering analysis of isolator, circulator and dir ton of gyrator.	otation principle.				
	CO3: Explore construction and working of principles pa	ssive microwave				
Outcomes for Unit III	devices / components.					
Unit IV	Active Microwave Components	<b>6 Hrs</b> .				
velocity modulation. Co	onal tubes, O and M type classification of microwave tubes, nonstruction, operation, performance analysis and applications Cylindrical wave magnetron and Helix traveling wave.					
· · ·	CO4: Explore construction and working of principles a devices/components.	ctive microwave				
Unit V	Solid State Microwave Devices	6 Hrs.				
Microwave transistors, I application as amplifiers	of operation, construction, characteristics, parameters w MOSFET, Varactor diodes, Parametric amplifiers, PIN diodes s, oscillators, modulators, demodulators, Schottky Barrier dio diode, Avalanche diode, Transit Time devices like IMPA <b>CO5: Analyze the structure, characteristics, operation, equiva</b>	s, Tunnel diodes, odes, Transferred ATT, TRAPATT				
Outcomes for Unit V	applications of various microwave solid state active devices.					

Mapping of Course Outcomes for Unit VI       CO6: Know the various microwave systems, device set ups of microwave measurement devices and Identify the effect of radiations on environmental sustainability.         Learning Resources         Text Books:         1. C.A. Balanis, "Antenna Theory - Analysis and Design", 4 <sup>th</sup> Edition, John Wiley.         2. Samuel Y. Liao, "Microwave Devices and Circuits", 3 <sup>rd</sup> Edition, Pearson.         3. Annapurna Das and Sisir K. Das, "Microwave Engineering", 2 <sup>nd</sup> Edition, Tata McGraw Hill.         Reference Books:         1. K. D. Prasad, "Antenna & Wave Propagation", 3 <sup>rd</sup> Edition, Satya Prakashan, New Delhi.         2. E.C. Jordon and E.G. Balman, "Electromagnetic Waves and Radiation Systems", 2 <sup>nd</sup> Edition, Prentice Hall Inc.         3. David M. Pozar, "Microwave Engineering", 4 <sup>th</sup> Edition, John Wiley.         4. Ahmad Shahid Khan, "Microwave Engineering: Concepts and Fundamentals", CRC Press         5. M. Kulkarni, "Microwave and Radar Engineering, 3 <sup>rd</sup> Edition, Umesh Publication         MOOC / NPTEL Courses:         1. NPTEL Course on "Microwave Theory and Techniques", By Prof. Girish Kumar, IIT Mumbai         Link: https://nptel.ac.in/courses/108101112         2. NPTEL Course on "Antenna", By Prof. Girish Kumar, IIT Mumbai	Unit VI	Microwave Systems and Microwave	6 Hrs.
Exage equation. Industrial applications of microwaves such as microwave heating, medical application such as microwave diathermy. Microwave measurement devices such as slotted line, tunable detector VSWR meter, power meter, and their working principles. Microwave measurement techniques to measure S-parameters, frequency, power, attenuation, VSWR, impedance. Radiation hazards and protection.           Mapping         O Course OC: Know the various microwave systems, device set ups of microwave measurement devices and Identify the effect of radiations on environmental sustainability.           Mapping         CO6: Know the various microwave systems, device set ups of microwave measurement devices and Identify the effect of radiations on environmental sustainability.           Learning Resources           Text Books:           1.         C.A. Balanis, "Antenna Theory - Analysis and Design", 4 <sup>th</sup> Edition, John Wiley.           2.         Samuel Y. Liao, "Microwave Devices and Circuits", 3 <sup>nd</sup> Edition, Pearson.           3.         Annapurna Das and Sisir K. Das, "Microwave Engineering", 2 <sup>nd</sup> Edition, Tata McGraw Hill.           Reference Books:         I. K. D. Prasad, "Antenna & Wave Propagation", 3 <sup>rd</sup> Edition, Satya Prakashan, New Delhi.           2. E.C. Jordon and E.G. Balman, "Electromagnetic Waves and Radiation Systems", 2 <sup>nd</sup> Edition, Prentice Hall Inc.           3.         David M. Pozar, "Microwave Engineering: Concepts and Fundamentals", CRC Press           5.         M. Kulkarni, "Microwave and Radar Engineering, 3 <sup>rd</sup> Edition, Umesh Publication           MOOC / NPTEL Courses         I. NPTEL Course on "Microwave Theor		<b>Measurement Techniques</b>	
Mapping of Course Outcomes for Unit VI       CO6: Know the various microwave systems, device set ups of microwave measurement devices and Identify the effect of radiations on environmental sustainability.         Learning Resources         Text Books:         1. C.A. Balanis, "Antenna Theory - Analysis and Design", 4 <sup>th</sup> Edition, John Wiley.         2. Samuel Y. Liao, "Microwave Devices and Circuits", 3 <sup>rd</sup> Edition, Pearson.         3. Annapurna Das and Sisir K. Das, "Microwave Engineering", 2 <sup>nd</sup> Edition, Tata McGraw Hill.         Reference Books:         1. K. D. Prasad, "Antenna & Wave Propagation", 3 <sup>rd</sup> Edition, Satya Prakashan, New Delhi.         2. E.C. Jordon and E.G. Balman, "Electromagnetic Waves and Radiation Systems", 2 <sup>nd</sup> Edition, Prentice Hall Inc.         3. David M. Pozar, "Microwave Engineering", 4 <sup>th</sup> Edition, John Wiley.         4. Ahmad Shahid Khan, "Microwave Engineering: Concepts and Fundamentals", CRC Press         5. M. Kulkarni, "Microwave and Radar Engineering, 3 <sup>rd</sup> Edition, Umesh Publication         MOOC / NPTEL Courses:         1. NPTEL Course on "Microwave Theory and Techniques", By Prof. Girish Kumar, IIT Mumbai         Link: https://nptel.ac.in/courses/108101112         2. NPTEL Course on "Antenna", By Prof. Girish Kumar, IIT Mumbai	range equation. Industria such as microwave diath VSWR meter, power n	al applications of microwaves such as microwave heating, marmy. Microwave measurement devices such as slotted line neter, and their working principles. Microwave measurem	edical application , tunable detector, ent techniques to
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	2. NPTEL Course of	n " <b>Antenna</b> ", By Prof. Girish Kumar, IIT Mumbai	
Link: <u>https://nptel.ac.in/courses/108101092</u>	Link• https://pr	ntel ac in/courses/108101092	

Savitribai Phule Pune University								
Four		igineering (2019 Course)						
roui								
404182: VLSI Design and Technology         Feaching Scheme:       Credit       Examination Scheme:								
<b>Teaching Scheme:</b>	Credit	<b>Examination Scheme:</b>						
Theory: 03 Hrs. / We	eek 03	In-Sem (Theory): 30	) Marks					
		End Sem (Theory): 70	) Marks					
Prerequisite Courses, if	any:							
1. Digital Electronics	;							
Companion Course, if an	ıy:							
1. Lab Practice – 2								
Course Objectives:								
<ol> <li>To train the student</li> <li>To realize important</li> <li>To overview an App Programmable Log</li> <li>Course Outcomes: On control</li> <li>CO1: Develop effective In CO2: Apply knowledge of CO3: Model digital circuit</li> <li>CO4: Design CMOS circuit</li> <li>CO5: Analyze various is</li> </ol>	as for Complementary Meta ace of testability in logic cir- plication Specific Integrated ic Devices (PLD) architectur ompletion of the course, lea HDL codes for digital design of real time issues in digital hit with HDL, simulate, syn cuits for specified application sues and constraints in design	d Circuit (ASIC) issues and to us ures with advanced features. Inner will be able to gn. I design. Ithesis and prototype in PLDs. ons. ign of an ASIC. Build In Self Test (BIST) circuit	) circuit designs. nderstand					
Unit I	Design	with HDL	7 Hrs.					
Sequential statements, Co Combinational, Sequenti Hierarchical and flat desig	oncurrent statements, Packa al circuits and FSM. S	ta types, Entity, Architecture & ages, Sub programs, Attributes, imulations, Synthesis, Efficie sis, Pipelining, Resource sharing <b>C codes for digital design.</b>	HDL modeling of nt coding styles,					
Unit II	Digital Des	sign and Issues	6 Hrs.					
Meta-stability and solution distribution, Clock jitter optimization. Interconnect	ons. Noise margin, Fan-ou r, Supply and ground I t routing techniques, Wire J	Mealy machines, HDL code fo at, Skew, Timing consideration bounce, Power distribution t parasitic, Signal integrity issues. eal time issues in digital design.	s, Hazards, Clock echniques, Power					

Unit III	PLD Architectures and Applications	<b>6 Hrs</b> .
Design Flow. CPLD	Architecture, Features, Specifications, Applications. FPG	A Architecture
-	ns, Applications. Clock management techniques. The	
· •	synthesis and implementation. Comparison of CPLD & FPG	
•	CO3: Model digital circuit with HDL, simulate, synthesis and p	
Outcomes for Unit III	PLDs.	
Unit IV	Digital CMOS Circuits	7 Hrs.
N-MOS, P-MOS and C	MOS. MOSFET parasitic, Technology scaling, Channel ler	igth modulation
	locity saturation. CMOS Inverter, Device sizing, CMOS con	0
	ons, Power delay product, Body Effect, Rise and fall times,	•
Transmission gates.	,	<b>.</b> F
Mapping of Course	CO4: Design CMOS circuits for specified applications.	
Outcomes for Unit IV		
Unit V	Application Specific Integrated Circuits	7 Hrs.
Design Flow Cell d	esign specifications, Spice simulation, AC and DC an	alvsis Transfe
methods of circuit eler	nt responses, Noise analysis, Lambda rules, Design Rule Ch ments, Layout of cell, Library cell designing for NAND a Rule Chack, Layout Vs, Schematic, Post layout Simulatic	& NOR, Circui
methods of circuit eler Extraction, Electrical I extraction, Design Issue	ments, Layout of cell, Library cell designing for NAND a Rule Check, Layout Vs. Schematic, Post-layout Simulatic es like Antenna effect, Electro migration effect, Cross talk a	& NOR, Circui on and Parasiti
methods of circuit eler Extraction, Electrical I extraction, Design Issue through, Timing analysi	ments, Layout of cell, Library cell designing for NAND a Rule Check, Layout Vs. Schematic, Post-layout Simulatic es like Antenna effect, Electro migration effect, Cross talk a	& NOR, Circui on and Parasitio and Drain puncl
methods of circuit eler Extraction, Electrical I extraction, Design Issue through, Timing analysi Mapping of Course Outcomes for Unit V	ments, Layout of cell, Library cell designing for NAND & Rule Check, Layout Vs. Schematic, Post-layout Simulatic es like Antenna effect, Electro migration effect, Cross talk a s. CO5: Analyze various issues and constraints in design of an AS	& NOR, Circui on and Parasitio and Drain puncl
methods of circuit eler Extraction, Electrical I extraction, Design Issue through, Timing analysi Mapping of Course	ments, Layout of cell, Library cell designing for NAND & Rule Check, Layout Vs. Schematic, Post-layout Simulatic es like Antenna effect, Electro migration effect, Cross talk a s.	& NOR, Circui on and Parasitio and Drain puncl
methods of circuit eler Extraction, Electrical I extraction, Design Issue through, Timing analysi Mapping of Course Outcomes for Unit V Unit VI	ments, Layout of cell, Library cell designing for NAND & Rule Check, Layout Vs. Schematic, Post-layout Simulatic es like Antenna effect, Electro migration effect, Cross talk a s. CO5: Analyze various issues and constraints in design of an AS	& NOR, Circui on and Parasitio and Drain punct SIC. 6 Hrs.
methods of circuit eler Extraction, Electrical I extraction, Design Issue through, Timing analysi Mapping of Course Outcomes for Unit V Unit VI Types of fault, Need of sensitizing, Test pattern	ments, Layout of cell, Library cell designing for NAND & Rule Check, Layout Vs. Schematic, Post-layout Simulatic es like Antenna effect, Electro migration effect, Cross talk a s. CO5: Analyze various issues and constraints in design of an AS VLSI Testing and Analysis	& NOR, Circui on and Parasitio and Drain punct SIC. 6 Hrs. ault models, Path
methods of circuit eler Extraction, Electrical I extraction, Design Issue through, Timing analysi Mapping of Course Outcomes for Unit V Unit VI Types of fault, Need of sensitizing, Test pattern TAP Controller.	ments, Layout of cell, Library cell designing for NAND & Rule Check, Layout Vs. Schematic, Post-layout Simulatic es like Antenna effect, Electro migration effect, Cross talk a s.           CO5: Analyze various issues and constraints in design of an AS           VLSI Testing and Analysis           Design for Testability (DFT), DFT Guideline, Testability, Fa generation, Sequential circuit test, Built In Self Test, JTAG &	<ul> <li>% NOR, Circui on and Parasition and Parasition and Drain punction of the second structure of</li></ul>
methods of circuit eler Extraction, Electrical I extraction, Design Issue through, Timing analysi Mapping of Course Outcomes for Unit V Unit VI Types of fault, Need of sensitizing, Test pattern TAP Controller. Mapping of Course	ments, Layout of cell, Library cell designing for NAND & Rule Check, Layout Vs. Schematic, Post-layout Simulatic es like Antenna effect, Electro migration effect, Cross talk a s.           CO5: Analyze various issues and constraints in design of an AS           VLSI Testing and Analysis           Design for Testability (DFT), DFT Guideline, Testability, Fa generation, Sequential circuit test, Built In Self Test, JTAG &	<ul> <li>% NOR, Circui on and Parasition and Parasition and Drain punction of the second structure of</li></ul>
methods of circuit eler Extraction, Electrical I extraction, Design Issue through, Timing analysi Mapping of Course Outcomes for Unit V Unit VI Types of fault, Need of sensitizing, Test pattern TAP Controller.	ments, Layout of cell, Library cell designing for NAND & Rule Check, Layout Vs. Schematic, Post-layout Simulatic es like Antenna effect, Electro migration effect, Cross talk a s.           CO5: Analyze various issues and constraints in design of an AS           VLSI Testing and Analysis           Design for Testability (DFT), DFT Guideline, Testability, Fa generation, Sequential circuit test, Built In Self Test, JTAG &	<ul> <li>% NOR, Circui on and Parasition and Parasition and Drain punch</li> <li>SIC.</li> <li>6 Hrs.</li> <li>wilt models, Pather Boundary scan</li> </ul>
methods of circuit eler Extraction, Electrical I extraction, Design Issue through, Timing analysi Mapping of Course Outcomes for Unit V Unit VI Types of fault, Need of sensitizing, Test pattern TAP Controller. Mapping of Course	ments, Layout of cell, Library cell designing for NAND & Rule Check, Layout Vs. Schematic, Post-layout Simulatic es like Antenna effect, Electro migration effect, Cross talk a s.           CO5: Analyze various issues and constraints in design of an AS           VLSI Testing and Analysis           Design for Testability (DFT), DFT Guideline, Testability, Fa generation, Sequential circuit test, Built In Self Test, JTAG &           Apply knowledge of testability in design and Build In Self Test	& NOR, Circui on and Parasition and Drain punct SIC. 6 Hrs. wilt models, Path a Boundary scan
methods of circuit eler Extraction, Electrical I extraction, Design Issue through, Timing analysi Mapping of Course Outcomes for Unit V Unit VI Types of fault, Need of sensitizing, Test pattern TAP Controller. Mapping of Course	ments, Layout of cell, Library cell designing for NAND & Rule Check, Layout Vs. Schematic, Post-layout Simulatic es like Antenna effect, Electro migration effect, Cross talk a s.           CO5: Analyze various issues and constraints in design of an AS           VLSI Testing and Analysis           Design for Testability (DFT), DFT Guideline, Testability, Fa generation, Sequential circuit test, Built In Self Test, JTAG &	& NOR, Circui on and Parasition and Drain punct SIC. 6 Hrs. wilt models, Path a Boundary scan
methods of circuit eler Extraction, Electrical I extraction, Design Issue through, Timing analysi Mapping of Course Outcomes for Unit V Unit VI Types of fault, Need of sensitizing, Test pattern TAP Controller. Mapping of Course Outcomes for Unit VI	ments, Layout of cell, Library cell designing for NAND & Rule Check, Layout Vs. Schematic, Post-layout Simulatic es like Antenna effect, Electro migration effect, Cross talk a s.           CO5: Analyze various issues and constraints in design of an AS           VLSI Testing and Analysis           Design for Testability (DFT), DFT Guideline, Testability, Fageneration, Sequential circuit test, Built In Self Test, JTAG &           Apply knowledge of testability in design and Build In Self Test           Learning Resources	& NOR, Circuit on and Parasiti and Drain punct SIC. 6 Hrs. tult models, Path a Boundary scart (BIST) circuit.
methods of circuit eler Extraction, Electrical I extraction, Design Issue through, Timing analysi Mapping of Course Outcomes for Unit V Unit VI Types of fault, Need of sensitizing, Test pattern TAP Controller. Mapping of Course Outcomes for Unit VI Text Books: 1. Charles H. Roth	ments, Layout of cell, Library cell designing for NAND & Rule Check, Layout Vs. Schematic, Post-layout Simulatic es like Antenna effect, Electro migration effect, Cross talk a s.           CO5: Analyze various issues and constraints in design of an AS           VLSI Testing and Analysis           Design for Testability (DFT), DFT Guideline, Testability, Fa generation, Sequential circuit test, Built In Self Test, JTAG & Apply knowledge of testability in design and Build In Self Test           Learning Resources           ""Digital Systems Design using VHDL", 2 <sup>nd</sup> Edition, Thompson	& NOR, Circuit on and Parasiti and Drain punct SIC.          6 Hrs.         oult models, Pate         Boundary scar         (BIST) circuit.
methods of circuit eler Extraction, Electrical I extraction, Design Issue through, Timing analysi Mapping of Course Outcomes for Unit V Unit VI Types of fault, Need of sensitizing, Test pattern TAP Controller. Mapping of Course Outcomes for Unit VI Text Books: 1. Charles H. Roth 2. Wyane Wolf, "N	ments, Layout of cell, Library cell designing for NAND a Rule Check, Layout Vs. Schematic, Post-layout Simulatic es like Antenna effect, Electro migration effect, Cross talk a s. CO5: Analyze various issues and constraints in design of an AS VLSI Testing and Analysis Design for Testability (DFT), DFT Guideline, Testability, Fa generation, Sequential circuit test, Built In Self Test, JTAG & Apply knowledge of testability in design and Build In Self Test Learning Resources , "Digital Systems Design using VHDL", 2 <sup>nd</sup> Edition, Thompson Modern VLSI Design (IP-Based Design)", 4 <sup>th</sup> Edition, Prentice	& NOR, Circuit on and Parasiti and Drain punct SIC.          6 Hrs.         ult models, Pate         Boundary scar         (BIST) circuit.         on Learning         Hall.
methods of circuit eler Extraction, Electrical I extraction, Design Issue through, Timing analysi Mapping of Course Outcomes for Unit V Unit VI Types of fault, Need of sensitizing, Test pattern TAP Controller. Mapping of Course Outcomes for Unit VI Text Books: 1. Charles H. Roth 2. Wyane Wolf, "N	ments, Layout of cell, Library cell designing for NAND a Rule Check, Layout Vs. Schematic, Post-layout Simulatic es like Antenna effect, Electro migration effect, Cross talk a s. CO5: Analyze various issues and constraints in design of an AS VLSI Testing and Analysis Design for Testability (DFT), DFT Guideline, Testability, Fa generation, Sequential circuit test, Built In Self Test, JTAG & Apply knowledge of testability in design and Build In Self Test Learning Resources , "Digital Systems Design using VHDL", 2 <sup>nd</sup> Edition, Thompson Modern VLSI Design (IP-Based Design)", 4 <sup>th</sup> Edition, Prentice dvanced FPGA Design Architecture, Implementation and Opt	& NOR, Circu on and Parasiti and Drain punc SIC.          6 Hrs.         ult models, Pat         z Boundary scar         (BIST) circuit.         on Learning Hall.

4. E. Weste, David Money Harris, "CMOS VLSI Design: A Circuit & System Perspective", 4<sup>th</sup> Edition, Pearson Publication.

## **Reference Books:**

- 1. R. Jacob Baker, "CMOS Circuit Design, Layout, and Simulation", 3rd Edition, Wiley-IEEE Press.
- 2. John F. Wakerly, "Digital Design Principles and Practices", 3rd Edition, Prentice Hall.
- 3. M. Morris Mano, "Digital Design", 3rd Edition, Pearson.
- 4. Cem Unsalan, Bora Tar, "Digital System Design with FPGA: Implementation Using Verilog and VHDL", McGraw-Hill.

- NPTEL Course on "VLSI Technology", By Dr. Nandita Dasgupta, IIT Madras Link: <u>https://nptel.ac.in/courses/117106093</u>
- NPTEL Course on "VLSI Circuits", By Prof. S.Srinivasan, IIT Madras Link: <u>https://nptel.ac.in/courses/117106092</u>

	Savitribai Phu	le Pune Univ	ersity				
Fou	irth Year of <mark>E &amp; Tc</mark>	Engineering	(2019 Course	<b>e</b> )			
	404183: Cl	oud Computi	ng				
Teaching Scheme:   Credit   Examination Scheme:							
Theory: 03 Hrs. / W	veek 03	In-Sen	n (Theory):	30 Marks			
		End Se	em (Theory):	70 Marks			
Prerequisite Courses, i	f any:						
1. Database Manage	ement						
Companion Course, if a	any:						
1. Lab Practice – 1							
Course Objectives:							
Course Outcomes: On a CO1: Understand the b CO2: Describe the unde CO3: Classify the types CO4: Examine the Clou	asic concepts of Cloud C erlying principles of diffe of Virtualization. ad Architecture and unde	Computing. erent Cloud Serv	ice Models.	Security.			
CO5: Develop application CO6: Evaluate distribute		ternet of Things.					
		e Contents					
Unit I	Fundamenta	ls of Cloud C	omputing	6 Hrs.			
Introduction to Cloud C Cloud Types: NIST, C Exploring the Cloud C computing. Mapping of Course Outcomes for Unit I	cloud cube, Cloud serv	ice models, Clo antages, Disadv	oud Computing antages and A	deployment mode Applications of clo			
Unit II	Cloud	Service Mod	els	6 Hrs.			
Introduction and benefit service models, Softwar (IaaS), Network as a se Comparison of cloud ser	e as a service(SaaS), Pl ervice (NaaS), Identity a	atform as a serv	ice (PaaS), Infra	astructure as a servi			

Mapping of Cour	ng of Course CO2: Describe the underlying principles of different Cloud Service Models.			
Outcomes for Unit II				
Unit III	Virtualization	6 Hrs.		

Introduction to Virtualization, Difference between Cloud Computing and Virtualization **Types of Virtualization:** Hardware, Software, Operating system, Server, Storage, Methods of implementing storage Virtualization, Network Virtualization Types, Advantages, Disadvantages, Virtualization Architecture and Software, Virtual Clustering, Applications of Virtualization.

Mapping of Course	CO3: Classify the types of Virtualization.		
Outcomes for Unit III		1	
Unit IV	Service Oriented Architecture and Cloud	<b>7 Hrs</b> .	
	Security		

**Cloud Computing Architecture (COA):** Design principles, Cloud computing life cycle (CCLC), Cloud computing reference architecture, Service Oriented Architecture (SOA) characteristics and fundamental components.

**Cloud Security:** Cloud CIA security model (Confidentiality, Integrity and Availability), Cloud computing security architecture, Service provider security issues, Cloud Security Issues and challenges, Security issues in virtualization, Host Security, Data Security, Firewalls.

Mapping of Course Outcomes for Unit IV	CO4: Examine the Cloud Architecture and understand the imp Cloud Security.	portance of
Unit V	<b>Cloud Environment and Application</b>	7 Hrs.
	Development	
Cloud Platforms: Go	ogle App Engine, Compute Services, Storage Services,	Communication
Services, Amazon Web	Services Architecture and core concepts, Application Lifecy	cle, Cost Model,
Microsoft Azure Cloud	services Azure core concepts, Windows Azure Platform Applia	ance.
Mapping of Course	CO5: Develop applications on Cloud Platforms.	
<b>Outcomes for Unit V</b>		
Unit VI	Distributed Computing and Internet of Things	6 Hrs.
Distributed Computing:	Need, Distributed computing vs. Cloud computing, Enabling	Fechnologies for
	nnovative Applications of the Internet of Things, Online Socia	-
Professional Networking		
Mapping of Course	CO6: Evaluate Distributed Computing and the Internet of Thi	ngs.
Outcomes for Unit VI		-

# **Learning Resources**

## **Text Books:**

- 1. Kailash Jayaswal, Jagannath Kallakurchi, Donald J. Houde, Dr. Deven Shah, "Cloud Computing: Black Book", Dreamtech Press.
- 2. Surbhi Rastogi, "Cloud Computing Simplified", 2021 Edition, BPB Publications.
- 3. Kai Hwang, Geoffrey.C.Fox., Jack J. Dongarra, "Distributed and Cloud Computing: From Parallel Processing to Internet of Things", MK Publications, Elsevier

## **Reference Books:**

- 1. Kamal Kant Hiran, et al. "Cloud Computing: Master the concepts, Architecture and Applications with Real-world examples and Case Studies", 1<sup>st</sup> Edition, BPB Publication.
- 2. Judith Hurwitz, "Cloud Computing for dummies", 2<sup>nd</sup> Edition, Wiley India.
- 3. A. Srinavasan, J. Suresh, "Cloud Computing: A Practical Approach for Learning and Implementation", Pearson.
- 4. Anthony T. Velte Toby J. Velte, Robert Elsenpeter, "Cloud Computing: A Practical Approach", McGraw-Hill.
- 5. Barrie Sosinsky, "Cloud Computing Bible", Wiley Publishing Inc.
- 6. Rajkumar Buyya, Christian Vecchiola, S. Thamarai Selvi, "Mastering Cloud Computing", McGraw Hill Education

# **MOOC / NPTEL Courses:**

1. NPTEL Course on "Cloud Computing", By Prof. Soumya Kanti Ghosh, IIT Kharagpur.

Link: https://nptel.ac.in/courses/106105093

2. NPTEL Course on "Google Cloud Computing Foundation Course", By Prof. Soumya Kanti

Ghosh, IIT Kharagpur.

Link: https://nptel.ac.in/courses/106105223

# **Recommended Websites:**

- 1. <u>www.whatiscloud.com</u>
- 2. <u>www.cloudcomputingpatterns.org</u>
- 3. <u>www.w3schools.com</u>

	Savitribai Phule	Puno University			
<b>F</b>		-			
		ngineering (2019 Course)			
40	4184 (A): Speech Pro	ocessing (Elective - III)			
<b>Teaching Scheme:</b>	CreditExamination Scheme:				
Theory: 03 Hrs. / Week03In-Sem (Theory): 30 Marks					
		End Sem (Theory): 70	Marks		
Prerequisite Courses, if a	ny:				
1. Signals & Systems					
2. Digital Signal Proces	ssing				
Companion Course, if an	y:				
1. Lab Practice – 2					
Course Objectives:					
sounds. 2. To understand the s 3. To extract the infor 4. To understand varie 5. To provide a platfor Course Outcomes: On cor CO1: Understand basics of CO2: Classify speech sour CO3: Analyse speech sign (pitch). CO4: Evaluate speech sign	short-term analysis of spe mation of the speech sign ous audio and speech cod rm for developing applica mpletion of the course, le of Human speech product nds based on acoustic and hal to extract the character nal for extracting LPC ar ms for processing of spee	tion mechanism. d articulatory phonetics. ristic of vocal tract (formants) and nd MFCC Parameters of speech sig ech and audio signals considering th	lomain. elling algorithms. dio processing. vocal cords		
	Course (	Contents			
Unit I	Fundamentals	of Speech Processing	6 Hrs.		
phonetics, articulators, man Classification of speech vowel triangle. Parameters of speech: Fu pitch period, AMDF metho	nner of articulation, place sounds: vowels, semivo ndamental frequency or p od for finding pitch period	l for speech production, Nature of articulation, linear time varying wels, nasal diphthongs, stops, aff pitch frequency-Autocorrelation m d. Formants. Human speech production mechani	g model. Fricates, fricative, wethod for finding		

Unit II	Time and Frequency domain methods for	<b>7 Hrs.</b>
	Speech and Audio signal analysis.	
Time dependent speech	processing. Short-time energy, short time average magnit	tude, Short time
average zero crossing r	ate. Speech Vs. silence discrimination using energy and zer	ro crossing rate.
Short-time autocorrelati	on function, short-time average magnitude difference functio	n. Audio feature
extraction, Spectral ce	ntroid, spectral spread, spectral entropy, spectral flux, s	spectral roll-off.
Spectrogram: narrow ba	nd and wide band spectrogram.	
Mapping of Course	CO2: Classify speech sounds based on acoustic and articulatory	y phonetics.
Outcomes for Unit II		
Unit III	Lincor prediction and constrol analysis	6 Hrs.
Unit III	Linear prediction and cepstral analysis	O HIS.
Computation of Mel Fre	tion of LPC equations, Cepstral analysis of speech, ceps quency Cepstral Coefficients (MFCC). CO3: Analyse speech signal to extract the characteristic of voca	
Outcomes for Unit III	(formants) and vocal cords (pitch).	in tract
Unit IV	Speech and Audio Coding	6 Hrs.
Time domain wavefor	<b>n coding:</b> Linear PCM, Companded PCM, DPCM.	
		ATC) Harmonia
-	bank analysis, sub-band coders, Adaptive transform coders ( re coders (LPC), Non-LP source voice coders: phase vocoder	
(Cepstral) vocoders.	e couers (LFC), Non-LF source voice couers. phase vocouer	s, momonorphic
Mapping of Course	CO4: Evaluate speech signal for extracting LPC and MFC	C Parameters of
Outcomes for Unit IV	speech signal.	
Unit V	Applications of Speech Processing	6 Hrs.
Automatia Spaach Daa		noo rocognition
-	ognition, Feature Extraction for ASR, Deterministic seque	•
-	ognition, ASR systems, Speaker identification and verification	
	xt-to-Speech Synthesis: Concatenative and waveform syn lness in speech synthesis, role of prosody.	intesis methous,
Mapping of Course	CO5: Implement algorithms for processing of speech and audio	signals
Outcomes for Unit V	considering the properties of acoustic signals.	, signais
Unit VI	Speech Processing using Machine Learning	6 Hrs.
		U III St
	techniques	
	n processing applications Automatic Speech Recognition	-
	ech Synthesis using Support Vector Machine (SVM), Conv	
	ecurrent Neural Networks (RNN). Performance parameters	1
•	es, True Negatives, False Positives, False Negatives, Sensiti	vity, Specificity,
Anon Under Comment (ATT)	7) Dessiver Operating Characteristic (DOC)	

Area Under Curve (AUC), Receiver Operating Characteristic (ROC).

Mapping	of	Course	CO6: Design speech recognition application for speech signal analysis.
Outcomes	for U	Init VI	

# **Learning Resources**

# **Text Books:**

- 1. L.R.Rabiner and S.W.Schafer, "Digital Processing of Speech Signals" 1<sup>st</sup>Edition Pearson Education.
- Daniel Jurafsky and James H Martin, "Speech and Language Processing An Introduction to Natural Language Processing, Computational Linguistics, and Speech Recognition", 2<sup>nd</sup> Edition, Pearson Education.

## **Reference Books:**

- 1. Thomas F. Quateri, "Discrete-Time Speech Signal Processing: Principles and Practice", Prentice Hall- Signal Processing Series.
- 2. Shaila Apte, "Speech and Audio Processing", 1st Edition, Wiley India Publication.
- 3. Ben Gold and Nelson Morgan, "Speech and Audio Signal Processing: Processing and Perception of Speech and Music", 2<sup>nd</sup> Edition, Wiley India Publication.
- Uday Kamath, John Liu, James Whitaker, "Deep Learning for NLP and Speech Recognition", 1<sup>st</sup> Edition, Springer Publication

# MOOC / NPTEL Courses:

 NPTEL Course on "Digital Speech Processing", By Prof. Shyamal Kumar Das Mandal, IIT Kharagpur.

Link: https://nptel.ac.in/courses/117105145

	Savitribai Phul	e Pune University			
Four	Fourth Year of E & Tc Engineering (2019 Course)				
	404184 (B): PLC SCADA and Automation (Elective - III)				
Teaching Scheme	e: Credit	Examination	Scheme:		
Theory: 03 Hrs. / W	Veek 03	In-Sem (Theory):	30Marks		
		End Sem (Theory):	70 Marks		
Prerequisite Courses,	, if any:				
1. Control Systems					
2. Sensor's in Automat	tion				
3. Power Devices and	Circuits				
Companion Course, i	f any:				
1. Lab Practice – 2					
Course Objectives:					
-	Recognize Industrial co	ontrol problems.			
2. Concept of PLC's	and Its Importance in In	ndustrial Automation.			
3. Development of La	dder Programming in P	LC and PLC Interface in real tim	e applications.		
4. Overview of technol	ology of advanced auton	nation Systems such as SCADA,	DCS Systems.		
5. Learning of CNC	fundamentals and Impor	tant Protocols in Industrial Auto	mation		
Course Outcomes: On co	ompletion of the course,	learner will be able to			
CO1: Understand and Ro	ecognize Industrial Cont	rol Problems.			
CO2: Analyze & explain	different hardware func	tions of PLC.			
CO3: Develop Ladder Pr	ogramming in PLC and	PLC Interface in real time applic	ations.		
CO4: Explore and interp	ret functionality of SCA	DA.			
CO5: Identify and interp	ret the functionality of E	DCS.			
CO6: Define and explain	CNC machines and App	plications of Industrial Protocols.			
	Course	Contents			
Unit I	Elements of Pro	ocess Control Automation	<b>7 Hrs.</b>		
Process control principles,	Control System Evalua	tion, Analog control, Digital con	trol, Architecture of		
Industrial Automation Sy	stems (Automation Pyr	amid), Advantages and limitati	ons of Automation,		
Concept and Need of tran	smitters, Standardizatio	n of signals, Current, Voltage a	nd Pneumatic signal		
standards, 2-Wire & 3-W	vire transmitters, Conce	pt of VFD, Energy conservation	on schemes through		
VFD.			-		
Mapping of Course C	<b>CO1: Understand and</b>	Recognize Industrial Control P	roblems		
Outcomes for Unit I		-			
Unit II	Fundar	nentals of PLC	7 Hrs.		
		ns of PLC's, PC v/s PLC, Differ			
		es of Sensors- Sinking, Sourci			
function. Monitoring of			Analog Addressing,		
continuous Process Monito	-		manog Addiessing,		
	0	n different hardware functions	of PLC		
Outcomes for Unit II	. Analyze & explai	n unter ent har uwar e functions			

Unit III	Programming of Programmable Logic7 Hrs.Controllers7			
PLC programming, NO/ NC Concept, Ladder diagram: of logic gates, arithmetic instructions,				
multiplexer, Ladder diagr	am for different logical conditions or logical equations or tru	th table. Timers:		
types of timer, Character	ristics, Function of timer in PLC, Classification of a PLC	C timer, Ladder		
diagram using timer, PL	C counter, Ladder diagram using counter. PLC Programn	ning of Branded		
PLCs. Concept of P,PI,PE	D,PID w.r.t. PLC, Data File Handling- Forcing I/O.			
Mapping of Course	CO3: Develop Ladder Programming in PLC and PLC I	nterface in real		
Outcomes for Unit III	time applications.			
Unit IV	Supervisory Control and Data Acquisition Systems (SCADA)	6 Hrs.		
Concept of SCADA. Arc	hitecture of SCADA, Components of SCADA Systems, M	TU- functions of		
	TRTU, Directly interact with devices such as sensors, valves			
	-machine interface (HMI) software. Working of SCADA,			
6	omation like Oil and gas, Power etc.			
Mapping of Course				
Outcomes for Unit IV	CO4: Explore and interpret functionality of SCADA.			
Unit V	Distributed Control Systems (DCS)	6 Hrs.		
Basic Concept of DCS, I	History and Hierarchy of DCS, Basic Componenents of D	CS as Operator		
-	and I/O module, Types of DCS, Need of DCS, Function	-		
Advantages and Disadvan PLC, DCS and SCADA	ntages, Applications of DCS such as Water Treatment Plant	, Comparison of		
Mapping of Course	CO5: Identify and interpret the functionality of DCS.			
Outcomes for Unit V	COS. Identify and interpret the functionality of DCS.			
Unit VI	<b>CNC Machines and Industrial Protocols</b>	7 Hrs.		
Introduction of CNC Mac	chines, Basics and need of CNC machines, NC, CNC and D	NC (Direct NC)		
systems, Structure of NC	systems, Applications of CNC machines in manufacturing	, Advantages of		
•		us, PROFIBUS,		
MODBUS, Ethernet, TCF	P/IP, Concept of Industry 4.0.			
Mapping of Course		ns of Industrial		
Outcomes for Unit VI	Protocols.			
	Learning Resources			
Text Books:				
1. Curtis Johnson, "Pro	ocess Control Instrumentation Technology", 8th Edition, Pear	son Education		
	tra, Samarjit Sen Gupta, "Programmable Logic controller			
	dition, Penram International Publishing India Pvt. Ltd.	s and muusuial		
Automation , 2 EC	muon, i emani miernational ruonsning mula rvt. Liu.			

#### **Reference Books:**

- 1. Stuart A. Boyer, "SCADA Supervisory Control and Data Acquisition", 4<sup>th</sup> Edition, ISA Publication.
- John W. Webb, Ronold A Reis," Programmable Logic Controllers, Principles and Applications", 5<sup>th</sup> Edition, Prentice Hall of India Pvt. Ltd.
- 3. Kilian, "Modern control technology: components & systems", 2<sup>nd</sup> Edition, Delmar.
- 4. Bela G Liptak "Process Software and Digital Networks", 4<sup>th</sup> Edition, CRC Press
- 5. Pollack. Herman, W & Robinson., T. "Computer Numerical Control", Prentice Hall.
- 6. Pabla, B.S. & Adithan, M. "CNC Machines", New Age Publishers.
- 7. R.G. Jamkar, "Industrial Automation Using PLC SCADA & DCS" Global Education Limited

#### **MOOC / NPTEL Courses:**

1. NPTEL Course on "Industrial Automation and Control", by Prof. S. Mukhopadhyay, IIT Kharagpur.

Link: https://nptel.ac.in/courses/108105088

Savitribai Phule Pune University						
Fourth Yea	Fourth Year of E & TC Engineering (2019 Course)					
	404184 (C): Java Script (Elective - III)					
Teaching Scheme:     Credit     Examination Scheme:						
Theory: 03 Hrs. / Week	03	In-Sem (Theory):	30 Marks			
		End Sem (Theory):	70 Marks			
Prerequisite Courses, if any: 1. Fundamentals of Java Pro 2. Advanced Java Programm Companion Course, if any:						
1. Lab Practice – 2						
	Course Con	ntents				
Unit I	Unit IIntroduction to Java Scripts6 Hrs.					
Introduction – First Look at Ja Element, Using the <script> Eler JavaScript Core Features- Over Data Types, Composite Types, JavaScript, Regular Expressions.</td><th>nent, Event Handlers, view-Basic Definition</th><th>Linked Scripts, History ar ns, Language Characteristi</th><td>nd Use of JavaScript, ics, Variables, Basic</td></tr><tr><td>Mapping of CourseCO1: UOutcomes for Unit I</td><td colspan=4></td></tr></tbody></table></script>						

Unit II	Data Types and Variables	8 Hrs.
Representation Issues, I Objects, The typeof Ope	Types- Numbers, Hexadecimal Literals, Octal Literals, Spec Data Representation Issues, Strings, Undefined and Null; Co erator, Type Conversion, Variables. and Statements- Statement Basics, Whitespace, Termination:	omposite Types-
Returns, Blocks.	and Statements- Statement Basics, wintespace, Termination.	Semicorons and
	Operator, Arithmetic Operators, Bitwise Operators, Bitwise	Shift Operators,
	ogical Operators, void Operator, Object Operators	
_	ments- if Statements, switch, while Loops, do-while Loops	, for Loops, for
1 0	tatements, Object Loops Using for in	• • /
Mapping of Course Outcomes for Unit II	CO2: Use relevant data types for developing application i	n java script.
Outcomes for Ome II		
Unit III	Functions and Objects	6 Hrs.
Function Basics- Param	eter-Passing Basics, return Statements, Parameter Passing: In	
	bles- Mask Out, Local functions	
Functions as Objects-	Function Literals and Anonymous Functions, Static Varial	oles, Advanced
Parameter Passing, Recu	rsive Functions, Using Functions	
<b>Objects-</b> Objects in Java	aScript, Object Fundamentals	
	, Objects Are Reference Types, Passing Objects to Func	
· · · · · · · · · · · · · · · · · · ·	Array, Date, Math, Number, String, Object Types and Primiti	• 1
Mapping of Course Outcomes for Unit III	CO3: Use the function and objects as self-contained, with and out through well-defined interfaces in developm systems.	
	Systems.	
Unit IV	Regular Expressions	6 Hrs.
The Need for Regular E	xpressions, Introduction to JavaScript Regular Expressions, C	reating Patterns
Repetition Quantifiers, C	Grouping, Common Character Classes, RegExp Object, exec().	-
String Methods for Reg	gular Expressions: search(), split(),replace(),replace() with Su	b expressions
Advanced Regular E	xpressions: Multiline Matching, Non-capturing Parenthes	ses, Lookahead
	ations of Regular Expressions.	
Mapping of Course Outcomes for Unit IV	CO4: Apply the regular expression for Text matching and	d manipulation.
Unit V	Eundomental Client Side JaveSerint and Event	6 Hrs
Unit v	Fundamental Client-Side JavaScript and Event Handling	01115
JavaScript Object Mod	lels: Object Model Overview, The Initial JavaScript Object M	odel, The Object
Models		
	ent Object Model: DOM Flavors, Document Trees, Acce	e
6	ng and Appending Nodes, Deleting and Replacing Nodes,	
	OM and CSS, The DOM Versus DHTML Object Models. Ov	
0	ne Basic Event Model, Netscape 4 Event Model, Internet Ex	plorer 4+ Event
	odel, Event Model Issues.	
Mapping of Course Outcomes for Unit V	CO5: Explore use of the various aspects of JavaScript obj are fundamental to the proper use of the language.	

Unit VI	Using Java scripts	8 Hrs.			
Controlling Windows and Frames: Introduction to Window, Dialogs, Opening and Closing Generic					
Windows, Window Feat	ures, Writing to Windows, Controlling Windows, Window Ex	vents, Frames: A			
Special Case of Window	s, Frames: A Special Case of Windows.				
Form Handling: Form	Basics, Form Fields, Select Menus Option Groups, Other	Form Elements:			
Label, Fieldset, and Lege	end, Form Validation, Form Usability and JavaScript, Dynami	c Forms.			
Mapping of Course	CO6: Develop the application using windows controlling	and for			
Outcomes for Unit VI	handling.				
	Learning Resources				
Text Books:					
1. Thomas Powell a	nd Fritz Schneider, "JavaScript 2.0: The Complete Reference"	'. 2 <sup>nd</sup> Edition.			
McGraw Hill		, ,			
2. Kogent Learning S	Solutions, "HTML, JavaScript, PHP, Java, JSP, XML and AJA	X" Black			
Book, Dreamtech					
<b>Reference Books:</b>					
1 Ion Duckett "Iav	vaScript & J Query: Interactive Front-End Web Development	" John Wiley &			
Sons.	abeript a 5 Query. Interactive From End (100 Development	, sonn veney æ			
	"JavaScript: The Definitive Guide", 7th Edition, O'Reilly Med	ia.			
	"Javascripts in Easy Steps" Dreamtech Press				
MOOC / NPTEL Courses:					
1. NPTEL Course on "Internet Technology", by Prof. Indranil Sengupta, IIT Kharagpur					
Link: <u>https://npte</u>	Link: <u>https://nptel.ac.in/courses/106105084</u>				

2. Udemy course on **"JavaScript: Understanding the Weird Parts"** Link: <u>https://www.udemy.com/course/understand-javascript/</u>

	Savitribai Phule Pune University					
Four	Fourth Year of E & Tc Engineering (2019 Course)					
404184	(D): Embedded Syste	m & RTOS (Elective -	III)			
Teaching Scheme	Teaching Scheme:CreditExamination Scheme:					
Theory: 03 Hrs. / We	ek 03	In-Sem (Theory):	30 Marks			
		End Sem (Theory):	70 Marks			
Prerequisite Courses, if						
1. ARM7 / ARM9 / A Companion Course, if an						
1. Lab Practice - 2	ıy.					
Course Objectives:						
, v	Embedded system design is	ssues.				
	time operating system con					
	Embedded Linux environm	1				
4. To understand eml	bedded software developme	nt and testing tools.				
	ompletion of the course, lear					
	-	design real time application	s to match recent			
trends in technolog	•					
CO2: Apply Real time sy						
	erating system and its service					
11 0	inux Development Environ	0				
• 1	rating system and device dr	ivers. ues for testing of real time E	mbaddad system			
COU. Analyze the hardwa	Course Co		indedded system.			
	Course Co					
Unit I	Introduction to	Embedded Systems	8 Hrs.			
Introduction to Embedde	d Systems, Architecture,	Classification and Characte	eristics of Embedded			
	•	ation of various parameters				
Embedded processor tech	nology, IC technology, Des	ign technology. Software de	evelopment life cycle.			
	* *	ping models and Compariso				
Mapping         of         Course         CO1: Apply design metrics of Embedded systems to design real time						
Outcomes for Unit I applications to match recent trends in technology.						
Unit II	Unit IIConcepts of Real Time Operating System6 Hrs.					
Foreground/ Background systems, Critical section of code, Resource, Shared resource, multitasking,						
Task, Context switch, Kernel, Scheduler, Non-Preemptive Kernel, Preemptive Kernel, Reentrancy,						
Round robin scheduling,	Task Priorities, Static & Dy	namic Priority, Priority Inve	ersion, Assigning task			
priorities, Mutual Exclusion, Deadlock, Clock Tick, Memory requirements, Semaphore as signaling &						
Synchronizing, External	nterrupt, Advantages & dis	advantages of real time kern	els.			
MappingofCourseCOutcomes for Unit II	CO2: Apply Real time system	as concepts.				

Unit III	μCOS ΙΙ	6 Hrs.
Features of µCOS II Ker	rnel structure. µCOS II RTOS services: Task management, Ti	me management,
Intertask Communicatio	n and Synchronization.	
Mapping of Course Outcomes for Unit III	CO3: Evaluate µCOS operating system and its services.	
<b>Unit IV</b>	<b>Embedded Linux Development Environment</b>	<b>6 Hrs.</b>
Need of Linux, Embedd	ed Linux Today, Open Source and the GPL, BIOS Versus Boo	ot loader, Storage
Considerations, Embedd	ed Linux Distributions.	
-	t Environment, Cross-Development Environment, Host Syster	· ·
Hosting Target Boards. Utilities.	Development Tools, GNU Debugger, Tracing and Profilin	ig Tools, Binary
Mapping of Course Outcomes for Unit IV	CO4: Apply Embedded Linux Development Environment tools.	and testing
T 1	Linux Vous al Starvetures	6 IIma
Unit V	Linux Kernel Structure           nd, Linux Kernel Construction, Kernel Build System, Kernel	6 Hrs.
	, Module Utilities, Driver Methods. Linux File System & Cond CO5: Analyze Linux operating system and device drivers.	1
Unit VI	Embedded Software Development and Testing	8 Hrs.
	evelopment process and tool chain, Host and Target M.	
Embedded Software into Introduction to Develop Arduino, Beaglebone, R Mapping of Course	o the Target System, Testing on Host Machine, Simulators. oment Platform Trends (only introduce IDE, board Details a asberry PI, Intel Galileo Gen 2 (Simple Programs to discussed CO6: Analyze the hardware – software co design issue real time Embedded system.	and Application)
	L corning Descources	
Text Books:	Learning Resources	
1. Jean J. Labrosse,	"MicroC OS II, The Real-Time Kernel", 2 <sup>nd</sup> Edition, CMP Beinan, "Embedded Linux Primer -A Practical, Real-World Appr Hall.	
<b>Reference Books:</b>		
McGraw Hill.	nbedded Systems – Architecture, Programming and Desig I Tony Givargis, "Embedded System Design – A Unified Ha	

Introduction " 3<sup>rd</sup> Edition, Wiley.
3. David E. Simon, "An Embedded Software Prime", Pearson Education.

- NPTEL Course on "Embedded System Design with ARM", by Prof. Indranil Sengupta, and Prof. Kamalika Datta, IIT Kharagpur Link of the Course: <u>https://nptel.ac.in/courses/106105193</u>
- NPTEL Course on "Real-Time Systems", by Prof. Rajib Mall, Prof. Durga Prasad Mohapatra, IIT Kharagpur Link of the Course: <u>https://nptel.ac.in/courses/106105229</u>

	Savitribai	i Phule I	Pune University	
Four			gineering (2019 Cours	e)
			d IoT (Elective - III)	,
Teaching Scheme		redit	Examinatio	n Scheme:
Theory: 03 Hrs. / We	ek	03	In-Sem (Theory):	30 Marks
v			End-Sem (Theory):	
Prerequisite Courses, if a 1. Basics of sensors an 2. Basic networking co 3. Knowledge of Micro Companion Course, if an 1. Lab Practice- 2	d hardware comp oncepts ocontroller and e		systems	
Course Objectives:				
<ol> <li>To Exposing stude: Gateway Network</li> <li>To develop design</li> <li>To provide IoT Sol</li> <li>Course Outcomes: On co</li> <li>CO1: Comprehend and a</li> <li>CO2: Interpret IoT Archit</li> <li>CO3: Comprehend the op</li> <li>CO4: Describe various Io</li> </ol>	nts to the usage of with Communica skills in industria utions with sense mpletion of the of nalyze concepts itecture Design A peration of IoT p T boards, interfa- plogies, Catalysts ution for domain	of Protocol ation protocal for-based a course, lea of sensors aspects. rotocols. cing, and prec	pplication through embedde rner will be able to , actuators, IoT and IoE. programming for IoT. ursors of IIoT using suitable pplications of IoT.	d system platform.
Unit I	Sens	sors, Act	uators, IoT & IoE	6 Hrs.
Principals and componen Physical Design of an Io' What is the IoE? Differen Transitioning to the IoE, B	ts, Wireless Ser Γ, Logical desig nee between IoT pringing It All To	nsor Netw n of IoT, and IoE, ogether.	Example and Working, Networks, Definition, and chara Communication Models, C Pillars of the IoE, Connect vze concepts of sensors, actua	acteristics of an IoT Communication API's ting the Unconnected
Unit II	IoT Ar	chitectu	re Design Aspects	6 Hrs.
An IoT architecture outl Devices and gateways, Lo Everything as a Service (X	ine, standards c cal and wide area (aaS), M2M and	onsiderati a networki IoT Analy	e, Main design principles ar ons. M2M and IoT Techn ng, Data management, Busi tics, Knowledge Manageme ecture Design Aspects.	ology Fundamentals iness processes in IoT

Unit III	IoT Protocols	6 Hrs.
PHY/MAC Layer (3GPI	PMTC, IEEE 802.11, IEEE 802.15), Wireless HART, Z Wave	, Bluetooth Low
Energy, Zigbee Smart	Energy, DASH7 - Network Layer-IPv4, IPv6, 6LoWPAN	, 6TiSCH, ND,
DHCP, ICMP, RPL, C	ORPL, CARP, Transport Layer (TCP, MPTCP, UDP, DCC	P, SCTP)-(TLS,
DTLS) – Session Layer	HTTP, CoAP, XMPP, AMQP, MQTT	
Mapping of Course	CO3: Comprehend the operation of IoT protocols.	
Outcomes for Unit III		
Unit IV	Interfacing Boards and Programming	6 Hrs.
Introduction to IoT B	oards, Interfacing with IoT Boards, IoT deployment fo	r Raspberry Pi
	tform – Reading from Sensors, Communication: Connecting	
	communication through Bluetooth, wifi and USB - Cor	
Simulator.		5
Mapping of Course Outcomes for Unit IV	CO4: Describe various IoT boards, interfacing, and programm	ing for IoT.
Unit V	Industrial IoT	<b>6 Hrs.</b>
Introduction, Key IIO	f technologies, Catalysts, and precursors of IIoT, Innovation	n and the IIoT,
	trol domain, operational domain and application domain, Thr vice network, legacy industrial protocols, Bluetooth, Zigbee IF m.	
	CO5: Illustrate the technologies, Catalysts, and precursor suitable use cases.	s of HoT using
Unit VI	Applications of IoT	<b>6 Hrs.</b>
Noise Urban maps, Sma Calculation, Health: Fall Ultraviolet Radiation	est Fire Detection, Air Pollution, Smart Cities: Parking, Struct rt Metering: Smart Grid, Tank level, Photovoltaic Installations Detection, Medical Fridges, Sportsmen Care, Patients Surveil	, Silos Stock
Mapping of Course		
Outcomes for Unit VI	<b>CO6</b> : <b>Provide</b> suitable solution for domain specific applications of	IoT.
	<b>Learning Resources</b>	IoT.
Outcomes for Unit VI Text Books:		IoT.
Text Books: 1. Ovidiu Vermesa Deployment", Ri 2. Olivier Hersent,		vation to market

#### **Reference Books:**

- 1. Dr. Ovidiu Vermesan, Dr. Peter Friess, "Internet of Things: Converging Technologies for Smart Environments and Integrated Ecosystems", River Publishers Series in Communication
- 2. "Internet of Things: Case Studies", Libelium Inc, White papers, Spain http://www.libelium.com/resources/case-studies

- 1. NPTEL Course on "Introduction to IoT", by Prof. Sudip Misra, IIT Kharagpur Link of the Course: <u>https://nptel.ac.in/courses/106105166</u>
- NPTEL Course on "Introduction to Industry 4.0 and Industrial Internet of Things", by Prof. Sudip Misra, IIT Kharagpur Link of the Course: <u>https://nptel.ac.in/courses/106105195</u>

	Savitribai Phule P	uno University	
		-	
Fourt	1 Year of E & Tc Eng	gineering (2019 Course)	
4	104185 (A): Data Min	ing (Elective - IV)	
<b>Teaching Scheme:</b>	Credit	Examination Scheme:	
Theory: 03 Hrs. / Wee	k 03	In-Sem (Theory): 30	Marks
		End Sem (Theory): 70	Marks
Prerequisite Courses, if an	y:		
1. Database Manageme	ent Systems		
2. Signals and Systems			
Companion Course, if any	:		
Course Objectives:			
	1	ng and major issues in Data Min	ing.
		ecture and its Implementation.	. alwataning and
3. To characterize the association rule mini	-	n be discovered by classification	i, clustering, and
	e	g algorithms, methods, tools.	
		and clustering techniques using	tools
	trends in Data Mining.	and endstering teeninques using	10015.
Course Outcomes: On com		mer will be able to -	
	-	rformance issues in data mining	
-	0 1	torical data collected in data war	ehouse
		ysis methods and advanced Patte	
techniques		-	C
CO4: Evaluate various data	a mining algorithms for de	eveloping effective data mining r	nodels
CO5: Analyze different clu	stering and outlier detection	on methods	
CO6: Design data mining n	nodels in different mining	application areas	
	Course Co	ontents	
Unit I	Introduction	n to Data Mining	<b>7 Hrs.</b>
Introduction: Definition,	Mining Functionalities,	Kinds of Patterns, Technologie	es used for data
mining- Machine Learning,	Database Systems and D	Data Warehouses, Major Issues	in Data Mining -
Mining Methodology, User	Interaction, Efficiency an	nd Scalability, Diversity of Data	base Types, Data
Mining and Society			
	• -	ata to be mined, choose and majo	r issues in Data
Outcomes for Unit I	Mining.		
		and Data Ware housing	6 Hrs.
		ing Values, Noisy Data- Bin	
	numan inspection, Incons	istent Data, Data Integration and	1 ransformation.
Data Reduction.	maanta Data Warahawaa J	Modeling Date Warshouse Desi	an and Liceas
	-	Modeling, Data Warehouse Desi processing, Model and design the	
Outcomes for Unit II	2. I CHOIM UNICICII Udla	processing, mouth and design the	

Unit III	<b>Frequent Pattern Analysis and Advanced</b>	7 Hrs
	Pattern Mining	

Frequent Pattern Analysis: Basic Concepts, Frequent Itemset Mining Methods Pattern Evaluation Method.

Advanced Pattern Mining: Basic Concepts, Decision Tree Induction, Bayes Classification Methods, Rule-Based Classification, Model Evaluation and Selection, Techniques to Improve Classification Accuracy,

Mapping	of	Course	CO3: Understand the Frequent pattern analysis and advanced	Pattern mining.
Outcomes	for U	J <b>nit III</b>		

Unit IV	Data mining algorithms	<b>7 Hrs.</b>

Classification - Basic issues regarding classification and predication - General Approach to solving a classification problem- Decision Tree Classification, Attribute Selection Measures, Tree Pruning-Bayesian Classification – Rule Based Classification – Support Vector Machines.

Mapping of Course Outcomes for Unit IV	CO4: Choose and employ suitable data mining algorithms to applications	build analytical

		I
Unit V	<b>Cluster Analysis and Outlier Detection</b>	<b>7 Hrs.</b>

Basics and Importance of Cluster Analysis- Different Types of Clusters Partitioning Methods, Clustering high dimensional data- Clustering with constraints, Outlier analysis-outlier detection methods.

**Outlier Detection:** Need, Detection Methods, Approaches – Statistical, Proximity-Based, Clustering-Based, Classification-Based, Outlier Detection in High-Dimensional Data

Mapping of Course Outcomes for Unit V	CO5: Implement clustering and outlier detection methods.	
Unit VI	Advanced Concepts	6 Hrs.

**Basic concepts in Mining data streams:** Mining Time series Data Mining sequence patterns in Transactional database Mining Object, Spatial Multimedia, Text - extracting attributes (keywords), structural approaches (parsing, soft parsing).

**Web Mining:** Introduction to Web Mining, Web content mining, Web usage mining, Web Structure mining, Web log structure and issues regarding web logs, Spatial Data Mining.

MappingofCourseCO6: Orient towards the advanced approaches of Data mining.Outcomes for Unit VI

Learning Resources

# **Text Books:**

1. Jiawei Han & Micheline Kamber, "Data Mining: Concepts and Techniques", 3<sup>rd</sup> Edition Elsevier.

2. Margaret H Dunham, "Data Mining Introductory and Advanced topics", 1<sup>st</sup> Edition Pearson **Reference Books:** 

 Ian H. Witten and Eibe Frank, "Data Mining: Practical Machine Learning Tools and Techniques" 2<sup>nd</sup> Edition, Morgan Kaufmann.

- NPTEL Course "Data Mining" by Prof. Pabitra Mitra IIT Kharagpur Link of the Course: <u>https://nptel.ac.in/courses/106105174</u>
- NPTEL Course "Business analytics and data mining Modeling using R" by Dr. Gaurav Dixit IIT Roorkee Link of the Course: <u>https://nptel.ac.in/courses/110107092</u>

Form	Savitribai Phule I	Pune University	
rour	th Year of <mark>E &amp; Tc En</mark>	gineering (2019 Course	)
404185	5 (B): Electronics Prod	duct Design (Elective - I	V)
Teaching Scheme:	Credit	Examination Schem	e:
Theory: 03 Hrs. / We	ek 03	In-Sem (Theory):	30 Marks
		End Sem (Theory):	70 Marks
Prerequisite Courses, if a	any:		
Companion Course, if ar	ny:		
Course Objectives:			
<ol> <li>To understand the Course Outcomes: On concord CO1: Understand and ext CO2: Associate with variation of the constraint of the</li></ol>	ed circuit boards and differ	of documentation. rner will be able to n of electronics product. nd testing. ects and the Importance of	product test & test
	al design considerations ar	nd importance of documentation	ion.
	al design considerations ar Course C	nd importance of documentation ontents	ion.
Unit I	Course C		
Overview Of System En Requirements, Design De And Integration, Mainte Engineering, Architecturi	Course C Introduction to El agineering, System Persp evelopment, Rapid Prototy nance And Life Cycle ( ng, Design Concerns And I	ontents	n 6 Hrs. oncept Development, lidation, Verification nd Judgment, Good rust.
Overview Of System En Requirements, Design De And Integration, Mainte Engineering, Architecturi Mapping of Course C	Course C Introduction to El agineering, System Persp evelopment, Rapid Prototy nance And Life Cycle o ng, Design Concerns And I CO1: Understand and explai	ontents ectronic Product Design ectives, Documentation, Co ping And Field Testing, Va Costs, Failure, Iteration A Heuristics, Teamwork And T	n 6 Hrs. oncept Development, lidation, Verification nd Judgment, Good rust.

Unit III	Software Design and Testing Methods	6 Hrs.
• 1	ditional Software Life Cycle, Models, Metrics and Software L Preventions, Software Bugs and Testing, Good Programmin	
	eal Time Software, Case Studies and Design Examples.	g Flactice, User
	CO3: Inferring different software designing aspects and the	a Importance of
Outcomes for Unit III	product test & test specifications.	e importance of
	product lest et lest specifications.	
<b>Unit IV</b>	PCB Design	<b>6 Hrs</b> .
Circuit Boards, Compo	onent Placement, Routing Signal Traces, Grounds, Return	ns and Shields,
Connectors and Cables	s, Design for Manufacture, Testing and Maintenance, Po	wer Conversion
	bution, Line Conditioning, Electromagnetic Interference,	
Mechanisms For Coolin	ng, Heat Sink Selection, Heat Pipes and Thermal Pillows, F	Fans and Forced
Cooling, Liquid Cooling	g, Evaporation and Refrigeration, Trade-Offs In Design.	
Mapping of Course	CO4: Summarizing printed circuit boards and different parameters	eters.
<b>Outcomes for Unit IV</b>		
Unit V	Product Debugging and Testing	6 Hrs.
Steps Of Debugging,	Techniques for Troubleshooting, Characterization, El	lectromechanical
1 00 0	Techniques for Troubleshooting, Characterization, El Components, Active Components, Active Devices, Operati	
Components, Passive (	Components, Active Components, Active Devices, Operati	ional Amplifier,
Components, Passive ( Analog-Digital Convers	Components, Active Components, Active Devices, Operatision, Digital Components, Inspection and Test Of Compone	ional Amplifier, ents, Simulation,
Components, Passive ( Analog-Digital Convers Prototyping and Testir	Components, Active Components, Active Devices, Operatision, Digital Components, Inspection and Test Of Components, Integration, Validation and Verification. Procurement,	ional Amplifier, ents, Simulation,
Components, Passive ( Analog-Digital Convers Prototyping and Testir Maintenance and Repair	Components, Active Components, Active Devices, Operation, Digital Components, Inspection and Test Of Components, Integration, Validation and Verification. Procurement,	ional Amplifier, ents, Simulation,
Components, Passive ( Analog-Digital Convers Prototyping and Testir Maintenance and Repair	Components, Active Components, Active Devices, Operatision, Digital Components, Inspection and Test Of Components, Integration, Validation and Verification. Procurement,	ional Amplifier, ents, Simulation,
Components, Passive ( Analog-Digital Convers Prototyping and Testir Maintenance and Repair Mapping of Course	Components, Active Components, Active Devices, Operation, Digital Components, Inspection and Test Of Components, Integration, Validation and Verification. Procurement,	ional Amplifier, ents, Simulation,
Components, Passive ( Analog-Digital Convers Prototyping and Testir Maintenance and Repair Mapping of Course	Components, Active Components, Active Devices, Operation, Digital Components, Inspection and Test Of Components, Integration, Validation and Verification. Procurement,	ional Amplifier, ents, Simulation,
Components, Passive ( Analog-Digital Converse Prototyping and Testir Maintenance and Repair Mapping of Course Outcomes for Unit V Unit VI	Components, Active Components, Active Devices, Operatision, Digital Components, Inspection and Test Of Component, Integration, Validation and Verification. Procurement, CO5: Estimating assorted product design aspects.	ional Amplifier, ents, Simulation, Manufacturing, 6 Hrs.
Components, Passive ( Analog-Digital Converse Prototyping and Testir Maintenance and Repair Mapping of Course Outcomes for Unit V Unit VI Definition, Need, Typ	Components, Active Components, Active Devices, Operation, Digital Components, Inspection and Test Of Components, Integration, Validation and Verification. Procurement, CO5: Estimating assorted product design aspects.	ional Amplifier, ents, Simulation, Manufacturing, 6 Hrs. ility. Audience.
Components, Passive ( Analog-Digital Converse Prototyping and Testir Maintenance and Repair Mapping of Course Outcomes for Unit V Unit VI Definition, Need, Typ	Components, Active Components, Active Devices, Operation, Digital Components, Inspection and Test Of Components, Integration, Validation and Verification. Procurement, CO5: Estimating assorted product design aspects. Documentation es of Documentation, Records, Accountability and Liab n, Preservation of Documents. Methods of Documentation, Vis	ional Amplifier, ents, Simulation, Manufacturing, 6 Hrs. ility. Audience.
Components, Passive ( Analog-Digital Converse Prototyping and Testir Maintenance and Repair Mapping of Course Outcomes for Unit V Unit VI Definition, Need, Typ Preparation, Presentation Layout of Documentation	Components, Active Components, Active Devices, Operation, Digital Components, Inspection and Test Of Components, Integration, Validation and Verification. Procurement, CO5: Estimating assorted product design aspects. Documentation es of Documentation, Records, Accountability and Liab n, Preservation of Documents. Methods of Documentation, Vis	ional Amplifier, ents, Simulation, Manufacturing, 6 Hrs. ility. Audience. sual Techniques,
Components, Passive ( Analog-Digital Converse Prototyping and Testir Maintenance and Repair Mapping of Course Outcomes for Unit V Unit VI Definition, Need, Typ Preparation, Presentation Layout of Documentation	Components, Active Components, Active Devices, Operation, Digital Components, Inspection and Test Of Components, Integration, Validation and Verification. Procurement, CO5: Estimating assorted product design aspects. Documentation es of Documentation, Records, Accountability and Liab n, Preservation of Documents. Methods of Documentation, Vison, Bill of Material.	ional Amplifier, ents, Simulation, Manufacturing, 6 Hrs. ility. Audience. sual Techniques,
Components, Passive ( Analog-Digital Converse Prototyping and Testir Maintenance and Repair Mapping of Course Outcomes for Unit V Unit VI Definition, Need, Typ Preparation, Presentation Layout of Documentation Mapping of Course	Components, Active Components, Active Devices, Operation, Digital Components, Inspection and Test Of Components, Integration, Validation and Verification. Procurement, CO5: Estimating assorted product design aspects. Documentation es of Documentation, Records, Accountability and Liab n, Preservation of Documents. Methods of Documentation, Vison, Bill of Material. CO6: Exemplifying special design considerations and	ional Amplifier, ents, Simulation, Manufacturing, 6 Hrs. ility. Audience. sual Techniques,
Components, Passive ( Analog-Digital Converse Prototyping and Testir Maintenance and Repair Mapping of Course Outcomes for Unit V Unit VI Definition, Need, Typ Preparation, Presentation Layout of Documentation Mapping of Course	Components, Active Components, Active Devices, Operation, Digital Components, Inspection and Test Of Components, Integration, Validation and Verification. Procurement, CO5: Estimating assorted product design aspects. Documentation es of Documentation, Records, Accountability and Liab n, Preservation of Documents. Methods of Documentation, Vison, Bill of Material. CO6: Exemplifying special design considerations and	ional Amplifier, ents, Simulation, Manufacturing, 6 Hrs. ility. Audience. sual Techniques,
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Components, Passive ( Analog-Digital Converse Prototyping and Testir Maintenance and Repair Mapping of Course Outcomes for Unit V Unit VI Definition, Need, Typ Preparation, Presentation Layout of Documentation Mapping of Course Outcomes for Unit VI Text Books: 1. Kim Fowler, "El 2. Robert J. Herrick	Components, Active Components, Active Devices, Operati sion, Digital Components, Inspection and Test Of Components, Integration, Validation and Verification. Procurement, CO5: Estimating assorted product design aspects. Documentation es of Documentation, Records, Accountability and Liab n, Preservation of Documents. Methods of Documentation, Vison, Bill of Material. CO6: Exemplifying special design considerations and documentation. Learning Resources ectronic Instrument Design", Oxford University Press. c, "Printed Circuit board design Techniques for EMC Compliant	ional Amplifier, ents, Simulation, Manufacturing, 6 Hrs. ility. Audience. sual Techniques, importance of
Components, Passive ( Analog-Digital Convers Prototyping and Testir Maintenance and Repair Mapping of Course Outcomes for Unit V Unit VI Definition, Need, Typ Preparation, Presentation Layout of Documentation Mapping of Course Outcomes for Unit VI Text Books: 1. Kim Fowler, "El 2. Robert J. Herrick Edition, IEEE pr	Components, Active Components, Active Devices, Operati sion, Digital Components, Inspection and Test Of Components, Integration, Validation and Verification. Procurement, CO5: Estimating assorted product design aspects. Documentation es of Documentation, Records, Accountability and Liab n, Preservation of Documents. Methods of Documentation, Vison, Bill of Material. CO6: Exemplifying special design considerations and documentation. Learning Resources ectronic Instrument Design", Oxford University Press. c, "Printed Circuit board design Techniques for EMC Compliant	ional Amplifier, ents, Simulation, Manufacturing, 6 Hrs. ility. Audience. sual Techniques, importance of
Components, Passive ( Analog-Digital Converse Prototyping and Testir Maintenance and Repair Mapping of Course Outcomes for Unit V Unit VI Definition, Need, Typ Preparation, Presentation Layout of Documentation Mapping of Course Outcomes for Unit VI Text Books: 1. Kim Fowler, "El 2. Robert J. Herrick Edition, IEEE pr Reference Books:	Components, Active Components, Active Devices, Operati sion, Digital Components, Inspection and Test Of Component ng, Integration, Validation and Verification. Procurement, CO5: Estimating assorted product design aspects. Documentation es of Documentation, Records, Accountability and Liab n, Preservation of Documents. Methods of Documentation, Vison, Bill of Material. CO6: Exemplifying special design considerations and documentation. Learning Resources ectronic Instrument Design", Oxford University Press. c, "Printed Circuit board design Techniques for EMC Compliant ess.	ional Amplifier, ents, Simulation, Manufacturing, 6 Hrs. ility. Audience. sual Techniques, importance of nce", 2 <sup>nd</sup>
Components, Passive ( Analog-Digital Converse Prototyping and Testir Maintenance and Repair Mapping of Course Outcomes for Unit V Unit VI Definition, Need, Typ Preparation, Presentation Layout of Documentation Mapping of Course Outcomes for Unit VI Text Books: 1. Kim Fowler, "El 2. Robert J. Herrick Edition, IEEE pr Reference Books: 1. James K. Peckol	Components, Active Components, Active Devices, Operati sion, Digital Components, Inspection and Test Of Components, Integration, Validation and Verification. Procurement, CO5: Estimating assorted product design aspects. Documentation es of Documentation, Records, Accountability and Liab n, Preservation of Documents. Methods of Documentation, Vison, Bill of Material. CO6: Exemplifying special design considerations and documentation. Learning Resources ectronic Instrument Design", Oxford University Press. c, "Printed Circuit board design Techniques for EMC Compliant	ional Amplifier, ents, Simulation, Manufacturing, 6 Hrs. ility. Audience. sual Techniques, importance of nce", 2 <sup>nd</sup>

**Udemy Courses :** 

1. Introduction to Product Management

Link: https://www.udemy.com/product-management/

2. Fundamental Steps of Product Management

Link: <u>https://www.udemy.com/productmgt/</u>

- Digital Product Manufacturing: The Roadmap to Success
   Link: <u>https://www.udemy.com/digital-product-manufacturing/</u>
- Agile Product Owner Career Guide
   Link: <u>https://www.udemy.com/product-owner-career-guide/</u>

	Sa	vitribai Phule P	une University	
Fou	ırth Yea	r of E & Tc En	gineering (2019 Course	e)
			-	,
404185 (B): Deep Learning (Elective - IV)				
Teaching Scheme:   Credit   Examination		Examination Schen	1e:	
Theory: 03 Hrs. / W	veek	03	In-Sem (Theory):	<b>30 Marks</b>
			End Sem (Theory):	70 Marks
Prerequisite Courses, i	f any:			
Companion Course, if	any:			
Course Objectives:				
Deep Learning algorit 3. To examine the case s <b>Course Outcomes:</b> On a <b>CO1: Classify</b> machine <b>CO2: Discuss</b> the conce <b>CO3: Identify</b> the deep <b>CO4: Demonstrate</b> diff <b>CO5: Discuss</b> natural la <b>CO6: Make</b> use of vario	tudies of c completion learning a pts of dee learning a erent arch nguage pro	leep learning techn n of the course, lear lgorithms and its ty p learning and its F rchitectures with re itectures of Convol occessing architectur	rner will be able to: pes. Frameworks. espect to the applications. utional neural networks. res. ning applications.	
Unit I		Machi	ne Learning	6 Hrs.
and Logistic Regressio Machine. Applications o	n, Decisio f machine	on Tree and Rand learning	ines Learning, Linear Regi dom Forest, Naïve Bayes ing algorithms and its types.	
Unit II	Introdu	ction to Deep L	earning and Framewo	rks 6 Hrs.
		• •	, the perceptron, Multi Lay	
		• •	lating Tensor Flow Var	
-			over the CPU and GPU, Spraining the Logistic Regres	
Keras, PyTorch.	CH501 110	w, Logging and T	ranning the Logistic Regles	
Mapping of Course Outcomes for Unit II	CO2: Disc	cuss the concepts of	deep learning and its Frame	works.

Unit III	Deep Learning Architecture	6 Hrs.
1	Neural Networks, Different Activation Functions, Batc zation., Dropout, regularization Unsupervised Training of N	
6 6	achines, Auto Encoders, Deep Learning Applications	icular networks,
	CO3: Identify the deep learning architectures with respect to th	ne applications.
Outcomes for Unit III	······································	
Unit IV	Computer Vision	6 Hrs.
Architectural Overview,	Motivation, Layers, Filters, Parameter sharing, Regularizati	on, Convolution
neural networks (CNNs)	), convolution, pooling and its variations, different deep CNI	N architectures -
LeNet, AlexNet, VGG	, PlacesNet, DenseNet, Training a CNNs: weights init	ialization, batch
normalization, hyperpara	ameter tuning .	
•	tures: ResNet, AlexNet – Applications.	
MappingofCourseOutcomes for Unit IV	CO4: Demonstrate different architectures of Convolutional neu	ıral networks.
Unit V	Natural Language Processing	6 Hrs.
- BPTT for training R	orks, Bidirectional RNNs, Encoder-decoder sequence to seque NN, Long Short Term Memory Networks. Advanced RNN ve Adversarial Networks (GANs).	
- BPTT for training RI introduction to Generative	NN, Long Short Term Memory Networks. Advanced RNN	
- BPTT for training RI introduction to Generativ Mapping of Course	NN, Long Short Term Memory Networks. Advanced RNN ve Adversarial Networks (GANs).	
- BPTT for training Ri introduction to Generativ Mapping of Course Outcomes for Unit V Unit VI	NN, Long Short Term Memory Networks. Advanced RNN ve Adversarial Networks (GANs). CO5: Discuss natural language processing architectures.	I: LSTM, GRU,
- BPTT for training Ri introduction to Generativ Mapping of Course Outcomes for Unit V Unit VI Computer Vision: Imag	NN, Long Short Term Memory Networks. Advanced RNN ve Adversarial Networks (GANs). CO5: Discuss natural language processing architectures. Case Study and Applictions	1: LSTM, GRU, 6 Hrs.
- BPTT for training Ri introduction to Generativ Mapping of Course Outcomes for Unit V Unit VI Computer Vision: Imag Natural Language Proo Mapping of Course	NN, Long Short Term Memory Networks. Advanced RNN ve Adversarial Networks (GANs). CO5: Discuss natural language processing architectures. Case Study and Applictions ge Classification, Image net- Detection-Audio Wave Net.	1: LSTM, GRU, <b>6 Hrs.</b> t
- BPTT for training Ri introduction to Generativ Mapping of Course Outcomes for Unit V Unit VI Computer Vision: Imag Natural Language Proc	NN, Long Short Term Memory Networks. Advanced RNN ve Adversarial Networks (GANs). CO5: Discuss natural language processing architectures. Case Study and Applictions ge Classification, Image net- Detection-Audio Wave Net. cessing: Sentimental Analysis, Text preprocessing and chatBo CO6: Make use of various case studies and deep learning applied	1: LSTM, GRU, <b>6 Hrs.</b> t
- BPTT for training Ri introduction to Generativ Mapping of Course Outcomes for Unit V Unit VI Computer Vision: Imag Natural Language Proo Mapping of Course	NN, Long Short Term Memory Networks. Advanced RNN ve Adversarial Networks (GANs). CO5: Discuss natural language processing architectures. Case Study and Applictions ge Classification, Image net- Detection-Audio Wave Net. cessing: Sentimental Analysis, Text preprocessing and chatBo	1: LSTM, GRU, <b>6 Hrs.</b> t
- BPTT for training Ri introduction to Generativ Mapping of Course Outcomes for Unit V Unit VI Computer Vision: Imag Natural Language Proo Mapping of Course	NN, Long Short Term Memory Networks. Advanced RNN ve Adversarial Networks (GANs). CO5: Discuss natural language processing architectures. Case Study and Applictions ge Classification, Image net- Detection-Audio Wave Net. cessing: Sentimental Analysis, Text preprocessing and chatBo CO6: Make use of various case studies and deep learning applied	1: LSTM, GRU, <b>6 Hrs.</b> t
- BPTT for training Ri introduction to Generative Mapping of Course Outcomes for Unit V Unit VI Computer Vision: Image Natural Language Proce Mapping of Course Outcomes for Unit VI	NN, Long Short Term Memory Networks. Advanced RNN ve Adversarial Networks (GANs). CO5: Discuss natural language processing architectures. Case Study and Applictions ge Classification, Image net- Detection-Audio Wave Net. cessing: Sentimental Analysis, Text preprocessing and chatBo CO6: Make use of various case studies and deep learning applied	E LSTM, GRU,
<ul> <li>BPTT for training Rl introduction to Generative Mapping of Course Outcomes for Unit V</li> <li>Unit VI</li> <li>Computer Vision: Image Natural Language Proceed Mapping of Course Outcomes for Unit VI</li> <li>Text Books:         <ol> <li>Nikhil Buduma,</li> </ol> </li> </ul>	NN, Long Short Term Memory Networks. Advanced RNN ve Adversarial Networks (GANs). CO5: Discuss natural language processing architectures. Case Study and Applictions ge Classification, Image net- Detection-Audio Wave Net. cessing: Sentimental Analysis, Text preprocessing and chatBo CO6: Make use of various case studies and deep learning applied Learning Resources	E LSTM, GRU,
<ul> <li>BPTT for training Rl introduction to Generative Mapping of Course Outcomes for Unit V</li> <li>Unit VI</li> <li>Computer Vision: Image Natural Language Proceed Mapping of Course Outcomes for Unit VI</li> <li>Text Books:         <ol> <li>Nikhil Buduma, Intelligence Algo 2. Michael Nielsen,</li> </ol> </li> </ul>	NN, Long Short Term Memory Networks. Advanced RNN ve Adversarial Networks (GANs). CO5: Discuss natural language processing architectures. Case Study and Applictions ge Classification, Image net- Detection-Audio Wave Net. cessing: Sentimental Analysis, Text preprocessing and chatBo CO6: Make use of various case studies and deep learning applie Learning Resources , "Fundamentals of Deep Learning Designing Next-Gene orithms", 1 <sup>st</sup> Edition, O'REILLY. "Neural Networks and Deep Learning", Determination Press.	t cations.
<ul> <li>BPTT for training Rintroduction to Generative Mapping of Course Outcomes for Unit V</li> <li>Unit VI</li> <li>Computer Vision: Image Natural Language Proceed Mapping of Course Outcomes for Unit VI</li> <li>Text Books:         <ol> <li>Nikhil Buduma, Intelligence Algo</li> <li>Michael Nielsen, 3. Ian Goodfellow,</li> </ol> </li> </ul>	NN, Long Short Term Memory Networks. Advanced RNN ve Adversarial Networks (GANs). CO5: Discuss natural language processing architectures. Case Study and Applictions ge Classification, Image net- Detection-Audio Wave Net. cessing: Sentimental Analysis, Text preprocessing and chatBo CO6: Make use of various case studies and deep learning applied Learning Resources , "Fundamentals of Deep Learning Designing Next-Gene orithms", 1 <sup>st</sup> Edition, O'REILLY. "Neural Networks and Deep Learning", Determination Press. YoshuaBengio and Aaron Courville, "Deep Learning", MIT P	E: LSTM, GRU,
<ul> <li>BPTT for training RI introduction to Generative Mapping of Course Outcomes for Unit V</li> <li>Unit VI</li> <li>Computer Vision: Image Natural Language Proceed Mapping of Course Outcomes for Unit VI</li> <li>Text Books: <ol> <li>Nikhil Buduma, Intelligence Algo</li> <li>Michael Nielsen,</li> <li>Ian Goodfellow,</li> <li>Josh Patterson, A</li> </ol> </li> </ul>	NN, Long Short Term Memory Networks. Advanced RNN ve Adversarial Networks (GANs). CO5: Discuss natural language processing architectures. Case Study and Applictions ge Classification, Image net- Detection-Audio Wave Net. cessing: Sentimental Analysis, Text preprocessing and chatBo CO6: Make use of various case studies and deep learning applied Learning Resources , "Fundamentals of Deep Learning Designing Next-Generation, Ist Edition, O'REILLY. "Neural Networks and Deep Learning", Determination Press. YoshuaBengio and Aaron Courville, "Deep Learning", MIT P	t cations. eration Machine ress. eeilly Media.
<ul> <li>BPTT for training RI introduction to Generative Mapping of Course Outcomes for Unit V</li> <li>Unit VI</li> <li>Computer Vision: Image Natural Language Proceed Mapping of Course Outcomes for Unit VI</li> <li>Text Books: <ol> <li>Nikhil Buduma, Intelligence Algo</li> <li>Michael Nielsen,</li> <li>Ian Goodfellow,</li> <li>Josh Patterson, A</li> <li>Kevin P. Murphy</li> </ol> </li> </ul>	NN, Long Short Term Memory Networks. Advanced RNN ve Adversarial Networks (GANs). CO5: Discuss natural language processing architectures. Case Study and Applictions ge Classification, Image net- Detection-Audio Wave Net. cessing: Sentimental Analysis, Text preprocessing and chatBo CO6: Make use of various case studies and deep learning applic Learning Resources , "Fundamentals of Deep Learning Designing Next-Gene orithms", 1 <sup>st</sup> Edition, O'REILLY. "Neural Networks and Deep Learning", Determination Press. YoshuaBengio and Aaron Courville, "Deep Learning", MIT P adam Gibson "Deep Learning: A Practitioner's Approach", O'R	t cations. eration Machine ress. ceilly Media. ress.
<ul> <li>BPTT for training RI introduction to Generative Mapping of Course Outcomes for Unit V</li> <li>Unit VI</li> <li>Computer Vision: Image Natural Language Proceed Mapping of Course Outcomes for Unit VI</li> <li>Text Books: <ol> <li>Nikhil Buduma, Intelligence Algo</li> <li>Michael Nielsen,</li> <li>Ian Goodfellow,</li> <li>Josh Patterson, A</li> <li>Kevin P. Murphy</li> <li>Ethem Alpaydin,</li> </ol> </li> </ul>	NN, Long Short Term Memory Networks. Advanced RNN ve Adversarial Networks (GANs). CO5: Discuss natural language processing architectures. Case Study and Applictions ge Classification, Image net- Detection-Audio Wave Net. cessing: Sentimental Analysis, Text preprocessing and chatBo CO6: Make use of various case studies and deep learning applie Learning Resources , "Fundamentals of Deep Learning Designing Next-Gene prithms", 1 <sup>st</sup> Edition, O'REILLY. "Neural Networks and Deep Learning", Determination Press. YoshuaBengio and Aaron Courville, "Deep Learning", MIT P adam Gibson "Deep Learning: A Practitioner's Approach", O'R "Machine Learning: A Probabilistic Perspective", The MIT P	t cations. eration Machine ress. ceilly Media. ress. l of India.
<ul> <li>BPTT for training Rintroduction to Generative Mapping of Course Outcomes for Unit V</li> <li>Unit VI</li> <li>Computer Vision: Image Natural Language Proceed Mapping of Course Outcomes for Unit VI</li> <li>Text Books: <ol> <li>Nikhil Buduma, Intelligence Algo</li> <li>Michael Nielsen,</li> <li>Ian Goodfellow,</li> <li>Josh Patterson, A</li> <li>Kevin P. Murphy</li> <li>Ethem Alpaydin,</li> <li>Umberto Michel</li> </ol></li></ul>	NN, Long Short Term Memory Networks. Advanced RNN ve Adversarial Networks (GANs). CO5: Discuss natural language processing architectures. Case Study and Applictions ge Classification, Image net- Detection-Audio Wave Net. cessing: Sentimental Analysis, Text preprocessing and chatBo CO6: Make use of various case studies and deep learning applic Learning Resources , "Fundamentals of Deep Learning Designing Next-Gene orithms", 1 <sup>st</sup> Edition, O'REILLY. "Neural Networks and Deep Learning", Determination Press. YoshuaBengio and Aaron Courville, "Deep Learning", MIT P adam Gibson "Deep Learning: A Practitioner's Approach", O'R	t cations. eration Machine ress. ceilly Media. ress. l of India.

### **Reference Books:**

- 1. Goodfellow. I., Bengio.Y., and Courville, A., "Deep Learning", MIT Press.
- 2. Bishop, C.M., "Pattern Recognition and Machine Learning", Springer.
- 3. Satish Kumar, "Neural Networks: A Classroom Approach", Tata McGraw-Hill Education.

## **MOOC / NPTEL Courses:**

1. NPTEL Course on "Deep Learning", by Prof. Prabir Kumar Bhiswas, IIT Kharagpur.

Link of the Course: <u>https://nptel.ac.in/courses/106105215</u>

2. NPTEL Course on "Deep Learning - Part I", by Prof. Sudarshan Iyengar, Prof Sanatan Sukhija IIT Ropar

Link of the Course: https://nptel.ac.in/courses/106106184

Sa	vitribai Phule P	une University		
Fourth Year of E & Tc Engineering (2019 Course)				
404185 (D): Low Power CMOS (Elective - IV)				
Teaching Scheme:	Credit	Examination Scl	heme:	
Theory: 03 Hrs. / Week	03	In-Sem (Theory): 30	Marks	
		End Sem (Theory): 70	Marks	
Prerequisite Courses, if any:				
1. Electronic Circuits				
2. Digital Circuits				
Companion Course, if any: 1. VLSI Design and Technology	OGN			
Course Objectives: is to make th				
1. Identify sources of power				
1	-	sed on technology independent	and technology.	
dependent power dissipatio				
3. To describe suitable techni		_		
4. To design memory circuits				
5. To learn to use CAD tools	1 1			
Course Outcomes: On completio				
<b>CO1: Explain</b> the sources of pow <b>CO2: Classify</b> the special techniq	-		ouite	
<b>CO3: Summarize</b> the power optim			cuits.	
<b>CO4: Illustrate</b> the power estima				
CO5: Explain the software design				
<b>CO6: Use</b> the CAD tools for low				
	Course Co	ontents		
Unit I Fund	damentals of Pov	wer Dissipation in CMOS	<b>07 Hrs.</b>	
Sources of power dissipation, Ph	sysics of power diss	ipation in MOSFET devices: Th	e MIS structure,	
long channel MOSFET, Submic	eron MOSFET, gat	e induced drain leakage, Powe	er dissipation in	
CMOS: short circuit dissipation,	dynamic dissipation	on, load capacitance, Low powe	er VLSI design:	
Limits, principles of low power of	lesign, hierarchy of	limits, fundamental limit, mater	rial limit, device	
limit, system limit.				
Mapping of Course CO1: Explain the sources of power dissipation in CMOS.				
Outcomes for Unit I	_			

Unit II	<b>Power Optimization Techniques</b>	<b>08 Hrs.</b>		
Power Reduction in Clock Networks: Clock Gating, Reduced Swing Clock, Oscillator Circuit for				
Clock Generation, Frequency Division and Multiplication, Other Clock Power Reduction Techniques,				
CMOS Floating Node: Tristate Keeper Circuit, Blocking Gate, Low Power Bus: Low Swing Bus,				
Charge Recycling Bus,	Delay Balancing, Low Power Techniques for SRAM: SRAM	M Cell, Memory		
Bank Partitioning, Pulse	d Word line and Reduced bit line Swing.			
Introduction to Low-Por	wer Design through Voltage Scaling: VTCMOS circuits, MT	CMOS circuits,		
Architectural Level App	roach – Pipelining and Parallel Processing Approaches. Switc	hed Capacitance		
Minimization Approache	es: System Level Measures, Circuit Level Measures, Mask lev	el Measures.		
	CO2: Classify the special techniques to mitigate the power cons	umption in		
Outcomes for Unit II	CMOS circuits.			
Unit III	<b>Design of Low Power Circuits</b>	<b>07 Hrs.</b>		
Transistor and Gate Sizi	ng : Sizing an Inverter Chain, Transistor and Gate Sizing for	Dynamic Power		
Reduction, Transistor	Sizing for Leakage Power Reduction, Network Re	structuring and		
Reorganization : Tra	nsistor Network Restructuring, Transistor Network P	artitioning and		
Reorganization, Special	Latches and Flip-flops : Self-gating Flip-flop, Combina	tional Flip-flop,		
Double Edge Triggered	Flip-flop, Low Power Digital Cell Library : Cell Sizes and Sp	pacing, Varieties		
of Boolean Functions, A	djustable Device Threshold Voltage.			
	CO3: Summarize the power optimization and trade off techniq	ues in digital		
Outcomes for Unit III	circuits.			
Unit IV	Power Estimation	<b>07 Hrs.</b>		
	signal probability calculation, Statistical techniques, estimat			
	ysis, Power estimation using input vector compaction, pow-	-		
-	liability, power estimation at the circuit level, Estimation of n	-		
	proach, steepest descent, generic based algorithm based approa	ich.		
Mapping of Course Outcomes for Unit IV	CO4: Illustrate the power estimation at logic and circuit level.			
Unit V	Software Design for Low Power	07 Hrs.		
	wer dissipation, software power estimation: Gate level, archit			
1	iction level power analysis, software power optimization: min	,		
	n selection and ordering, power management, Automated 1			
generation, Co-design fo		pomer code		
Mapping of Course	-			
Outcomes for Unit V	CO5: Explain the software design for low power in various leve	<u>.</u>		
Unit VI	Hardware Design for Low Power	06 Hrs.		
Adiabatic Switching Cir	cuits, Battery-aware Synthesis, Variation tolerant design, CA	AD tools for low		
power synthesis.				
Mapping of Course				

MappingofCourseOutcomes for Unit VICO6: Able to use the CAD tools for low power synthesis

Learning Resources
Text Books:
<ol> <li>Kaushik Roy and S. C. Prasad, "Low power CMOS VLSI Circuit Design", Wiley Publication</li> <li>Gary Yeap, "Practical Low Power Digital VLSI Design", Springer</li> </ol>
3. A. P. Chandrasekaran and R. W. Broadersen, "Low Power Digital CMOS Design", Kluwer, 1995
Reference Books:
1. J. B. Kulo and J.H Lou, "Low voltage CMOS VLSI Circuits", Wiley Publication
2. Dimitrios Soudris, Christians Pignet, Costas Goutis, "Designing CMOS Circuits for Low Power", Kluwer.
3. James B. Kulo, Shih-Chia Lin, "Low voltage SOI CMOS VLSI devices and Circuits", John Wiley and sons.
4. Steven M. Rubin, "Computer Aids for VLSI Design", Addison Wesley Publishing
5. Abdelatif Belaouar, Mohamed. I. Elmasry, "Low power digital VLSI design", Kluwer.
Online Resources:
1. <u>https://www.youtube.com/watch?v=w0cSahiDvFQ</u>
2. <u>https://www.youtube.com/watch?v=LjDb6VQlOeQ</u>
3. <u>http://freevideolectures.com/Course/3059/Low-Power-VLSI-Circuits-and-Systems</u>

4. http://www.springer.com/us/book/9788132219361

	Sav	vitribai Phule I	Pune University	
Fou	irth Yeai	r of E & Tc En	gineering (2019 Course)	
	404185 (	E): Smart Ant	ennas (Elective - IV)	
Teaching Schen	ne:	Credit	Examination S	cheme:
Theory: 03 Hrs. / W	/eek	03	In-Sem (Theory): 30	Marks
			End Sem (Theory):70	Marks
Prerequisite Courses, if 1. Electromagnetic 2. Cellular Network Companion Course, if a	Field Theo	ry		
Course Objectives:				
quality, power m Course Outcomes: On a CO1: Compare various analyze them base given application. CO2: Classify Microstri	vill be on the anagement completion linear wire d on the cu p & re-con tenna syste imation me forming m	he 4G, 5G and bey and BW for higher of the course, lease antenna and unif arrent distribution figurable antenna ems and discuss the othods and classify nethods.	yond needs of antenna to improver data rate. rner will be able to orm array in terms of antenna pa and identify an appropriate wire and techniques. e beam steering and mutual cou	arameters and e antenna for
COO: Describe and Con		2	antanta	
TT •/ T		Course C		0.11
Unit I		<u>U</u>	lements and Array	8 Hrs.
wave length dipole, and	analytical	treatment of these	mal dipole, small dipole, finite e elements. Types of Array ant d spaced linear broadside and en	enna, two element
Mapping of Course Outcomes for Unit I	of a dist	ntenna paramete	ear wire antenna and uniform rs and analyze them based on atify an appropriate wire ante	the current
Unit II	Mic	rostrin and Re	configurable Antenna	6 Hrs.
			es, Fractal antenna and array.	
—			nfigurable antenna, Re-config	urable techniques
Multiple Re-configurable				1
Mapping of Course Outcomes for Unit II	CO2: Class	sify Microstrip &	re-configurable antenna and tech	nniques.

<b>Unit III</b>	Smart Antennas	<b>8 Hrs.</b>
Introduction, Need for	Smart Antennas, Overview: Smart Antenna Configurations,	Switched-Beam
Antennas, Adaptive Ant	tenna Approach, beam steering, degree of freedom.	
Architecture of a Sm	art Antenna System: Transmitter and Receiver, Types of S	Smart Antennas,
Benefits and Drawbac	eks of Smart Antennas, Mutual Coupling Effects, Applica	ations of Smart
Antennas.		
Mapping of Course	CO3: Describe smart antenna systems and discuss the bea	am steering and
Outcomes for Unit III	mutual coupling effects.	
Unit IV	<b>Direction of Arrival Estimation (DOA) Methods</b>	6 Hrs.
Unit I V	Direction of Arrivar Estimation (DOA) Methods	0 1115.
1	ethods, linear prediction method, Maximum entropy met gen structure methods, MUSIC algorithm – root music ar algorithm.	
Mapping of Course Outcomes for Unit IV	CO4: Explain DOA estimation methods and classify.	1
TT •4 T7		
	Beam Forming Methods , Statistically Optimum Beam-forming Weight Vectors, Maxim	
Classical Beam former former, Multiple Sidelo (MMSE), Direct Matr	, Statistically Optimum Beam-forming Weight Vectors, Maxin be Canceler and Maximum, SINR Beam former, Minimum Me rix Inversion (DMI), Linearly Constrained Minimum Var	mum SNR Beam ean Square Erroi
Classical Beam former former, Multiple Sidelo	, Statistically Optimum Beam-forming Weight Vectors, Maxin be Canceler and Maximum, SINR Beam former, Minimum Me rix Inversion (DMI), Linearly Constrained Minimum Van r Beam forming.	mum SNR Beam
Classical Beam former former, Multiple Sidelo (MMSE), Direct Matr Adaptive Algorithms fo Mapping of Course	, Statistically Optimum Beam-forming Weight Vectors, Maxin be Canceler and Maximum, SINR Beam former, Minimum Me rix Inversion (DMI), Linearly Constrained Minimum Van r Beam forming.	mum SNR Beam
Classical Beam former former, Multiple Sidelo (MMSE), Direct Matr Adaptive Algorithms fo Mapping of Course Outcomes for Unit V Unit VI	<ul> <li>Statistically Optimum Beam-forming Weight Vectors, Maxin be Canceler and Maximum, SINR Beam former, Minimum Metrix Inversion (DMI), Linearly Constrained Minimum Van Beam forming.</li> <li>CO5: Classify the beam forming methods.</li> </ul>	mum SNR Beam ean Square Error riance (LCMV), <b>6 Hrs.</b>
Classical Beam former former, Multiple Sidelo (MMSE), Direct Matr Adaptive Algorithms fo Mapping of Course Outcomes for Unit V Unit VI Introduction, Principles	<ul> <li>Statistically Optimum Beam-forming Weight Vectors, Maxin be Canceler and Maximum, SINR Beam former, Minimum Metrix Inversion (DMI), Linearly Constrained Minimum Var Beam forming.</li> <li>CO5: Classify the beam forming methods.</li> </ul>	mum SNR Beam ean Square Error riance (LCMV), <b>6 Hrs.</b>
Classical Beam former former, Multiple Sidelo (MMSE), Direct Matr Adaptive Algorithms fo Mapping of Course Outcomes for Unit V Unit VI Introduction, Principles Wave, massive MIMO:	<ul> <li>Statistically Optimum Beam-forming Weight Vectors, Maxin be Canceler and Maximum, SINR Beam former, Minimum Merix Inversion (DMI), Linearly Constrained Minimum Var Beam forming.</li> <li>CO5: Classify the beam forming methods.</li> <li>MIMO Antennas</li> <li>of MIMO systems: SISO, SIMO, MISO MIMO, Hybrid anter concept and applications.</li> </ul>	mum SNR Beam ean Square Error riance (LCMV), <b>6 Hrs.</b>
Classical Beam former former, Multiple Sidelo (MMSE), Direct Matr Adaptive Algorithms fo Mapping of Course Outcomes for Unit V Unit VI Introduction, Principles Wave, massive MIMO:	<ul> <li>Statistically Optimum Beam-forming Weight Vectors, Maxin be Canceler and Maximum, SINR Beam former, Minimum Matrix Inversion (DMI), Linearly Constrained Minimum Var Beam forming.</li> <li>CO5: Classify the beam forming methods.</li> <li>MIMO Antennas</li> <li>of MIMO systems: SISO, SIMO, MISO MIMO, Hybrid anter concept and applications.</li> </ul>	mum SNR Beam ean Square Error riance (LCMV), <b>6 Hrs.</b>
Classical Beam former former, Multiple Sidelo (MMSE), Direct Matr Adaptive Algorithms fo Mapping of Course Outcomes for Unit V Unit VI Introduction, Principles Wave, massive MIMO: Mapping of Course	<ul> <li>Statistically Optimum Beam-forming Weight Vectors, Maxin be Canceler and Maximum, SINR Beam former, Minimum Metrix Inversion (DMI), Linearly Constrained Minimum Var Beam forming.</li> <li>CO5: Classify the beam forming methods.</li> <li>MIMO Antennas</li> <li>of MIMO systems: SISO, SIMO, MISO MIMO, Hybrid anter concept and applications.</li> <li>CO6: Describe and Compare MIMO systems.</li> </ul>	mum SNR Beam ean Square Error riance (LCMV), <b>6 Hrs.</b>
Classical Beam former former, Multiple Sidelo (MMSE), Direct Matr Adaptive Algorithms fo Mapping of Course Outcomes for Unit V Unit VI Introduction, Principles Wave, massive MIMO: Mapping of Course Outcomes for Unit VI	<ul> <li>Statistically Optimum Beam-forming Weight Vectors, Maxin be Canceler and Maximum, SINR Beam former, Minimum Merix Inversion (DMI), Linearly Constrained Minimum Var Beam forming.</li> <li>CO5: Classify the beam forming methods.</li> <li>MIMO Antennas</li> <li>of MIMO systems: SISO, SIMO, MISO MIMO, Hybrid anter concept and applications.</li> </ul>	mum SNR Beam ean Square Error riance (LCMV), <b>6 Hrs.</b>
Classical Beam former former, Multiple Sidelo (MMSE), Direct Matr Adaptive Algorithms fo Mapping of Course Outcomes for Unit V Unit VI Introduction, Principles Wave, massive MIMO: Mapping of Course Outcomes for Unit VI	<ul> <li>Statistically Optimum Beam-forming Weight Vectors, Maxin be Canceler and Maximum, SINR Beam former, Minimum Merix Inversion (DMI), Linearly Constrained Minimum Var Beam forming.</li> <li>CO5: Classify the beam forming methods.</li> <li>CO5: Classify the beam forming methods.</li> <li>MIMO Antennas         <ul> <li>of MIMO systems: SISO, SIMO, MISO MIMO, Hybrid anter concept and applications.</li> <li>CO6: Describe and Compare MIMO systems.</li> </ul> </li> </ul>	mum SNR Beam ean Square Error riance (LCMV), 6 Hrs. nna array for mm
Classical Beam former former, Multiple Sidelo (MMSE), Direct Matr Adaptive Algorithms fo Mapping of Course Outcomes for Unit V Unit VI Introduction, Principles Wave, massive MIMO: Mapping of Course Outcomes for Unit VI Text Books: 1. C.A. Balanis "Ar	<ul> <li>Statistically Optimum Beam-forming Weight Vectors, Maxin be Canceler and Maximum, SINR Beam former, Minimum Metrix Inversion (DMI), Linearly Constrained Minimum Var Beam forming.</li> <li>CO5: Classify the beam forming methods.</li> <li>MIMO Antennas</li> <li>of MIMO systems: SISO, SIMO, MISO MIMO, Hybrid anter concept and applications.</li> <li>CO6: Describe and Compare MIMO systems.</li> </ul>	mum SNR Beam ean Square Error riance (LCMV) 6 Hrs. nna array for mm

#### **Reference Books:**

- 1. C.A.Balanis,"Introduction to Smart Antennas", John Wiley & Sons
- 2. Mohammod Ali, "Reconfigurable antenna Design and Analysis", Publisher: Artech House
- George Tsoulos," MIMO system technology for wireless communications", CRC- Taylor & Francis.
- 4. Long Zhao, Hui Zhao, Kan Zheng, Wei Xiang, "Massive MIMO in 5G Networks: Selected Applications", Springer.
- 5. Jian Li and Petre Stoica," Robust adaptive Beamforming", John Wiley.

			ne Univers	•
	Fourth Yes	ar of E & Tc Engi	neering (20	)19 Course)
		404186: Lab Pr	actice - 1	
Teachin	CreditExamination Scheme:			
Practical: 04 Hrs. / Week 02 Term Work: 25 Marks				
			Oral:	50 Marks
Companie	on Course, if any:			
1. Ra	diation and Microwave	Theory		
	oud Computing	5		
		delines for Studen	t's Lab Jou	ırnal
The stude	ent's Lah Journal can h	e experimental write	une It should	d include following as applicabl
		-	-	f Submission, Aims & Objective
-	Description of data used,		ance, Date of	buomission, rums & objective
		idelines for Lab /1	W Assess	nent
			•	student in the Lab course. Suitab
ubrics ca	n be used by the interna			
	Subjec	t: Radiation and I	Microwave	Theory
		List of Exper	riments	
1.	To study of different typ	bes of Microwave Com	ponents	
				antenna at microwave frequency
2.		ttern and gain of horn	or parabolic	
2. 3.	To measure radiation pa	ttern and gain of horn ode characteristics of F	or parabolic Reflex klystro	n.
2. 3. 4.	To measure radiation pa To measure and plot Mo To measure V-I characte	ttern and gain of horn ode characteristics of F eristics of Gunn Diode	or parabolic a Reflex klystro and study of	n.
2. 3. 4. 5.	To measure radiation pa To measure and plot Mo To measure V-I characte To measure and verify p	ttern and gain of horn ode characteristics of F eristics of Gunn Diode port characteristics of r	or parabolic Reflex klystro and study of nicrowave tee	n. PIN modulator. es (E, H, E-H or magic planes).
2. 3. 4. 5.	To measure radiation pa To measure and plot Mo To measure V-I characte To measure and verify p	ttern and gain of horn ode characteristics of R eristics of Gunn Diode ort characteristics of r port characteristics	or parabolic Reflex klystro and study of nicrowave tee	n. PIN modulator. es (E, H, E-H or magic planes).
2. 3. 4. 5. 6.	To measure radiation pa To measure and plot Mc To measure V-I characte To measure and verify p To measure and verify factor, insertion loss and	ttern and gain of horn ode characteristics of F eristics of Gunn Diode port characteristics of r port characteristics I directivity.	or parabolic Reflex klystro and study of nicrowave tee of directiona	n. PIN modulator. es (E, H, E-H or magic planes). l coupler and calculate couplin
2. 3. 4. 5. 6. 7.	To measure radiation pa To measure and plot Mo To measure V-I characte To measure and verify p To measure and verify factor, insertion loss and To measure and verify loss and isolation in dB.	ttern and gain of horn ode characteristics of R eristics of Gunn Diode oort characteristics of r o port characteristics l directivity. port characteristics of	or parabolic Reflex klystro and study of nicrowave tee of directiona f isolator and	n. PIN modulator. es (E, H, E-H or magic planes). Il coupler and calculate couplir circulator and calculate insertio
2. 3. 4. 5. 6. 7. 8	To measure radiation pa To measure and plot Mo To measure V-I characte To measure and verify p To measure and verify factor, insertion loss and To measure and verify loss and isolation in dB. To measure wavelength	ttern and gain of horn ode characteristics of R eristics of Gunn Diode oort characteristics of r o port characteristics l directivity. port characteristics of	or parabolic Reflex klystro and study of nicrowave tee of directiona f isolator and	n. PIN modulator. es (E, H, E-H or magic planes). Il coupler and calculate couplin circulator and calculate insertio
2. 3. 4. 5. 6. 7. 8.	To measure radiation pa To measure and plot Mo To measure V-I character To measure and verify p To measure and verify factor, insertion loss and To measure and verify loss and isolation in dB. To measure wavelength theoretical calculations.	ttern and gain of horn ode characteristics of F eristics of Gunn Diode oort characteristics of r oport characteristics d directivity. port characteristics of n of the microwave us	or parabolic a Reflex klystro and study of nicrowave tee of directiona f isolator and sing microwa	n. PIN modulator. es (E, H, E-H or magic planes). Il coupler and calculate couplin circulator and calculate insertion we test bench and verify with i
2. 3. 4. 5. 6. 7. 8. 9	To measure radiation pa To measure and plot Mo To measure V-I characte To measure and verify p To measure and verify factor, insertion loss and To measure and verify loss and isolation in dB. To measure wavelength theoretical calculations. To plot standing wave p	ttern and gain of horn ode characteristics of F eristics of Gunn Diode oort characteristics of r port characteristics I directivity. port characteristics of n of the microwave us	or parabolic Reflex klystro and study of nicrowave tee of directiona f isolator and sing microwa	n. PIN modulator. es (E, H, E-H or magic planes). Il coupler and calculate couplin circulator and calculate insertion we test bench and verify with in , short and matched termination
2. 3. 4. 5. 6. 7. 8. 9.	To measure radiation pa To measure and plot Mo To measure V-I character To measure and verify p To measure and verify factor, insertion loss and To measure and verify loss and isolation in dB. To measure wavelength theoretical calculations. To plot standing wave p microwave frequency us	ttern and gain of horn ode characteristics of F eristics of Gunn Diode oort characteristics of r o port characteristics d directivity. port characteristics of n of the microwave us pattern and measure S sing slotted section with	or parabolic Reflex klystro and study of nicrowave tee of directiona f isolator and sing microwa WR for open th probe carri	n. PIN modulator. es (E, H, E-H or magic planes). I coupler and calculate couplir circulator and calculate insertion ave test bench and verify with in , short and matched termination age.
2. 3. 4. 5. 6. 7. 8. 9. 10.	To measure radiation pa To measure and plot Mc To measure V-I characte To measure and verify p To measure and verify factor, insertion loss and To measure and verify loss and isolation in dB. To measure wavelength theoretical calculations. To plot standing wave p microwave frequency us Study the network analy	ttern and gain of horn ode characteristics of R eristics of Gunn Diode oort characteristics of r port characteristics directivity. port characteristics of n of the microwave us pattern and measure S sing slotted section with zer and carry out the r	or parabolic Reflex klystro and study of nicrowave tee of directiona f isolator and sing microwa WR for open th probe carri neasurements	n. PIN modulator. es (E, H, E-H or magic planes). I coupler and calculate coupling circulator and calculate insertion ave test bench and verify with i , short and matched termination age. s of s-parameters.
2.         3.         4.         5.         6.         7.         8.         9.         10.         11.	To measure radiation pa To measure and plot Mo To measure V-I characte To measure and verify p To measure and verify factor, insertion loss and To measure and verify loss and isolation in dB. To measure wavelength theoretical calculations. To plot standing wave p microwave frequency us Study the network analy To design and simulate	ttern and gain of horn ode characteristics of R eristics of Gunn Diode oort characteristics of r port characteristics directivity. port characteristics of n of the microwave us pattern and measure S sing slotted section with zer and carry out the r	or parabolic Reflex klystro and study of nicrowave tee of directiona f isolator and sing microwa WR for open th probe carri neasurements	n. PIN modulator. es (E, H, E-H or magic planes). I coupler and calculate couplir circulator and calculate insertion ave test bench and verify with in , short and matched termination age.
2.         3.         4.         5.         6.         7.         8.         9.         10.         11.	To measure radiation pa To measure and plot Mo To measure V-I characte To measure and verify p To measure and verify factor, insertion loss and To measure and verify loss and isolation in dB. To measure wavelength theoretical calculations. To plot standing wave p microwave frequency us Study the network analy To design and simulate	ttern and gain of horn ode characteristics of R eristics of Gunn Diode oort characteristics of r port characteristics directivity. port characteristics of n of the microwave us pattern and measure S sing slotted section with zer and carry out the r	or parabolic Reflex klystro and study of nicrowave tee of directiona f isolator and sing microwa WR for open th probe carri neasurements	n. PIN modulator. es (E, H, E-H or magic planes). I coupler and calculate coupling circulator and calculate insertion ave test bench and verify with i , short and matched termination age. s of s-parameters.
2. 3. 4. 5. 6. 7. 8. 9. 10. 11. Virtual I	To measure radiation pa To measure and plot Mo To measure V-I characte To measure and verify p To measure and verify factor, insertion loss and To measure and verify loss and isolation in dB. To measure wavelength theoretical calculations. To plot standing wave p microwave frequency us Study the network analy To design and simulate	ttern and gain of horn ode characteristics of F eristics of Gunn Diode oort characteristics of r port characteristics of directivity. port characteristics of n of the microwave us battern and measure S sing slotted section with yzer and carry out the r any type of microwave	or parabolic a Reflex klystro and study of nicrowave tee of directiona f isolator and sing microwa WR for open th probe carri neasurements e antenna usin	n. PIN modulator. es (E, H, E-H or magic planes). I coupler and calculate couplir circulator and calculate insertion ave test bench and verify with i , short and matched termination age. s of s-parameters. ng EM simulation software.
2.         3.         4.         5.         6.         7.         8.         9.         10.         11.         Virtual I	To measure radiation pa To measure and plot Mo To measure V-I character To measure and verify p To measure and verify factor, insertion loss and To measure and verify loss and isolation in dB. To measure wavelength theoretical calculations. To plot standing wave p microwave frequency us Study the network analy To design and simulate a Lab:	ttern and gain of horn ode characteristics of F eristics of Gunn Diode oort characteristics of r oport characteristics of directivity. port characteristics of n of the microwave us oattern and measure S sing slotted section with vzer and carry out the r any type of microwave	or parabolic a Reflex klystro and study of nicrowave tee of directiona f isolator and sing microwa WR for open th probe carri neasurements e antenna usin	n. PIN modulator. es (E, H, E-H or magic planes). I coupler and calculate couplir circulator and calculate insertion ave test bench and verify with in , short and matched termination age. s of s-parameters. ng EM simulation software.

	Subject: Cloud Computing				
-	List of Experiments (Any 6 to be performed)				
1.	Install Google App Engine. Create hello world app and other simple web applications using				
	python / java.				
2.	Use GAE launcher to launch the web applications.				
3.	Simulate a cloud scenario using CloudSim and run a scheduling algorithm that is not				
	present in CloudSim.				
4.	Find a procedure to transfer the files from one virtual machine to another virtual machine.				
5.	Find a procedure to launch virtual machine using try stack (Online Openstack Demo				
	Version)				
6.	Design and deploy a PaaS environment.				
7.	Design and develop custom Application (Mini Project) using Cloud (like				
	Salesforce/GCP/AWS.)				
8.	Design an Assignment to retrieve, verify, and store user credentials using Firebase				
	Authentication, the Google App Engine standard environment, and Google Cloud Data				
	store.				
	Case Studies (Any 2 to be performed)				
1.	Data storage security in private cloud.				
2.	Application of IoT / Ubiquitous based on cloud.				
3.	Tools for building private cloud.				
4.	Instance creation in cloud environment.				

	Sa	witribai Phule Pu	ine University	
Fourth Year of E & Tc Engineering (2019 Course) 404187: Lab Practice – 2				
Pract	Practical: 04 Hrs. / Week 02 Term Work: 25 Marks			
			Practical: 50 Marks	
Compa	nion Course, if any:			
1.	VLSI Design and Technol	ogy		
2.	Speech Processing (Electi	ve - III)		
3.	PLC SCADA and Automa	·		
	JAVA Script (Elective - II			
	Embedded System and RT	, ,		
6.	Modernized IoT (Elective	- III) delines for Studer		
Theory	, Description of data used, Gui al examination will be base can be used by the interna	Results, Conclusion. <b>Idelines for Lab</b> /' ed on the work carrie I & external examine	ed out by the student in the Lab course. Suitable	
Part	A: To write VHDL cod	le, simulate with t	est bench, synthesis, implement on PLD	
	(Any 5 to be perfo			
1.	4 bit ALU for Add, Sub	tract, AND, NAND, O	OR, XOR & XNOR.	
2.	-	with mode selection in	nput for SISO, SIPO, PISO, & PIPO.	
3.	Mod - N Counter			
<u>4.</u> 5.	FIFO memory LCD Interface			
5. 6.	Keypad interface			
	•1	avout in selected t	echnology, simulate with & without	
		-	Ill times. (Any 3 to be performed)	
1.	Inverter, NAND, NOR			
2.	Half Adder & Full Adde			
3.	2:1 Mux using logic gate	es & transmission gat	es	
4.	One bit SRAM Cell			
	al Lab:			
	https://vlsi-iitg.vlabs.ac.in		ign Lab.)	
2.	https://cse14-iiith.vlabs.a	(v L SI L a D.)		

	Subject: Speech Processing (Elective - III)
NOT	E:
1.	To perform the experiments software like Python, SCILAB, OCTAVE or any
	appropriate open source software can be used.
2	For analysis of speech signals tools like PRAAT, Audacity, WAVESURFER,
2.	WEKA can be used.
	Part A (Any 7 to be performed)
1.	Record speech signals (isolated words, continuous speech) and analyse the speech signal using
1.	speech analysis tool (e.g. PRAAT). Observe spectrogram, pitch, formants, intensity etc.
2.	Write a program for extracting pitch period for a voiced part of the speech signal using
	autocorrelation method and average magnitude difference function (AMDF).
3.	Write a program to compute short time Energy and ZCR for different frame rates and comment
	on the result.
4.	Write a program to classify voiced, unvoiced and silence frames using frame level energy and
	zero crossing rate.
5.	Write a program to compute narrow band and wide band spectrogram. Comment on the time
	and frequency resolution of wide band and narrow band spectrogram.
6.	Write a program to design a Mel filter bank and using this filter bank write a program to
	extract MFCC features.
7.	Write a program to perform the cepstral analysis of speech signal and detect the pitch from the
	voiced part using cepstrum analysis.
8.	Write a program to enhance the noisy speech signal using spectral subtraction method.
9.	Write a program to extract frequency domain audio features like SC, SF and Spectral roll off.
	Part B (Any 1 to be performed)
1.	Write a program for Automatic Speech Recognition using Convolutional Neural Networks
	(CNN) or Recurrent Neural Networks (RNN).
2.	Write a program for Text to Speech synthesis using Convolutional Neural Networks (CNN) or
	Recurrent Neural Networks (RNN).
Virtua	al Lab:
1.	https://ssp-iiith.vlabs.ac.in/Introduction.html
2.	https://vlab.amrita.edu/index.php?sub=59&brch=164
Speec	h database:
http://	festvor org/databases/jijit_vojess/
<u>mup.//</u>	<u>festvox.org/databases/iiit_voices/</u>

	Subject: PLC SCADA and Automation (Elective - III)
	Part A (Any 5 to be performed)
1.	Implementation of Logic Gates Using PLC(Software/Hardware Implementation).
2.	Development of a ladder program for DOL Starter.
3.	Implementation of Boolean Expression using PLC(Software/Hardware Implementation).
4.	Traffic Light Control using PLC (Any Application of Timer using PLC will be accepted) (Software/Hardware Implementation).
5.	Counting Objects (Any Application of Counter using PLC will be accepted) (Software/Hardware Implementation).
6.	Interfacing of Encoder with PLC to control a particular application.
7.	Interfacing of Limit Switch/ Proximity Switch/or any sensor/sensors with PLC to control a particular application.
	Part B (Any 2 to be performed)
1.	Interfacing of RTD with PLC for Temperature control application.
2.	Motor speed control using PLC and VFD.
3.	Pneumatic Trainer Kit/Hydraulic Trainer Kit control using PLC.
4.	Close Loop control using PID Controller (Any One Parameter Like Temperature, Flow, Pressure, Level)
	Part C (Any 1 to be performed)
1.	Any Example Using SCADA.
2.	Study of Hardware and Software Platform for DCS
	https://ial-coep.vlabs.ac.in/exp/software-platforms-dcs/procedure.html
3.	PLC controlled Case study- 1:
	[Faculty will give (or students will choose) one problem statement to a group of 2/3 students. Students will develop a program and simulate it on their own]
	Suggested case studies (Not Limited to)
	a. Bottle Filling Plant using PLC
	b. Operation of Lift (Elevator) using PLC
	c. PLC based Gas Detection System using Ladder Logic Project
	d. Alarm Management Systems using PLC
	e. Water Distribution System using PLC
Virtua	ll Lab:
1.	http://plc-coep.vlabs.ac.in/ (Programmable Logic Controller Lab.)
2.	http://ial-coep.vlabs.ac.in/List%20of%20experiments.html (Industrial Automation Lab.)

	Subject: JAVA Script (Elective - III)			
	Part A (Compulsory)			
1.	Write a JavaScript program to calculate area of triangle, area of rectangle and area of circle			
2.	Write a JavaScript program to generate the multiplication table of a given number.			
3.	Write a JavaScript program to following operations on a given string,			
	Reverse string			
	• Replace characters of a string.			
	• String is Palindrome.			
4.	Write a JavaScript program to compare two strings using various methods.			
5.	Write a JavaScript program that will create a countdown timer.			
	Part B (Any 2 to be performed)			
1.	Write a JavaScript program that will create an array and perform following operations			
	• To remove specific element from the array.			
	• Check if an array contains a specified value.			
2	• To empty an array			
2.	Write a JavaScript program that will append an object to an array and will check if an object is			
3.	an array. Write a JavaScript program to illustrate different Set operations like-			
5.	Union			
	Intersection			
	• Difference			
	Set Difference			
	Part C (Any 2 to be performed)			
1.	Write a JavaScript program to create a Home page of any website and change background			
	color using			
	<ul><li>On mouse over event</li><li>On focus event</li></ul>			
2.	Create a student information Form to accept information like Name, Address, City, State			
2.	Gender, Mobile Number, and email id. Perform validations for:			
	Correct Names			
	Mobile Names			
	• Email I.D.'s			
	• If no entered value			
	• Re-display for wrongly entered values with message			
	Congratulation and Welcome page upon successful entries			
3.	Design and implement a simple calculator using Java script for operations like addition			
	multiplication, subtraction, division, square of a number etc:			
	• Design a calculator like text field for input and output, buttons for numbers			
	<ul><li>and operations etc.</li><li>Validate input values</li></ul>			
	<ul> <li>Validate input values</li> <li>Prompt / Alerts for invalid values etc.</li> </ul>			
Virtu	al Lab:			
1.	https://cse02-iiith.vlabs.ac.in/List%20of%20experiments.html (Computer Programming			
	Lab.)			

	Subject: Embedded System and RTOS (Elective - III)			
	Part A (Any 4 to be performed)			
NOT	NOTE: Practicals from 1 to 5 in Group A can be performed using $\mu$ COS -II / Free			
	RTOS on ARM 7 / ARM Cotex – M / Arduino			
1.	Multitasking in $\mu COS$ II RTOS using minimum 3 tasks on ARM7/ ARM Cortex- M.			
2.	Semaphore as signaling & Synchronizing on ARM7/ ARM Cortex- M.			
3.	Mailbox implementation for message passing on ARM7/ ARM Cortex- M.			
4.	Queue implementation for message passing on ARM7/ ARM Cortex- M.			
5.	Implementation of MUTEX using minimum 3 tasks on ARM7/ ARM Cortex- M.			
6.	Porting of linux operating system on ARM9/ARM Cortex-M.			
	Part B (Any 4 to be performed)			
1.	Interfacing sensors and actuators with Arduino Uno- Door opener using Ultrasonic sensor and servo motor.			
2.	Weather Station- Build a cloud-ready temperature and Humidity sensor (DHT-11/22) with the Node MCU and the any IoT Platform.			
3.	IoT based Wireless Controlled Home Automation using ESP8266.			
4.	Interfacing of 4 LED bank with Raspberry Pi to blink.			
5.	Interfacing Sensors and actuators with Raspberry Pi- Hand gesture robot.			
Virtual Lab:				
1.	https://docs.simuli.co/getting-started/arduino/arduino-ide-and-vlab			
2.	https://docs.simuli.co/getting-started/raspberry-pi/setting-up-iotify-virtual-lab			

Subject: Modernized IoT (Elective – III)		
List of Experiments		
1.	Study of Raspberry-Pi, Beagle board, Arduino, and different operating systems for Raspberry-	
	Pi/Beagle board/Arduino. Understanding the process of OS installation on Raspberry-	
	Pi/Beagle board/Arduino	
2.	Open-source prototype platform- Raspberry-Pi/Beagle board/Arduino -Simple program digital	
	read/write using LED and Switch -Analog read/write using sensor and actuators.	
3.	Interfacing sensors and actuators with Arduino/Raspberry-pi.	
4.	IoT based Stepper Motor/DC Motor Control with Arduino/Raspberry Pi.	
5.	Introduction to MQTT/ CoAP and sending sensor data to cloud using Raspberry-Pi/Beagle	
	board/Arduino.	
6.	Get the status of a bulb at a remote place (on the LAN) through web.	
7.	Interfacing Arduino to Bluetooth Module	
8.	Communicate between Arduino and Raspberry PI using any wireless medium like ZigBee	
9.	IoT based small project implementation on the topics based on small problem statements of	
	the fields like chat bot, smart home (Home Automation), social issues and environmental	
	issues etc. This project can be built on any IoT simulation platform like Tinkercad, Cooja etc.	

## Savitribai Phule Pune University

# Fourth Year of E & Tc Engineering (2019 Course)

404188: Project Phase – I				
Teaching Scheme:	Credit	Examination Scheme:		
Practical: 02 Hrs. / Week	01	Term Work: 50 Marks		

**Course Objectives:** 

- To understand the basic concepts & broad principles of projects.
- To understand the value of achieving perfection in project implementation & completion.
- To apply the theoretical concepts to solve real life problems with teamwork and Multidisciplinary approach.
- To demonstrate professionalism with ethics; present effective communication skills and relate engineering issues to broader societal context.

#### **Course Outcomes:**

**CO1: Demonstrate** a sound technical knowledge in field of E&TC in the form of project.

CO2: Undertake real life problem identification, formulation and solution.

**CO3: Design** engineering solutions to complex problems utilizing a systematic approach.

**CO4: Demonstrate** the knowledge, effective communication skills and attitudes as professional engineer.

Project phase 1 is an integral part of the project work. The project work shall be based on the knowledge acquired by the student during the graduation and preferably it should meet and contribute towards the needs of the society. The project aims to provide an opportunity of designing and building complete system or subsystems in the field of Electronics and communication where the student likes to acquire specialized skills. The student shall prepare the duly certified Fourth report of project work in standard format for satisfactory completion of the work by the concerned guide and head of the Department/Institute.

#### **Guidelines:**

- 1. Group Size: The student shall carry the project work individually or by a group of students. Optimum group size shall be 3 students. However, if project complexity demands a maximum group size of 4 students, the project committee should be convinced about such complexity and scope of the work. Projects selected should meet and contribute towards the needs of the society.
- 2. Selection and approval of topic: Topic should be related to real life application in the field of Electronics and Telecommunication engineering.
- 3. The topic may be based on : Investigation of the latest development in a specific field of Electronics or Communication / The investigation of practical problem in manufacture and / or testing of electronics or communication equipment/ Software based projects related to VHDL, Communication, Instrumentation, Signal Processing agriculture Engineering etc. with the justification for techniques used / any topic in the field of E&TC may be allowed.
- 4. Interdisciplinary projects should be encouraged. The examination of Interdisciplinary projects shall be conducted independently in respective departments.
- 5. The term work assessment of project phase 1 shall be based on Innovative Idea of selected project, literature survey, Depth of understanding, Applications, Individual contributions, presentation, project report, timely completion of work.
- 6. The department should prepare project planner and should follow accordingly
- 7. A log book of work carried out during the semester should be maintained with weekly review remarks by the guide and committee.
- 8. A certified copy of report preferably using LATEX is required to be presented to external examiner at the time of Fourth examination.
- The project report must undergo by plagiarism check and the similarity index must be less than 15%. The plagiarism report should be included in the project report.

Savitribai Phule Pune University					
Fourth Year of E & Tc Engineering (2019 Course)					
404	404189: Mandatory Audit Course - 7				
Teaching Scheme:	Teaching Scheme:CreditExamination Scheme:				

#### **GUIDELINES FOR CONDUCTION OF AUDIT COURSE**

In addition to credits courses, it is mandatory that there should be audit course (non-credit course) from second year of Engineering. The student will be awarded grade as AP on successful completion of audit course. The student may opt for two of the audit courses (One in each semester). Such audit courses can help the student to get awareness of different issues which make impact on human lives and enhance their skill sets to improve their employability. List of audit courses offered in the semester is provided in the curriculum. Student can choose one of the audit course from list of courses mentioned. Evaluation of audit course will be done at institute level.

The student registered for audit course shall be awarded the grade AP and shall be included such grade in the Semester grade report for that course, provided student has the minimum attendance as prescribed by the Savitribai Phule Pune University and satisfactory in-semester performance and secured a passing grade in that audit course. No grade points are associated with this 'AP' grade and performance in these courses is not accounted in the calculation of the performance indices SGPA and CGPA. Evaluation of audit course will be done at institute level itself.

#### Selecting an Audit Course:

#### **Using NPTEL Platform:**

NPTEL is an initiative by MHRD to enhance learning effectiveness in the field of technical education by developing curriculum based video courses and web based e-courses. The details of NPTEL courses are available on its official website <u>www.nptel.ac.in</u>

- Student can select any one of the courses mentioned above and has to register for the corresponding online course available on the NPTEL platform as an Audit course.
- Once the course is completed the student can appear for the examination as per the guidelines on the NPTEL portal.

• After clearing the examination successfully; student will be awarded with certificate.

#### Assessment of an Audit Course:

- The assessment of the course will be done at the institute level. The institute has to maintain the record of the various audit courses opted by the students. The audit course opted by the students could be interdisciplinary.
- During the course students will be submitting the online assignments. A copy of same students can submit as a part of term work for the corresponding Audit course.
- On the satisfactory submission of assignments, the institute can mark as "Present" and the student will be awarded the grade AP on the marksheet.

# **SEMESTER - VIII**

4041 Teaching Scheme: Theory: 03 Hrs. / Week	90: Fiber Optic Credit 03	Communication Examination S	cheme:			
		Examination S	cheme:			
Theory: 03 Hrs. / Week	02	Teaching Scheme:CreditExamination Scheme:				
	03	In-Sem (Theory): 30	0 Marks			
		End Sem (Theory): 70	) Marks			
Prerequisite Courses, if any: 1. Digital Communication 2. Electromagnetics Field The Companion Course, if any:	ory					
1. Fiber Optic Lab						
<ol> <li>communication systems.</li> <li>To study the impact of choires.</li> <li>To introduce students to the 4. To extend the fundamentalistic for the fundamentalistic for the mean optical networks.</li> </ol>	e WDM component s to design and anal asurement standard	s and their role in capacity upg ysis of fiber optic communicat s, specifications and state of a	ion links.			
Course Outcomes: On completion						
CO1: Explain the working of com CO2: Calculate the important par- telecommunication systems	ameters associated					
CO3: Compare and contrast the p CO4: Evaluate the performance v analysis. CO5: Design digital optical link	erformance of majo iability of optical li	nks using the power and rise ti	C			
simulation tools.	by proper selection	on or components and check	its viuoliity using			
CO6: Compile technical information and current technological tree knowledge.		of art components, standards, since online resources to update th				
	Course Co	ontents				
Unit I C		or Telecommunication	8 Hrs.			

aperture, fiber types, mode theory for circular waveguides: overview of modes & key modal concepts (V number, number of modes, power in clad), single mode fibers, cutoff wavelength

**Transmission characteristics of optical fibers: attenuation** - material absorption, scattering losses, fiber bend loss, loss due to fiber misalignment, splices and connectors; **signal distortion** - intermodal delay, intramodal dispersion or chromatic dispersion, modal delay, bit rate-distance product, plot of material & waveguide dispersions for standard single mode, dispersion shifted and dispersion flattened fibers; optical fibers for 5G networks, comparison.

	CO1: Explain the working of components and measurement ecoptical fiber networks.	uipments in	
Mapping of Course Outcomes for Unit I	CO2: Calculate the important parameters associated with optical components used in fiber optic telecommunication systems.		
	CO3: Compare and contrast the performance of major compo- links.	nents in optical	
Unit II	Optical Sources	7 Hrs.	

**Optical Sources:** Introduction, wavelength and material consideration (direct & indirect bandgap semiconductors); requirements from optical sources for telecommunication.

**LED:** principle of working, quantum efficiency, optical output power characteristics, spectral width, effect of temperature on characteristics, modulation bandwidth, analog modulation, digital modulation, LED analog transmitter;

**Semiconductor Laser Diodes:** absorption, spontaneous emission, stimulated emission, concept of population inversion and optical feedback, output power characteristics of LASER; Bias point and amplitude modulation range for analog applications of LEDs & laser diodes, comparison of LEDs & Lasers.

	CO1: Explain the working of components and measurement equipments in optical fiber networks.			
Mapping of Course Outcomes for Unit II:	I fill / fill / fight the important norometers associated with ontical component			
	CO3: Compare and contrast the performance of major components in optic links.			
Unit III	Photodetectors	6 Hrs.		
Introduction, requirement	nts from optical detectors, material considerations, types: p-n	, pin, Avalanche		
photodiode, photo tran	sistor, principle of working, quantum efficiency, responsiv	vity, long cutoff		

photodiode, photo transistor, principle of working, quantum efficiency, responsivity, long cutoff wavelength, detector response time, comparison of photodetectors, thermal noise, dark current noise, quantum noise and receiver sensitivity, bit error rate

1	5,	
	CO1: Explain the working of components and measurement e optical fiber networks.	equipments in
Mapping of Course Outcomes for Unit III	CO2: Calculate the important parameters associated with optused in fiber optic telecommunication systems.	cical components
	CO3: Compare and contrast the performance of major complinks.	onents in optical
Unit IV	Fiber Optic Link Design & WDM Systems	8 Hrs.

**Point to point optical link:** Choice of components, system design considerations, optical power budget, rise time budget, bit rate for RZ and NRZ pulse format. Optical system design and performance analysis using software tools.

**WDM Concepts & Components:** Overview of WDM, WDM components: 2 x 2 fiber coupler, isolator, circulator, basics of fiber grating filters, optical add/drop multiplexer, architecture of optical amplifiers (SOA, EDFA & FRA), Noise figure, OSNR & system impact of ASE.

	<ul> <li>CO1: Explain the working of components and measurement equipments in optical fiber networks.</li> <li>CO4: Evaluate the performance viability of optical links using the power and rise time budget analysis.</li> </ul>		
Mapping of Course Outcomes for Unit IV			
	CO5: Design digital optical link by proper selection of compon its viability using simulation tools.	ents and check	
Unit V	<b>Optical Networks</b>	7 Hrs.	
	<b>ptts</b> : fundamentals, network terminology, desirable properties		
-	network topology types, advantages of optical network.	, elements of an	
	Networks: FDDI, SONET/SDH, FTTX, FTTP, FTTH, PON	I CPON Long	
-	omarine optical networks, role of fiber optic network in the 5G		
	is, standards and challenges.	networks.	
	CO6: Compile technical information related to the state of art	components.	
Mapping of Course Outcomes for Unit V	standards, simulation tools and current technological trends by accessing		
	the online resources to update their domain knowledge.		
<b>Unit VI</b>	<b>Optical Fiber Measurements</b>	<b>6 Hrs.</b>	
<b>Overview of Measurem</b>	ent Standards for fiber optics:		
Test Equipments for f	ield work: Test support lasers, visual fault indicator, optic	al power meter	
Optical Time Domain R	eflectometry (OTDR), optical spectrum analyzer (OSA), BER	test equipment	
Measurements: measur	ement of: optical power, numerical aperture of fiber, fiber atte	nuation (cutback	
method, insertion loss m	ethod, OTDR), macrobending loss, fiber dispersion		
System performance ev	valuation: Eye Diagram Test, study of OTDR.		
	CO1: Explain the working of components and measurement equipation optical fiber networks.	uipments in	
Mapping of Course Outcomes for Unit VI	CO6: Compile technical information related to state of art components, standards, simulation tools and current technological trends by accessing the online resources to update their domain knowledge.		
	Learning Resources		
Text Books:			
	ical Fiber Communications " 4 <sup>th</sup> Edition, Tata McGraw Hill. Optical Fiber Communications " 2 <sup>nd</sup> Edition, PHI.		
<b>Reference Books:</b>			
1 Diofor V Mumboo	v and Lowell L Scheiner, "Fiber Optic Communications	Technology" 1	

- Edition, Pearson Education. Uyless Black, "Optical Networks- Third Generation Transport Systems", Pearson Education.
   Govind P Agrawal, "Fiber Optic Communication Systems", 3<sup>rd</sup> Edition, Wiley India.
- 4. Fredrick C Allard, "Fiber Optics Handbook for Engineers & Scientists", MH International

#### **MOOC / NPTEL Courses:**

1. NPTEL Course on "Advanced Optical Communication", by Prof R K Shevgaonkar, IIT Madras

Link of the Course: https://nptel.ac.in/courses/117101002

- NPTEL Course on "Fiber Communication Technology", by Prof Deepa Venkitesh, IIT Madras Link of the Course: <u>https://nptel.ac.in/courses/108106167</u>
- NPTEL Course on "Fiber- Optic Communication Systems & Techniques", by Dr Pradeep Kumar K, IIT Kanpur Link of the Course: <u>https://nptel.ac.in/courses/108104113</u>

Fourt	n vograt H Af In H	ngineering (2019 Course)			
404191 (A): Biomedical Signal Processing (Elective - V)					
<b>Teaching Scheme:</b>	Credit	Examination Sc	cheme:		
<b>Fheory: 03 Hrs. / Wee</b>	k 03	In-Sem (Theory): 30	Marks		
		End-Sem (Theory): 70	Marks		
Prerequisite Courses, if an	y:				
1. Digital Signal Proce					
Companion Course, if any	:				
1. Lab Practice -3					
Course Objectives:	sic biomedical signals.				
	e	t commonly used biomedical signation	ale including		
	potentials, and EMG.	commonly used biomedical signa	ars, including		
, ,	<b>1</b> '	ssing of physiclogical signals			
		ssing of physiological signals.	da within the		
-	on of meaningful morms	ation to identify patterns and trend	us within the		
signals.	wasses and shows staristics	of noise and artifacts in his sizes	1.		
Course Outcomes: On com		s of noise and artifacts in bio signa			
CO2: Analyze ECG Signal CO3: Explain Processing o	f EEG signals for Disea	ningful information ases of Central Nervous System			
CO2: Analyze ECG Signal CO3: Explain Processing o CO4: Analyze EMG signa CO5: Analyze various Bior	s with extraction of mean f EEG signals for Disea ils for understanding Neu nedical Signals	ningful information ases of Central Nervous System			
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CO2: Analyze ECG Signal CO3: Explain Processing o CO4: Analyze EMG signa CO5: Analyze various Bior	s with extraction of mean f EEG signals for Disea ils for understanding Neu nedical Signals al signals to remove ada Course C	ningful information ases of Central Nervous System uromuscular Diseases ptive interference and noise Contents	7 Hrs.		
CO2: Analyze ECG Signals CO3: Explain Processing of CO4: Analyze EMG signa CO5: Analyze various Bior CO6: Process the biomedic Unit I	s with extraction of mean f EEG signals for Disea ils for understanding Neu nedical Signals al signals to remove ada Course C Introduction	ningful information ases of Central Nervous System uromuscular Diseases ptive interference and noise Contents to Biomedical Signals			
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CO2: Analyze ECG Signals CO3: Explain Processing of CO4: Analyze EMG signal CO5: Analyze Various Bion CO6: Process the biomedic Unit I Introduction and Overview Characteristics of Cell Men Equivalent Electric Circuit Potential as a Wave , Some	s with extraction of mean f EEG signals for Disea ils for understanding Neu nedical Signals al signals to remove ada <b>Course C</b> Introduction to , Ion Transport in Biol abrane, Membrane Resist t, Action Potential, Ele Practical Considerations	ningful information ases of Central Nervous System uromuscular Diseases ptive interference and noise <b>Contents</b> to Biomedical Signals logical Cells, Trans membrane F stance, Membrane Capacitance , G ectric Data Acquisition, Propaga on Biomedical Electrode Summa	Potential, Electr Cell Membrane ation of Electr		
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Frequency-Domain Analysis, Wavelet-Domain Analysis

MappingofCourseCO 2: Analyze ECG Signalsfor extraction of meaningful informationOutcomes for Unit II		
Unit III	Neurological Signal Processing	<b>7 Hrs.</b>
Brain and Its Functions		

Brain and its Functions

Electroencephalogram: Signal of the Brain- EEG Frequency Spectrum, Significance of EEG,

Evoked Potentials- Auditory-Evoked Potentials, Somatosensory-Evoked Potentials, Visual-Evoked Potentials, Event-Related Potentials, Diseases of Central Nervous System and EEG- Epilepsy, Sleep Disorders, Brain Tumor Processing and Feature Extraction of EEG- Sources of Noise on EEG, Frequency-Domain Analysis, Time-Domain Analysis, Wavelet-Domain Analysis

Mapping of Course Outcomes for Unit III	CO 3: Explain use of EEG signals for Diseases of Centra System.	EEG signals for Diseases of Central Nervous	
Unit IV	Electromyogram (EMG)	<b>7 Hrs.</b>	

Muscle- Motor Unit, Muscle Contraction, Muscle

EMG: Signal of Muscles- Significance of EMG

Neuromuscular Diseases and EMG- Abnormal Enervation, Pathological Motor Units,

Neuromuscular Transmission in Motor Units, Defects in Muscle Cell Membrane

Processing and Feature Extraction of EMG- Sources of Noise on EMG, Time-Domain Analysis, Frequency- and Wavelet-Domain Analysis

Mapping of Course Outcomes for Unit IV		CO 4: Analyze EMG Diseases.	signals for understanding Neuro	muscular

Unit V	Other Biomedical Signals	6 Hrs.
Introduction and Overvi	iew, Blood Pressure and Blood Flow, Electrooculogram, Res	piratory Signals

Magneto encephalogram.

0 1 0	,	
Mapping of Course	CO5: Analyze the various Biomedical Signals.	
<b>Outcomes for Unit V</b>		

Unit VI	Adaptive interference / Noise Cancellation	6 Hrs.
C-mag of mains in his si	an also	

#### Types of noise in bio signals:

Digital filters: IIR and FIR, Notch filters, Optimal and adaptive filters, Weiner filters.

LMS adaptive algorithm, Steepest descent algorithm

Adaptive noise canceller: Cancellation of 50 Hz signal in ECG

	CO6: Process the biomedical signals to remove adaptive interference		
<b>Outcomes for Unit VI</b>	and noise.		
	Learning Resources		
Text Books:			

- 1. Kayvan Najarian, Robert Splinter, "Biomedical Signal and Image Processing", 2<sup>nd</sup> Edition, CRC Press
- 2. R. Rangayan, "Biomedical Signal Analysis", Wiley

**Reference Books:** 

- 1. R.S.Khandpur, "Handbook of Biomedical Instrumentation", 2<sup>nd</sup> Edition, Tata McGraw Hill,
- 2. C.Reddy "Biomedical Signal Processing: Principles and techniques", Tata McGraw Hill.
- 3. Joseph J. Carr and John M. Brown, "Introduction to Biomedical Equipment Technology", 4<sup>th</sup> Edition, Prentice Hall.

## **MOOC / NPTEL Courses:**

1. NPTEL Course on "**Biomedical Signal Processing**", by Prof Sudipta Mukhopadhyay, IIT Kharagpur

Link of the Course: <u>https://nptel.ac.in/courses/108105101</u>

	Savitribai Phule Pune University					
Fou	urth Year of E & 7	fc Engineering (	2019 Course	e)		
	Fourth Year of E & Tc Engineering (2019 Course) 404191 (B): Industrial Drives & Control (Elective - V)					
Teaching Schem	Teaching Scheme:CreditExamination Scheme:					
Theory: 03 hrs. / we	Theory: 03 hrs. / week03In-Sem (Theory): 30 Marks					
		End-Sei	m (Theory):	70 Marks		
<ul> <li>Prerequisite Courses, if any: <ol> <li>Basic Electrical Engineering</li> <li>Electronic Circuits</li> <li>Electrical Circuits</li> <li>Power Devices and Converters</li> </ol> </li> <li>Companion Course, if any: <ol> <li>Lab Practice -3</li> </ol> </li> <li>Course Objectives: <ol> <li>To introduce components of electrical drives and its parameters .</li> <li>To understand working, design and performance analysis of DC motor drives, Induction motor and stepper motor drives.</li> <li>To know various protections circuit required for motor drives.</li> </ol> </li> <li>Course Outcomes: On completion of the course, learner will be able to - <ol> <li>CO1: Understand significance and design of various components of electrical drives.</li> <li>CO2: Develop, evaluate and analyze the performance of DC motor drives.</li> <li>CO3: Design, estimate and examine the performance of chopper controlled DC drives.</li> <li>CO4: Adapt, choose and categorize performance of PWM inverter drives for Induction motors.</li> </ol></li></ul>						
CO5: Elaborate, interpre CO6: Develop, explain a		-		ive.		
	Cou	rse Contents				
Unit I	Compor	nents of Electrica	al Drives	6 Hrs.		
Electric machines, Power converter, Controllers, Dynamics of electric drive - torque equation -         equivalent values of drive parameters- components of load torques types of load – four-quadrant         operation of a motor – steady state stability – load equalization – classes of motor duty determination         of motor rating.         Mapping of Course         Outcomes for Unit I         CO1: Understand significance and design of various components of electrical drives.						
Unit II	D	C Motor Drives		6 Hrs.		
I			nent magnet m			
DC motors & their performance (shunt, series, compound, permanent magnet motor, universal motor, dc servomotor) – braking – regenerative, dynamic braking, plugging –Transient analysis of separately excited motor – converter control of dc motors – analysis of separately excited & series with 1-phase and 3-phase converters ,soft start and field failure protection in DC drives, BLDC motor drive Mapping of Course CO2: Develop, evaluate and analyze the performance of DC motor						
Outcomes for Unit II	drives.					

Unit III	<b>Chopper Controlled DC Drives</b>	6 Hrs.			
Closed loop control – transfer function of self, separately excited DC motors – linear transfer function					
model of power converters – sensing and feeds back elements – current and speed loops, P, PI and					
PID controllers – respon	se comparison – simulation of converter and chopper fed DC of	drive			
Mapping of Course	CO3: Design, estimate and examine the performance of c	hopper			
Outcomes for Unit	Controlled DC drives.				
III					
Unit IV	<b>PWM Drives for Induction Motors</b>	6 Um			
		6 Hrs.			
-	rotor resistance control – slip torque characteristic – torque eq				
	ower recovery scheme – torque equation – torque slip charac	-			
synchronous speed opera	proving power factor – limited sub synchronous speed op ation	peration – super			
Mapping of Course	CO4: Adapt, choose and categorize performance of PWM	l inverter			
Outcomes for Unit IV	drives for Induction motors.				
Unit V	Synchronous Motor Drives	6 Hrs.			
Synchronous motor drive	es – speed control of synchronous motors – adjustable frequen				
-	inciples of synchronous motor control – voltage source inverte				
open loop control					
Mapping of Course	CO5: Elaborate, interpret and analyze the performance o	f Synchronous			
<b>Outcomes for Unit V</b>	motor drive.	•			
Unit VI	Stepper Motors	6 Hrs.			
Constructional features,	principle of operation, modes of excitation, single phase s	stepping motors,			
torque production in vari	able Reluctance (VR) stepping motor, Dynamic characteristic	s, Drive systems			
-	op control, Closed loop control of stepping motor, micro	processor based			
controller.					
Mapping of Course Outcomes for Unit VI	CO6: Develop, explain and examine performance of stepp	er motor			
	control.				
	Looming Descurres				
Text Books:	Learning Resources				
1. R. Krishnan, "Electrical Motor Drives: Modeling, Analysis, and Control", PHI					
2. G. K.Dubey, "Fundamentals of Electrical Drives", Narosa Publishers					
<b>Reference Books:</b>					
1. K.Dubey, "Power Semiconductor Controlled Drives", Prentice Hall.					
2. S.A. Nasar, Bold	2. S.A. Nasar, Boldea, "Electrical Drives", 2 <sup>nd</sup> Edition, CRC Press.				
3. M. A. ElSharkawi, "Fundamentals of Electrical Drives", Thomson Learning.					
4. W. Leohnard, "Control of Electric Drives", Springer.					
	rnbull, "Power Electronic Control of AC motors", Pergamon F				
6. Vedam Subrahmaniam, "Electric Drives: Concepts and Applications", McGraw Hill					

#### **MOOC / NPTEL Courses:**

- NPTEL Course on "Power Electronics", Prof. D.Prasad, Prof. N.K. De, Dr. D.Kastha, Prof. Sabyasachi Sengupta, IIT Kharagpur Link of the Course: <u>https://nptel.ac.in/courses/108105066</u>
- 2. NPTEL Course on "**Power Electronics**", Prof. G.Bhuvanseshwari, IIT Delhi Link of the Course: <u>https://nptel.ac.in/courses/108102145</u>
- NPTEL Course on "Advanced Power Electronics and Control", Prof. Avik Bhattacharya, IIT Roorkee Link of the Course: <u>https://nptel.ac.in/courses/108107128</u>
- NPTEL Course on "Industrial Drives: Power Electronics", Prof. K.Gopakumar, IISc Bangalore Link of the Course: <u>https://nptel.ac.in/courses/108108077</u>

	Savitribai Phule	Pune University				
Fourt	n Year of <mark>E &amp; Tc E</mark> i	n <mark>gineering</mark> (2019 Cour	se)			
40419	404191 (C): Android Development (Elective - V)Teaching Scheme:CreditExamination Scheme:					
Teaching Scheme:						
Theory: 03 hrs. / week	03	In-Sem (Theory):	30 Marks			
		End-Sem (Theory	): 70 Marks			
Prerequisite Courses, if an	<b>v:</b>					
1. Object Oriented Prog	•					
Companion Course, if any	:					
Course Objectives:						
1. To understand the A	ndroid Operating System	n.				
2. To study Android Ap	ops Development Cycle.					
3. To learn to create Ar	ndroid Applications.					
Course Outcomes: On com	pletion of the course, le	arner will be able to -				
CO1: Describe the process	of developing mobile ap	plications.				
CO2: Create mobile application	ations on the different a	ndroid platform.				
CO3: Design and implement	t mobile applications in	volving data storage in data	bases.			
	Course (	Contents				
Unit IIntroduction to JAVA and Android5 Hrs.						
Overview of Java, XML and SQL, History of Android, Android Stack, Android Project Structure,						
Android OS, Features of A	Android, Android Arch	itecture and building block	ks, Android App build			
process, Android UI- resour	ces, themes, threads etc	,				
Mapping of Course CO Outcomes for Unit I	<b>D1: Describe the proce</b>	ss of developing mobile ap	plications.			
Unit II	Introdu	cing Android	5 Hrs.			
SDK Overview, Android E	mulator, Android Instal	lation, setting up developn	nent environment using			
Eclipse/ Android Studio, E	DMS, Activity Lifecy	cle, Manifest File, Locales	s, Drawable, Listeners,			
Supporting Multiple Screens	5.					
Mapping of Course C(	01: Describe the proce	ss of developing mobile ap	plications.			
Outcomes for Unit II						
Unit III		olication Structure	8 Hrs.			
Android basic building blo	cks: Activities, Service	es, Broadcast Receivers &	Content providers, UI			
Components - Views & n	otifications, Componen	nts for communication -In	tents & Intent Filters,			
Android API levels (version						
Dalvik Virtual Machine &	-	· ·	, Layouts & Drawable			
Resources, Activities and Activity lifecycle, First sample Application.						
Mapping of CourseCO2: Create mobile applications on the different android platform.Outcomes for UnitIII						

<b>Unit IV</b>	Activities, Fragments, Intents and Android	<b>8 Hrs.</b>			
	User Interface				
Introduction to Activitie	s, Activity Lifecycle, Introduction to Intents, Linking Activiti	es using Intents,			
calling built-in applicati	ons using Intents, Introduction to Fragments, Adding Fragmen	nts Dynamically,			
Lifecycle of Fragment,	Toast, Understanding the components of a screen, Adap	oting to Display			
Orientation, Split Screen	/ Multi-Screen Activities.				
Mapping of Course Outcomes for Unit IV	CO2: Create mobile applications on the different android	platform.			
Unit V	Designing User Interface with Widgets	8 Hrs.			
Using Basic Views: Te	ext View, Button, ImageButton, EditText, CheckBox, Switch	n, ToggleButton,			
	io Group Views, ProgressBar View, AutoCompleteTextVie				
	ecyclerView to Display Long Lists, Understanding Special	-			
Displaying Pictures and		0			
Multimedia, Animation	and Graphics: Playing Audio, Playing Video, Rotate Anima	tion, Fade In /			
Fade Out Animation, Zo	om Animation, Scale Animation, 2D and 3D Graphics.				
Mapping of Course	CO3: Design and implement mobile applications involvin	g data storage			
<b>Outcomes for Unit V</b>	in databases.				
Unit VI	<b>Databases, Location-Based Services and Google</b>	8 Hrs.			
	Мар				
Data Storage: Shared	Preferences, Internal Storage, External Storage, SQLite Dat	tabases, Content			
provider. and Remote Databases.					
Introduction to SQLite and Room library, SQLite Open Helper and SQLite Database, Creating, opening and closing database, Creating, opening and closing database, Building and executing queries,					
	ng E-mail. Web App, JSON Parsing, JSON Web Service.				

opening and closing database, Creating, opening and closing database, Building and executing queries, SMS Messaging, Sending E-mail, Web App, JSON Parsing, JSON Web Service, Display Google Maps, Getting Location Data, Monitoring a Location. Accessing Phone services (Call, SMS, MMS), Network connectivity services, Sensors, Bluetooth/Wi-Fi Connectivity.

Mapping of Course Outcomes for Unit VI	CO3: Design and implement mobile applications involving data storage in databases.

# **Learning Resources**

# **Text Books:**

- David Griffiths and Dawn Griffiths, "Head First Android Development: A Brain-Friendly Guide", 2<sup>nd</sup> Edition, Shroff / O'Reily Publication
- 2. Barry Burd, "Java Programming for Android Developers for Dummies", 2<sup>nd</sup> Edition, Dummies.
- 3. Wei-Meng Lee, "Beginning Android 4 Application Development", WROX Publication

**Reference Books:** 

- 1. Herbert Schildt, "Java: The Complete Reference", 9th Edition, Tata McGraw Hill
- 2. Reto Meier, "Professional Android 4 Application Development", John Wiley and sons
- 3. John Horton, "Android Programming for Beginners", 3<sup>rd</sup> Edition, Packt Publication

#### **MOOC / NPTEL Courses:**

 NPTEL Course on "Introduction to Mobile Application Development", by Prof. G.Raina, T.Gopal, IIT Madras
 Link of the Course: https://aptel.og.in/courses/106106156

Link of the Course: <u>https://nptel.ac.in/courses/106106156</u>

 Swayam Course on "Android Mobile Application Development", by Dr. Himanshu.N.Patel, Dr. Babasaheb Ambedkar Open University Ahmedabad.
 Link of the Course: <u>https://onlinecourses.swayam2.ac.in/nou21\_ge41/preview</u>

**Ebooks:** 

- $1. \ \underline{https://enos.itcollege.ee/~jpoial/allalaadimised/reading/Android-Programming-Cookbook.pdf.}$
- 2. https://www.programming-book.com/download/?file=10988
- 3. https://www.programmer-books.com/professional-android-4th-edition-pdf/

#### Websites:

- 1. <u>https://developer.android.com</u>
- 2. <u>https://www.javatpoint.com/android-tutorial</u>

Sa	avitribai Phule Pu	ne University				
Fourth Ye	ar of <mark>E &amp; Tc Eng</mark> i	ineering (2019 Course	2)			
<b>404191 (D)</b> :	: Embedded Syste	m Design (Elective - V	V)			
Teaching Scheme:	Teaching Scheme:   Credit   Examination Scheme:					
Theory: 03 hrs. / week	03	In-Sem (Theory):	30 Marks			
		End-Sem (Theory):	70 Marks			
Prerequisite Courses, if any:						
Companion Course, if any:						
1. Lab Practice -3						
Course Objectives:		1				
1. To define design consider		•				
2. To utilize specific resourc	-	essor.				
<ol> <li>To integrate embedded ha</li> <li>To design embedded system</li> </ol>						
4. To design embedded system Course Outcomes: On completion		or will be able to				
CO1: Apply the design aspects o		er will be able to -				
<b>CO1:</b> Apply the design aspects of <b>CO2:</b> Create and debug a firmwa		System using APM Cortex	х <b>М</b> /			
CO2: Create and debug a fifthwa CO3: Develop a specific softward						
<b>CO3: Develop</b> a specific softward <b>CO4: Utilize</b> an open source RTC		•	stem.			
CO5: Design an advanced embed	•	eni design.				
CO5: Design an advanced embedded CO6: Explore Embedded Androi						
COU. Explore Embedded Andro	Course Co	ntents				
Unit I In		bedded System Design	n 6 Hrs.			
Embedded System fundamental,						
Classification and Characteristics						
Processor to be embedded into a	-	•				
hardware components and soft	• •		•			
architecture. Integration of embe						
and debugging techniques. Embed			ie development te			
Mapping of Course CO1: A		s of Embedded system.				
Outcomes for Unit I						
Unit II E	mbedded Process	or ARM Cortex M4	8Hrs.			
			ication, Architectu			
Comparison of STM32F family	and MCU selection	criteria for specific appli				
			Timer module, AI			
review of STM32F4XX MCU: F	Pin diagram, CPU, M	emory, GPIO, Clock and				
review of STM32F4XX MCU: F DAC module, Study of STM32	Pin diagram, CPU, M F4 Development boa	emory, GPIO, Clock and T rd, Software development	tool SM32CubeI			
review of STM32F4XX MCU: F DAC module, Study of STM32 IDEs for STM32; Interfacing red	Pin diagram, CPU, M F4 Development boa quirements issues, G	emory, GPIO, Clock and 7 rd, Software development PIO configuration of STM	tool SM32CubeI 132F4, interfacing			
Comparison of STM32F family review of STM32F4XX MCU: F DAC module, Study of STM32 IDEs for STM32; Interfacing rec input switch, heavy loads (sample and RTC, Configure an UAR	Pin diagram, CPU, Me F4 Development boa quirements issues, G e program mapping wi	emory, GPIO, Clock and T rd, Software development PIO configuration of STM ith any application), Conce	tool SM32CubeI A32F4, interfacing pt of Watchdog tin			
review of STM32F4XX MCU: F DAC module, Study of STM32 IDEs for STM32; Interfacing rec input switch, heavy loads (sample	Pin diagram, CPU, Me F4 Development boa quirements issues, G e program mapping wi	emory, GPIO, Clock and T rd, Software development PIO configuration of STM ith any application), Conce	tool SM32CubeI A32F4, interfacing pt of Watchdog tin			

Unit III	GPIO and HAL	6 Hrs.	
Overview of Hardware	Abstraction Layer (HAL) drivers; HAL data structure, Al	PI classification,	
naming rules, Configur	ration, GPIO HAL API, Driving a GPIO.GPIO ports fur	nction and their	
relationship to HAL, ,	Use of HAL library for SPI, I2C and CAN module, USB	Modules in the	
STM32F4Microcontrolle	er.		
Mapping of Course Outcomes for Unit III	CO3: Develop a specific software code for the functionalit Embedded System.	ty of the	
Unit IV	RTOS for STM32F4	8 Hrs.	
Reviewing the concepts	underlying an RTOS, Introduction to FreeRTOS. Configure I	FreeRTOS Using	
STM32CubeMX, Threa	d Management, FreeRTOS and the C stdlib, Synchroniza	ation Primitives,	
Debugging features of F	FreeRTOS, debugging with STM32CubeIDE. Alternatives op	en source RTOS	
to FreeRTOS: ChibiOS	and Contiki OS. Create a FreeRTOS project in STM32CubeIE	DE. Write C code	
for any task/event/thread	l with FreeRTOS		
Mapping of Course Outcomes for Unit IV	CO4: Utilize an open source RTOS for embedded system	design.	
Unit V	Embedded System Design with STM32	<b>6 Hrs.</b>	
between PC and STM32 the PC and STM32F4 M	2F4, PID speed control of DC motor, Transferring the Digital licrocontroller.	Signal Between	
Mapping of Course Outcomes for Unit V	CO5: Design an advanced embedded system.		
Unit VI	Embedded Android	6 Hrs.	
development tools, Over Loading and interfacing	stics of Android, different android platforms, requirements erall architecture of Android, Linux Kernel, Hardware Ab g methods. Device hardware methods and interfaces, File and imported into the AOSP(Android Open Source Project) CO6: Explore Embedded Android system.	straction Layer,	
Outcomes for Unit VI	COO. Explore Embedded Android System.		
	Looming Descurres		
Torrt Doolage	Learning Resources		
Publications.	ny-Givargis, "Embedded System Design", 3 <sup>rd</sup> Edition Wiley Ir In Embedded Software Primer", 2 <sup>nd</sup> Edition, Pearson Publication		
<ol> <li>Cem Unsalan, Huseyin Deniz Gurhan, Mehmet Erkin Yucel, "Embedded System Design with</li> </ol>			

Arm Cortex-M", Spinger.

- 1. Carmine Noviello, "Mastering STM32", 2<sup>nd</sup> Edition, Lean Publisher.
- 2. Muhamad Ali Mazidi, Shujen Chen, Eshragh, "STM32 ARM Programming for Embedded Systems".
- 3. Donald Norris, "Programming with STM32", Mc Graw Hill Publication,
- 4. KarimYagbmour, "Embedded Android", 1st Edition, O'Reilly publishers.
- 5. RM0390 Reference manual, STM32F446xx advanced Arm®-based 32-bit MCUs

#### **MOOC/NPTEL Courses:**

1. NPTEL Course on, "Introduction to Embedded System Design", by Prof. D.V.Gadre,

Prof.B.N. Subudhi IIT Jammu

Link of the course: https://nptel.ac.in/courses/108102169

	Savitribai Phule P	une University	
Fourth	Year of E & Tc En	gineering (2019 Course)	
		puting (Elective - V)	
Teaching Scheme:CreditExamination Scheme:			
Theory: 03 Hrs. / Week	. 03	In-Sem (Theory): 30	Marks
		End Sem (Theory): 70	Marks
Prerequisite Courses, if any 1. Basics of Communica 2. Fundamental of Netw	ation Technologies.		
Companion Course, if any:			
Course Objectives:			
network, features of r	• • •	blogy and the working of Mobil	e IP, ad hoc
Course Outcomes: On comp CO1: Understand concepts CO2: Analyse next generation CO3: Understand network I CO4: Understand IP and Tr CO5: Study of different mat CO6: Understand different	of Mobile Communication on Mobile Communication ayers of Mobile Communication cansport layers of Mobile hematical models.	on. on System. nication.	
	Course Co	ontents	
Unit I		Mobile Computing	6 Hrs.
Introduction to Mobile Co Communication Technologi FDMA, and CDMA.	mputing: Applications es, Multiplexing: Sprea	of Mobile Computing- Gener d spectrum, MAC Protocols:	ations of Mobile SDMA, TDMA,
Mapping of Course Outcomes for Unit I	CO1: Understand conce	pts of Mobile Communicatior	1.
Unit II	Mobile Telecon	nmunication System	7 Hrs.
Allocation, Routing, Mobili Security.	ty Management, Securi	Protocols, Connection Establis ty, GPRS and UMTS: Archit hitecture, Applications, 5G ena	ecture, Handover,

Recent trends in Telecommunication Industries.

MappingofCourseCO2: Analyse next generation Mobile Communication System.Outcomes for Unit II

		I
Unit III	Network Layer	6 Hrs.
Mobile IP, DHCP, Adl	Hoc, Proactive protocol-DSDV, Reactive Routing Protocols	S: DSR, AODV,
Hybrid routing: ZRP, M	lulticast Routing: ODMRP, Vehicular Ad Hoc networks (VA	ANET), MANET
Vs VANET: Security.		
Mapping of Course Outcomes for Unit III	CO3: Understand network layers of Mobile Communication.	
Unit IV	Mobile IP and Transport Layer	8 Hrs.
Mobile IP: Need of mo	bbile IP, IP packet delivery, Agent Discovery, Registration,	Tunnelling and
encapsulation, Route opt		C
Transport Layer: Over	view of Traditional TCP and implications of mobility control.	Improvement of
	oop TCP, Mobile TCP, Fast Retransmit/fast recovery, Tin	me-out freezing,
	Transaction-oriented TCP.	
Mapping of Course Outcomes for Unit IV	CO4: Understand IP and TCP layers of Mobile Communication	<b>n.</b>
Unit V	Fading Channels	<b>7 Hrs.</b>
Rayleigh Fading and Sta	tistical Characterization, Properties of Rayleigh Distribution,	BER in Fading,
Narrowband vs Wideba	and Channels, Characterization of Multipath Fading Chan	nels, Choice of
Modulation, Coherent ve	prsus Differential Detection, BER in Fading, Ricean Fading.	
Mapping of Course Outcomes for Unit V	CO5: Study of different mathematical models.	
Unit VI	Operating System & Applications of Mobile Computing	8 Hrs.
<b>Operating System:</b> A F	Few Basic Concepts, Special Constraints and Requirements of	of Mobile OS, A
	Mobile Operating Systems, Windows Mobile, Palm OS, Sy	
Android, Blackberry OS	, A Comparative study of Mobile OS, OS for sensor Network.	
Applications: M-Comm	erce, Business to Consumer (B2C) Applications, Business to	Business (B2B)
Applications. Structure	of M-Commerce, Pros and Cons of M-Commerce, Mobile F	Payment System,
Mobile Payment Schen	nes, Desirable properties of a Mobile Payment system, M	Mobile Payment
solutions, Process of Mo	bile Payment, Security Issues.	
Mapping of Course Outcomes for Unit VI	<b>CO6:</b> Understand different mobile applications.	
	Learning Resources	
Text Books:		
1. Clint Smith, Dan	iel Collins, "Wireless Networks", 3 <sup>rd</sup> Edition, McGraw Hill Pu	ublications,

Clint Smith, Daniel Collins, "Wireless Networks", 3<sup>rd</sup> Edition, McGraw Hill Publications,
 Share Conder, Lauren Darcey, "Android Wireless Application Development", Volume I, 3<sup>rd</sup> Edition, Pearson.

- 1. Jochen Schiller, "Mobile Communications", 2<sup>nd</sup> Edition, Pearson.
- 2. Paul Bedell, "Cellular networks: Design and Operation A real world Perspective", Outskirts Press.
- 3. Zigurd Mednieks, Laird Dornin, G, Blake Meike and Masumi Nakamura, "Programming Android", O"Reilly.
- 4. Alasdair Allan, "iPhone Programming", O"Reilly.
- 5. Donny Wals, "Mastering iOS 12 Programming".
- 6. Reza B"Far, "Mobile Computing principles", Cambridge University Press.

**MOOC / NPTEL Courses:** 

 NPTEL Course "Mobile Computing" by Prof. Sridhar Iyer and Prof. Pushpendra Singh IIT Madras

Link of the Course: <u>https://nptel.ac.in/courses/106106147</u>

- NPTEL Course "Funadamentals of MIMO Wireless Communication" by Prof. Suvra Sekhar Das IIT Kharagpur Link of the Course: <u>https://nptel.ac.in/courses/117105132</u>
- NPTEL Course "Principles of Modern CDMA/MIMO//OFDM Wireless Communications" by Prof. Aditya. K. Jagannatham IIT Kanpur Link of the Course: <u>https:// nptel.ac.in/courses/117104115</u>

			une University	
Fou	rth Year of	f E & Tc Eng	ineering (2019 Course)	)
	404192 (A)	: System on (	Chip (Elective - VI)	
Teaching Schem	ne:	Credit	Examination	Scheme:
Theory: 03 hrs. / we	ek	03	In-Sem (Theory):	30 Marks
			End-Sem (Theory):	70 Marks
Prerequisite Courses, if	any:			
Companion Course, if a	any:			
Course Objectives:				
1. To understand the	e basic concer	ots and architect	ure of SOC.	
2. To understand the	e basic termin	ology of Verilog	g HDL programming.	
3. To apply the vari	ous Verilog n	nodeling styles in	n writing the design and test	bench codes.
4. To understand the	e basic steps u	used in the VLSI	Physical Design.	
5. To understand the	e basic archite	ecture of various	processors used in SOC.	
6. To understand the	e working prii	nciple of various	Buses and memory used in	SOC.
Course Outcomes: On c	completion of	the course, learn	ner will be able to -	
CO1: Understand the b	asic concepts	and architecture	e of SOC.	
CO2: Understand the b	asic terminolo	ogy of Verilog H	IDL programming.	
CO3: Apply the various	Verilog mod	eling styles in w	riting the design and testber	nch codes.
CO4: Understand the b	asic steps use	d in the VLSI P	hysical Design.	
CO5: Understand the b	asic architect	ure of various pr	rocessors used in SOC.	
CO6: Understand the v	vorking princi	ple of various B	Buses and memory used in S	OC.
		Course Co	ntents	
Unit I		Introduo	ction to SOC	6 Hrs.
System Architecture; System	ystem Comple	exity; Compone	ents of the system; Hardwa	are & Software; An
	-		ts, and challenges; Application	
-	Accelerators,	Memory and I	Peripherals, On-chip interco	onnects, and various
signal processing units.				
Mapping of Course Outcomes for Unit I	CO1: Unde	rstand the basic	c concepts and architectur	e of SOC.
Unit II		Verilog	HDL - I	8 Hrs.
	CAD tools;		<b>5 HDL - I</b> /hy Verilog; Verilog: data	
Evolution and need of		HDL tools; W	-	types, system tasks,
Evolution and need of compiler directives; Hier	rarchical Mod	HDL tools; W leling Concepts:	hy Verilog; Verilog: data	types, system tasks, design methodology;
Evolution and need of compiler directives; Hier modules and module ins	rarchical Mod stances; Modu	HDL tools; W leling Concepts: ules and Ports:	hy Verilog; Verilog: data Top-down and bottom-up	types, system tasks, design methodology;
Evolution and need of compiler directives; Hier	rarchical Mod stances; Modu referencing, a	HDL tools; W leling Concepts: ules and Ports: nd timescale.	hy Verilog; Verilog: data Top-down and bottom-up	types, system tasks, design methodology; claration, connecting

Unit III	Verilog HDL-II	8 Hrs.	
	Adeling using basic Verilog gate primitives, description of		
BUF/NOT type gates; Dataflow Modeling: Continuous assignments, delay specification, expressions,			
	Iodeling: Structured procedures, initial and always blocks, bl	· •	
1 '	lay control, conditional statements, multiway branching, loop	0	
	nd Functions: tasks vs functions, declaration, invocation, auto		
functions; testbench			
Mapping of Course	CO3: Apply the various Verilog modeling styles in writing	g the design	
Outcomes for Unit III	and testbench codes.		
Unit IV	Physical Design	<b>8 Hrs.</b>	
	and Non-abutted floorplan techniques, floorplan control para	-	
-	rplan; Partitioning; need of partitioning, rules of partitioning		
	goal of placement, coarse placement, legalization, placeme	ent blockage,	
	g: netlist, congestion, fixed-die routing, variable-die routing.		
Mapping of Course	CO4: Understand the basic steps used in the VLSI Physic	al Design.	
Outcomes for Unit IV			
Unit V	SOC Processors	<b>6 Hrs.</b>	
	cessors; Processor selection for SOC; Basic concepts in proce		
-	hitecture; Basic elements in Instruction handling; Buffers: min	• • •	
delays, Branches; More VLIW Processors, Supe	e Robust Processors: Vector processors and vector instruct rscalar Processors.	ions extensions,	
Mapping of Course	CO5: Understand the basic architecture of various proce	ssors used in	
<b>Outcomes for Unit V</b>	SOC.		
Unit VI	SOC Buses and Memory	6 Hrs.	
AMBA: Generation of	AMBA (ASB, AHB, APB), Architecture of AMBA, Spe	cification; Core	
Connect bus: PLB, OP	B, DCR; ST bus protocols: Type I, II, III; SOC memory;	Cache memory:	
performance, partitionin	g, multi-level cache; Memory chip technology: On die or Off o	lie.	
Mapping of Course	CO(, U, denote a data a matrix sinte a formation Prove	1	
	CO6: Understand the working principle of various Buses used in SOC.	and memory	
Outcomes for Unit VI		and memory	
	used in SOC.		
Outcomes for Unit VI Text Books:	used in SOC.		
Outcomes for Unit VI Text Books: 1. Michael J. Flynn, sons.	used in SOC. Learning Resources Wayne Luk, "Computer System Design: System on Chip", Joh	n Wiley and	
Outcomes for Unit VI Text Books: 1. Michael J. Flynn, sons.	used in SOC. Learning Resources	n Wiley and	

- 1. M.Wolf, "Principles of Embedded Computing System Design",4<sup>th</sup> Edition, Morgan Kaufmann Publications.
- 2. Michael .D. Ciletti, "Advanced Digital Design with the Verilog(TM) HDL",2<sup>nd</sup> Edition, Pearson.

3. J.Bhasker, "A Verilog HDL Primer", 3<sup>rd</sup> Edition, Star Galaxy Press.

**MOOC / NPTEL Courses:** 

 NPTEL Course on "Hardware modeling using Verilog", by Prof. Indranil Sengupta IIT Kharagpur

Link of the course: <u>https://nptel.ac.in/courses/106105165</u>

- NPTEL Course on "VLSI Physical Design", by Prof. Indranil Sengupta IIT Kharagpur Link of the course: <u>https://nptel.ac.in/courses/106105161</u>
- NPTEL Course on "Embedded Systems", by Prof. Santanu Choudhary IIT Delhi Link of the course: <u>https://nptel.ac.in/courses/106105161</u>

	Sa	witribai Phule P	une University			
For			•	<i>(</i>		
	Fourth Year of E & Tc Engineering (2019 Course) 404192 (B): Nanoelectronics (Elective - VI)					
		· ·				
Teaching Schen	eme: Credit Examination Scheme:					
Theory: 03 hrs. / week03In-Sem (Theory): 30 Marks						
			End-Sem (Theory):	70 Marks		
Prerequisite Courses, i	f any:					
Companion Course, if	any:					
Course Objectives:						
1. To understand th	e process	es in Nanoelectronic	e devices manufacturing.			
			, and operation of Nanoelec	tronic devices.		
• •		no-CMOS technolo				
C	1		odevice fabrication.			
		achines and nanodev				
• •			ectronics in the electronics i	industry.		
Course Outcomes: On o	-					
<b>CO1: Understand</b> the f <b>CO2: Understand</b> to Na		6	a nanotecnnology.			
CO3: Explore various N						
CO4: Understand the in			es.			
<b>CO5: Understand</b> Nand						
CO6: Understand varie	ous applic					
<b>.</b>		Course Co				
Unit I			to Nanotechnology	6 Hrs.		
	0.		ce behind Nanotechnology	•		
	o make i	nanostructures and	imagine nano behaviors, L	limitations of Silicon		
Material						
Mapping of Course Outcomes for Unit I	<b>CO1: U</b>	nderstand the funda	mental knowledge behind na	notechnology.		
Unit II		Nano CM	IOS Devices	6 Hrs.		
	n-volatile		dielectric materials for futu			
			be instrument, nanoscale lith			
Mapping of Course		nderstand to Nano-C				
Outcomes for Unit II						
Unit III		-	s and Nanotubes	6 Hrs.		
Properties of Nanopart	ticles: M	etal nanostructures a	and semiconducting nanopar	ticles.		
Carbon nanostructures						
			oplications of Nanotubes.			
Mapping of Course		-	oelectronics material.			
Outcomes for Unit III	004: 0	nderstand the imp	ortance of carbon nanotub	les.		

Unit IV	Nanoelectronics	6 Hrs.
Introduction, the tools of	f manufacturing of micro and nano fabrication optical lithogra	phy, electron
beam lithography, ato communication.	omic lithography. Nano-Electronics for advanced comp	putation and
Mapping of Course	CO3: Explore various Nanoelectronics material.	
Outcomes for Unit IV	CO4: Understand the importance of carbon nanotubes.	
Unit V	Nanomachine and Nanodevice Fabrications	6 Hrs.
	anodevices, NEMS and MEMS and their fabrication,	
supermolecular switches		molecular and
Mapping of Course Outcomes for Unit V	CO5: Understand Nanomaterial and Nanodevice fabricat	ion.
		~ ~~
Unit VI	Applications of Nanotechnology	6 Hrs.
	in Electronics: Application of nanostructures in electronics,	-
Mapping of Course	nation, and storage. Application of nanotechnology in biomed CO6: Understand various applications of Nanotechnology	
Outcomes for Unit VI	COO: Understand various applications of Nanotechnology	in Electronics.
	Learning Resources	
Text Books:		
1. Anatoli Korkin, Ja	an Labanowski, Evgeni Gusev, Serge Luryi, "Nanotechnology	for Electronic
Materials and Dev	vices", Springer.	
	niel Ratner, "Nanotechnology: A Gentle introduction to a	next big Idea"
1 <sup>st</sup> Edition, Pearso	on Education. lanotechnology", Springer.	
5. Olegory rimp, N	anoteemology, springer.	
sons	Jr., Frank J. Owens, "Introduction to Nanotechnology" Joh	n Wiley and
<b>Reference Books:</b>		
1. K. Goser P. Glose	ekotter, J. Dienstuhl, "Nanoelectronics & Nanosystems"; Sprin	nger
MOOC / NPTEL Cours	ses:	
1. NPTEL Course o	on "Nanostructured materials-synthesis,properties,self asse	mbly and
applications", by	y Prof. A.K.Ganguli IIT Delhi	
Link of the cour	rse: <u>https://nptel.ac.in/courses/118102003</u>	
2. NPTEL Course o	on " <b>Nanoelectronics: Devices and Materials</b> ", by Dr. Navkar	ita Bhat, Dr.
S.N.Shivashanka	r, Prof. K.N.Bhat IISc Bangalore	
	se: https://nptel.ac.in/courses/117108047	

Savitribai Phule Pune University				
Fou	rth Yea	ar of E & Tc Engi	neering (2019 Course)	
404192 (C): Remote Sensing (Elective - VI)				
Teaching Schem	ne:	Credit	Examination Sci	heme:
Theory: 03 Hrs. / W	Veek	03	In-Sem (Theory): 30 N	Aarks
			End-Sem (Theory): 70	Marks
Prerequisite Courses, if	any:			
Companion Course, if a	any:			
Course Objectives:				
1. To introduce the	basic prin	nciples of remote sensi	ng.	
	-	-	tellite sensors characteristics.	
	• 1	1	ucts, visual interpretation and	basics of digital
processing of sate		0		
		0 0	ellite system and its application	n.
		-	e and lidar remote sensing	
Course Outcomes: On a	-			~ *
<b>CO1: Describe</b> the conc <b>CO2: Explain</b> the sensor	-	-	tromagnetic radiation interaction	011.
-		•		ositas
<b>CO3: Classify</b> different types of satellite data products and design various color composites. <b>CO4: Describe</b> the fundamentals of microwave remote sensing.				
<b>CO5: Analyze</b> GNSS signal structure and augmentation systems.				
CO6: Demonstrate and		e	•	
		Course Con	tents	
Unit I		Principles of R	emote Sensing	7 Hrs.
Basic principles of Remo	ote Sensi	ng, Data and Informati	on, Remote Sensing Data Col	lection, Types of
Remote Sensing- Active	and Pass	sive remote sensing; A	dvantages and Limitations of	Remote Sensing,
Electromagnetic Energy	- Electro	magnetic Spectrum, I	Interaction of EMR: Interaction	on with Earth's
Atmosphere and Atmosp	heric wir	ndow, Spectral Signatu	re:Interaction with Soil, Water	and Vegetation
Mapping of Course Outcomes for Unit I		escribe the concepts of a ceraction.	emote sensing and electromage	netic radiation
Unit II		Satellite Sensors	and Resolution	7 Hrs.
Types of Remote Sensir	ng Platfor	rms, Types of Satellite	e Orbits - Geosynchronous an	d Geostationary,
Polar and sun synchrono	ous orbit,	low earth, medium e	arth, highly elliptical orbits, H	Recent Trends in
			al Space Missions : Indian &	
-			Spatial, Temporal, Spectra	
Differences between Mu	ltispectra	l and Hyperspectral re	mote sensing	
Mapping of Course Outcomes for Unit II	CO2: Ex	xplain the sensors chara	cteristics and analyze its resolu	ition.

Unit III	Satellite Data Products & Processing	7 Hrs.
Satellite Data Analysis:	Data Products and Their Characteristics, Data Pre-processing	g – Atmospheric,
Radiometric, Geometric	Corrections - Basic Principles of Visual Interpretation, Equip	oment for Visual
Interpretation, Ground	Truth; Color Composite : False and True Color C	Composite;Image
enhancements; Classific	ations - Supervised and Unsupervised, Normalized satellite	Indices - NDVI,
NDWI, GDVI, NDSI etc	; Remote Sensing Data Sources : USGS, Bhuvan, ESA, Sentin	nel etc
Mapping of Course	CO3: Classify different types of satellite data products and des	ign various color
Outcomes for Unit III	composites.	
Unit IV	Active Remote Sensing	6 Hrs.
Microwave Remote Se	nsing: Active and Passive Systems, Advantages, Platforn	ns and Sensors,
Microwave Radiation	and Simulation, Principles of Radar - Resolution, I	Range, Angular
	ave Scattering, Imagery – characteristics and Interpretation	0
LiDAR - Concepts and i		, ,
Mapping of Course	CO4: Describe the fundamentals of microwave remote sensing	
Outcomes for Unit IV	CO4: Describe the fundamentals of incrowave remote sensing	•
Unit V	GNSS Technology	<b>7 Hrs.</b>
Introduction of GNSS	Technology : GNSS Signal Structures, GNSS Vulner	abilities, GNSS
Applications, GNSS M	arket and Business, Indian Regional Navigation Satellite S	ystem (IRNSS),
Ground Based Augmen	tation Systems, Space Based Augmentation Systems - GAGA	N; Principles of
satellite positioning - P	rinciple of Satellite Positioning, GNSS Orbits, Navigation M	Message Details;
Positioning Errors, Data	Formats, Location-Based Services (LBS), Tools for GNSS da	ta processing.
Mapping of Course Outcomes for Unit V	CO5: Analyze GNSS signal structure and augmentation system	15.
<b>Unit VI</b>	<b>Applications of Remote Sensing</b>	<b>6 Hrs.</b>
Applications of Remote	Sensing: Environmental and Disaster, Coastal and Near Sl	hore, Forest and
Agriculture, Water Reso	urce, Urban Planning and Management, Land Use and Land C	Cover Analysis.
Mapping of Course Outcomes for Unit VI	CO6: Demonstrate and describe real life applications of remot	e sensing.
	Learning Resources	
Text Books:		
1. John A. Richards	s, "Remote Sensing Digital Image Analysis - An Introduction"	5 <sup>th</sup> Edition
	Berlin Heidelberg.	5 Lunion,
1 0 0	damentals of Remote Sensing", Universities Press,	
-	vedi. R. S., "Remote Sensing Application", Published b	W NRSC ISPO
$\mathbf{J}_{\mathbf{n}} = \mathbf{N} \mathbf{U} \mathbf{y}_{\mathbf{n}} \mathbf{I}_{\mathbf{n}} \mathbf{S}_{\mathbf{n}}, \mathbf{D} \mathbf{W} \mathbf{I}_{\mathbf{n}}$	vou. R. S., Remote Sensing Application, rubilshed b	y miller ISKU

Hyderabad.

- 1. Liu, J.-G., & Mason, P.J. "Image Processing and GIS for Remote Sensing: Techniques and Applications", 2<sup>nd</sup> Edition, Wiley-Blackwell.
- 2. Sabins, F. F., "Remote Sensing: Principles and Interpretation", 4th Edition, Waveland Pr. Inc.
- 3. Navalgund, R. R. Ray, S. S., "Hyperspectral Data, Analysis Techniques Application", Indian Society of Remote Sensing.
- 4. Lillesand, T. M., Kiefer, R. W., Chipman, J. W., "Remote Sensing and Image Interpretation", 7<sup>th</sup> Edition, John Wiley & Sons.
- 5. Bernhard Hofmann-Wellenhof, Herbert Lichtenegger, Elmar Wasle, "GNSS Global Navigation Satellite Systems: GPS, GLONASS, Galileo, and more", Springer.
- 6. Pinliang Dong, Qi Chen, "LiDAR Remote Sensing and Applications", 1st Edition CRC Press.

### **MOOC / NPTEL Courses:**

- NPTEL Course "Remote Sensing: Principal and Application", by Prof. Eswar Rajasekaran, IIT Bombay Link of the Course: <u>https://nptel.ac.in/courses/105101206</u>
- 2. NPTEL Course "**Remote Sensing Essentials**", by Dr. Arun.K.Saraf, IIT Roorkee Link of the Course: <u>https://nptel.ac.in/courses/105107201</u>
- NPTEL Course "Global Navigation Satellite Systems and Applications", by Dr. Arun.K.Saraf, IIT Roorkee Link of the Course: <u>https://nptel.ac.in/courses/105107194</u>

Savitribai Phule Pune University					
Fourt	th Year of <mark>E &amp; Tc Engi</mark> r	neering (2019 Course)			
404192 (D): Digital Marketing (Elective - VI)					
Examination Scheme:CreditExamination Scheme:					
Theory: 03 Hrs. / Wee	ek 03	In-Sem: 30 Marks			
		End Sem: 70 Marks			
Prerequisite Courses, if an	ny:	1			
Companion Course, if any	•				
1. Digital Business Ma	nagement				
<b>Course Objectives:</b>					
e	al marketing & process of we	6			
	words for a website & underst	tand the SEO.			
	s Digital Marketing Tools. social media websites for Dig	ital Markating			
5. To be conversant with		itai marketing.			
	trends in Digital Marketing.				
	npletion of the course, learner	r will be able to			
	1	nd explore it for digital marke	ting.		
CO2: Apply various keywo	ords for a website & to perfor	rm SEO.	_		
	_	ent the Digital Marketing Tools			
	CO4: Illustrate the use of Facebook, Instagram and Youtube for Digital Marketing in real life.				
1	rm for various campaigning.	· · · · · ·			
CO6: Understand the imp	ortance of recent trends in dig				
	Course Con				
Unit I		anning and Structure	<b>7 Hrs.</b>		
Importance of Digital Marketing, Digital Marketing Vs. Traditional Marketing, Inbound vs Outbound					
Marketing, Understanding	Demographics. WWW, Buy	ing a Domain, Core Objective	e of Website and		
Flow, One Page Website, S	Strategic Design of Products	& Services Page, Strategic De	esign of Landing		
Page, Segmentation & Targ	geting and Positioning to Dig	gital Marketing, Portfolio, Gal	lery and Contact		
Us Page, Google Analytics	Tracking Code, Designing	Wordpress Website. Mobile F	riendly Website,		
Payment Gateway like UPI	, e-Commerce				
Mapping of Course Outcomes for Unit I	CO1: Design websites usir for digital marketing	ng free tools like Wordpress	s and explore it		
		-			
Unit II	Search Engine Or	ptimization (SEO)	7 Hrs.		
		& Business Objectives; Writin			
		king, Website SEO Auditing,	0		
		etitor's Website and their traffi	•		
Mapping of Course					
Outcomes for Unit II CO2: Apply various keywords for a website & to perform SEO.					

Unit III	Search Engine Marketing	7 Hrs.

Importance of Adwords, Google Ad Types, PPC Cost Formula, Ad Page Rank, Billing and Payments, Adwords User Interface, Keyword Planner, Creating Ad Campaigns, Creating Text Ads, Creating Ad Groups, Search Engine Marketing (SEM) Tools, Bidding Strategy for CPC, Case Studies. Conversion Tracking Code, Designing Image Ads, Creating Video Ads, Youtube Video Promotion, Hi-Jack Competitor's Video Audience, Case Studies. Remarketing Strategies, Remarketing Tracking Code, Website or Blog Linking Google Analytics, Designing Remarketing Images, Shared Budget, Mobile Advertising.

MappingofCourseOutcomes for Unit III	CO3: Understand the various SEM Tools and implement the Marketing Tools.	e Digital
Unit IV	Social Media Marketing (SMM) Part 1	8 Hrs.

B to C Perspective, B to B Perspective:

Introduction; Major Social Media Platforms for Marketing; Developing Data-driven Audience & Campaign Insights; Social Media for Business;

Facebook & Instagram Marketing: Understanding of Facebook Marketing, Types of Facebook Advertising, Creating first ad on Facebook, Setting Campaign and optimization, Facebook Power Editor, Facebook Video Marketing, Facebook App & Shopping Marketing

Youtube Marketing: YouTube Account Setup (Create a business account with a personal account), YouTube Monetization, YouTube Ads, YouTube Analytics.

Mapping of Course	CO4: Illustrate the use of Facebook, Instagram and Youtube	for Digital
<b>Outcomes for Unit IV</b>	Marketing in real life.	
Unit V	Social Media Marketing (SMM) Part 2	<b>8 Hrs.</b>

**LinkedIn Advertising:** How to use Linkedin Professionally, Types of LinkedIn Advertising, LinkedIn New feed Advertising, LinkedIn Message Pitching, Traffic and Leads Generation, Billing and Report.

**Email Marketing:** Email Software and Tools, Importing Email Lists, Planning Email Campaign, Email Templates and Designs, Sending HTML Email Campaigns, Web Forms Lead Importing, Integrating Landing Page Forms, Campaign Reports and Insights, Segmentation Strategy, Responder Tracker

Mapping of Course	CO5: Use Linked in platform for various campaigning.		
Outcomes for Unit V			
Unit VI	Upcoming Trends in Digital Marketing	6 Hrs.	
Podcast, OTT Platforms, Mob-Ad, No Click Searches, Google Verified Listing, Voice Search, Visual			
Search, Online Reviews, Automated and Smart Bidding, Chatbots, Affiliate Marketing			
MappingofCourseOutcomes for Unit VICO6: Understand the importance of recent trends in digital marketing.			

### **Learning Resources**

#### **Text Books:**

- 1. Cory Rabazinsky, "Google-Ad words for Beginners: A Do-It-Yourself Guide to PPC Advertising"
- 2. Ian Brodie, "Email Persuasion: Captivate and Engage Your Audience, Build Authority and Generate More Sales With Email Marketing"
- 3. Jan Zimmerman and Deborah, "Social Media Marketing All-In-One for Dummies"
- 4. Dave Chaffey, Fiona Ellis-Chadwick, Kevin Johnston, Richard Mayer, "Internet Marketing", Pearson Education.
- 5. Oliver J Rich, "Digital Marketing"
- 6. Gerry T. Warner and Joe Wilson Schaefer "Online Marketing"

#### **Reference Books:**

- 1. Prof. Seema Gupta, "Digital Marketing", Mcgraw Hill Publications.
- 2. Judy Strauss, Adel Ansary, Raymond Frost, Prentice Hall, "E- Marketing"
- 3. Dr. Andy Williams ,"WordPress for Beginners 2020: A Visual Step-by-Step Guide to Mastering WordPress"
- 4. Cecilia Figueroa, "Introduction To Digital Marketing 101", BPB Publications.

#### **MOOCs / NPTEL:**

- 1. Digital Tools Certification- By Google Link of the Course: <u>https://skillshop.exceedlms.com/student/catalog</u>
- Swayam Certification course on, "Digital Marketing", by Dr. Tejindarpal Singh Panjab University Chandigarh Link of the Course: https://swayam.gov.in/nd2\_ugc19\_hs26/preview

	Savitribai Phule Pur	ne University			
Fourth Year of E & Tc Engineering (2019 Course)					
404193: Innovation and Entrepreneurship					
Examination Scheme	Examination Scheme:CreditExamination Scheme:				
Tutorial: 02 Hrs. / We	ek 02	Term Work: 50 Marks			
Prerequisite Courses, if an	•	1			
1. Project Management Companion Course, if any					
	· 				
Course Objectives:					
1. To know innovation a	· ·				
2. To be trained in desig	-				
3. To comprehend idea	6				
<ol> <li>To gain knowledge o</li> <li>To study about patent</li> </ol>	e				
2 1	1 0				
6. To become skilled at <b>Course Outcomes:</b> On com		er will be able to			
		characteristics of an entreprene	eur.		
		rocess and its application in va			
business settings.			5		
CO3: Generate sustainable	e ideas.				
CO4: Explore various proc	cesses required to be an ent	repreneur.			
<b>CO5: Understand</b> patents	-				
CO6: Choose and use appr	opriate social media for ma	urketing.			
	Course Con	tents			
Unit I	Introduction to	Innovation and	3 Hrs.		
	<b>_</b>	eneurship			
		t takes to be an entreprer			
		tween innovation and entrepret	-		
Mapping of Course C Outcomes for Unit I	O1: Understand Innovation entrepreneur.	, Entrepreneurship and charac	teristics of an		
	*				
Unit II	<b>Design</b>	Thinking	3 Hrs.		
Introduction to Design T	hinking, Design Research	Strategies, Design Researc	ch - tools for		
observation and immersion,		<u> </u>			
Mapping of Course Outcomes for Unit IICO2: Develop a strong understanding of the Design Process and its application in variety of business settings.					
TI °4 TTT			2 11		
Unit III		neration	3 Hrs.		
Types of innovations and the		tion sustainable conditions, I	Jesign factors,		
rypes or mnovations and th	en market impact.				

Mapping of Course Outcomes for Unit III	CO3: Generate sustainable ideas.	
Unit IV	Becoming an Entrepreneur	4 Hrs.
Creating a business plan,	Preparing a Pitching presentation, Building business strategy	
Mapping of Course Outcomes for Unit IV	CO4: Explore various processes required to be an entrepreneur	•
Unit V	Creating a Startup	3 Hrs.
Types of companies, lega	al processes for registering companies, registering as startup	
Mapping of Course Outcomes for Unit V	CO5: Understand patents and its process of filing.	
Unit VI	Indian Patents	2 Hrs.
	ent basics, Patent analytics, Role in R&D and business planning nent, Technology transfer.	g, Patents to
Mapping of Course Outcomes for Unit VI	CO6: Choose and use appropriate social media for marketing.	
	Learning Resources	
Reference Books:		
<ol> <li>"The Field Guide</li> <li>Kalyan C. Kanka Practice", Oxford</li> </ol>	epreneurship for Engineers", Dhanpat Rai & Co. (p) Ltd. to Human-Centered Design", by IDEO.org mala, A.K. Narasani, V. Radhakrishnan, "Indian Patent Law and l Press. ean Startup", Penguin Books Limited ( <b>E-Book</b> ).	d
1. Swayam Course of	on " <b>Entrepreneurship</b> " by Prof. C. Bhaktvatsala Rao IIT Madr rse: <u>https://onlinecourses.nptel.ac.in/noc21_mg70/preview</u>	as
Ramadurai IIT Ma	n " <b>Design Thinking-A Primer</b> " by Prof. A. Mahalingam, Prof adras se: <u>https://onlinecourses.nptel.ac.in/noc22_mg32/preview</u>	. В.
Madras	n "Patent Law for Scientists and Engineers" by Prof. Feroz Ali se: <u>https://onlinecourses.nptel.ac.in/noc20_hs55/preview</u>	IIT
Agarwal, Prof. V	n "Innovation, Business Models and Entrepreneurship" by Prof 7 inay Sharma IIT Roorkee <b>rse: <u>https://nptel.ac.in/courses/110107094</u></b>	f. Rajat

# List of Tutorials to be carried out

1.	Design a strategy by writing steps to market the project you are building.
2.	Generate an idea having novelty.
3.	Prepare a business plan.
4.	Create a pitching deck.
5.	Preparing a business strategy.
6.	Write a patent draft.

	Savitribai Phule Pu	ıne University			
Fourth Year of E & Tc Engineering (2019 Course)					
					404194: Digital Business Management
Examination Schen	Examination Scheme: Credit Examination Scheme:				
Tutorial: 02 Hrs. / W	eek 02	Term Work: 50 Marks			
Prerequisite Courses, if a	•				
1. Project Managemer					
<b>Companion Course, if an</b> 1. Digital Marketing	.y:				
Course Objectives:					
	digital business concept.				
2. To acquaint with E					
	to E-business and its strategi mpletion of the course, learn				
<b>CO1: Identify</b> drivers of c	•	ler will be able to			
-	-	r E-business and management.			
<b>CO3: Prepare</b> E-business					
	Course Co	ntents			
Unit I	Introduction (	o Digital Business	4 Hrs.		
		places, structures, mechanisms			
-		Iobile, Cloud Computing, Soci ervices), Opportunities and Cha			
Mapping of Course Outcomes for Unit I	CO1: Identify drivers of dig	șital business.			
Unit II	Overview o	f E-Commerce	8 Hrs.		
E-Commerce: Meaning, F	Retailing in e-commerce-pro	ducts and services, consumer b	ehavior, market		
		and buying in private e-market	· •		
0 11		borative Commerce, Intra busi			
		innovative EC System-From E	-		
and learning to C2C, mobi	le commerce and pervasive	computing EC Strategy and Im	plementation-		
EC strategy and global EC	, Economics and Justification	n of EC, Using Affiliate marke	ting to promote		
your e- commerce busines	s, Launching a successful on	line business and EC project, I	egal, Ethics		
and Societal impacts of EC					
Mapping of Course Outcomes for Unit II	CO2: Illustrate various app management.	roaches and techniques for E-bu	isiness and		
Unit III	Digital Busines	s Support Services	3 Hrs.		
		edge Tope Apps, Information a			
system:		pplications and Infrastructure	and referrur		
		reaches and techniques for E by			

Mapping	of	Course	CO2: Illustrate various approaches and techniques for E-business and
Outcomes f	or Un	it III	management.

Unit IV	Managing E-Business	4 Hrs.
	anagement skills for e-business, Managing Risks in e –t curity Overview, Electronic Commerce Threats, Encryptio	-
over Public Networks: HT	ey Cryptography, Digital Signatures, Digital Certificates, S TP, SSL, Firewall as Security Control, Public Key Infrast	•
Security, Prominent Crypto		
Mapping of Course Outcomes for Unit IV	CO2: Illustrate various approaches and techniques for E-bus management.	siness and
Unit V	E-Business Strategy	3 Hrs.
-	llation- Analysis of Company's Internal and external enviro	onment, Selection
of strategy, E-business stra	tegy into Action, challenges and E-Transition	
Mapping of Course Outcomes for Unit V	CO2: Illustrate various approaches and techniques for E-bus management. CO3: Prepare E-business plan.	siness and
Unit VI	Materializing e-business:	2 Hrs.
From Idea to Realization-B	Susiness plan, Case Studies.	
Mapping of Course Outcomes for Unit VI	CO3: Prepare E-business plan.	
	I coming Decomos	
Text Books:	Learning Resources	
<ol> <li>Urmi Dutta, Neha So</li> <li>Elias M. Awad, "E-co</li> <li>Dave Chaffey, "Digit</li> <li>Colin Combe, "Introd</li> </ol>	mani, "E-Commerce & Business Communication", Oxford ommerce from vision to fulfilment" 3 <sup>rd</sup> Edition, Prentice Hal cal Business and E-Commerce Management", 6 <sup>th</sup> Edition, Pe duction to E-business: Management and Strategy", 1 <sup>st</sup> Edition ital Business Concepts and Strategy", 2 <sup>nd</sup> Edition, Pearson	ll India arson
<ol> <li>Erika Darics, "Digita</li> <li>"E-Governance-Chal Conference theory ar</li> <li>"Perspectives the D Consulting Journal V</li> </ol>	conomy-A new perspective", OECD Publishing	al

#### **MOOCs / NPTEL:**

- 1. Coursera Course on "Digital Business Specialization" Link of the course: www.coursera.org/specializations/digital-business
- 2. NPTEL Course on "E-Business" by Prof. Mamta Jenamani IIT Kharagpur Link of the course: <u>https://nptel.ac.in/courses/110105083</u>

# List of Tutorials to be carried out

1.	Compare conventional business with e- business based on structure, mechanisms and economics.
2.	Discuss the role of Big Data and Data Analytics in Digital Business Management.
3.	Review various Opportunities and Challenges in Digital Business.
4.	Prepare a report on societal impacts of Digital Business.
5.	Review various security aspects of Digital Business.
6.	Discuss the various steps for executing the business plan digitally.
7.	Develop a strategy for E-Business for selling a product online.
8.	Discuss a typical case study of any one Digital Business.

	Sa	witribai Phule P	Pune Univers	ity
	Fourth Yea	ar of <mark>E &amp; Tc En</mark>	gineering (20	019 Course)
		404195: Fiber	Optic Lab	
Tea	ching Scheme:	Credit	E	xamination Scheme:
Practica	al: 02 Hrs. / Week	01	Term Wo	ork: 25 Marks
			Oral:	50 Marks
Prerequi	site Courses, if any: -			
Compani	ion Course, if any:			
List	t of Laboratory Exp	eriments (Hardy	ware/Program	ms/Simulation Software)
		Grou	p A	
1.	To estimate the numer	ical aperture of given	n MMSI optical	fiber.
2.	To plot electrical and o	optical characteristic	s of any one opt	ical source LED/Laser.
3.	To measure attenuation coefficient and bending losses in optical fibers.			
4.	To plot characteristics of any one photo detector pn/pin/phototransistor.			
5.	Tutorial on optical key components: numerical on optical fiber, optical source and			
	photodetector.			
		Grou	p B	
1.	Establish a digital opti	cal link.		
2.	Simulate optical power	r budget and rise tim	e budget analys	is of optical fiber systems.
3.	Study of any one field	instrument such as o	optical power me	eter, OTDR, splicing machine etc
4.	Tutorial on optical linl	k budget: Optical po	wer budget & ri	se time budget analysis to commen
	on the viability of the	systems.		
		Grou	p C	
1.	Simulation of WDM	system to compute	OSNR using an	y simulation software.
2.	Study of current trend	ds in: optical sourc	es, detectors, fi	bers for telecommunication, mux
	demux, filters, isolate	ors, circulators, cou	plers, connecto	ors, optical amplifiers etc and the
	measuring instrument	s and standards.		

http://vlabs.iitb.ac.in/vlabs-dev/labs/physics-basics/labs/numerical-aperture-measurement- iitk/index.html

## (Physical Sciences Lab)

	Sa	avitribai Phule I	Pune University	y
	Fourth Yea	ar of <mark>E &amp; Tc En</mark>	gineering (201	9 Course)
		404196: Lab l		
Teach	ing Scheme:	Credit	Examinatio	on Scheme:
Practi	ical: 02 Hrs. / Week	01	Term Worl	k: 25 Marks
			Practical:	50 Marks
Prereq	uisite Courses, if any:			
Compa	nion Course, if any:			
1.	Biomedical Signal Process	sing (Elective - V)		
2.	Industrial Drives and Cont	trol (Elective - V)		
	Android Development (El			
	Embedded System Design	<i>,</i>		
5.	Mobile Computing (Electi	,		
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	Part C (Any 1 to be Performed)		
1.	Import the EEG signal and Determine the onset of the epileptic EEG pattern. Plot the power		
	spectrum of the signal.		
2.	Design a Filter to remove the noise in the ECG signal.		
3.	Implement LMS adaptive algorithm for noise cancellation.		
	UAL LAB LINKS: tps://bmsp-coep.vlabs.ac.in/List%20of%20experiments.html (Biomedical and Signal		
Processing Lab.) 2. <u>https://bmi-iitr.virtuallabs.ac.in/</u> (Biomedical Instrumentation Lab.)			

	Subject: Industrial Drives and Control (Elective - V)		
	List of Experiments		
1.	DC motor control using full singlephase converter.		
2.	Dual converter single phase controlled dC drives		
3.	Microprocessor/microcontroller based single phase controlled dc drives.		
4.	Four quadrant chopper reversible dc drives.		
5.	Three phase induction motor control using PWM inverters.		
6.	Microprocessor/microcontroller based single phase control AC drive.		
7.	Simulation of DC drives using of power SIM.		
8.	Simulation of AC drives using of power SIM.		
9.	Case study on drive application (Industrial Visit)		
	Industrial visit to company dealing with Variable Speed DC Drive replacing an existing troublesome DC control system, resulting in increased production and reduced downtime.		

# Subject: Android Development (Elective - V)

# **Guidelines for Instructor's Manual**

The instructor's manual is to be developed as a hands-on resource and reference. The instructor's manual need to include prologue (about University/program/ institute/ department/foreword/ preface etc), University syllabus, conduction & Assessment guidelines, topics under consideration-concept, objectives, outcomes, set of typical applications/assignments/ guidelines, and references.

## **Guidelines for Student Journal**

The laboratory assignments are to be submitted by student in the form of journal. Journal consists of prologue, Certificate, table of contents, and handwritten write-up of each assignment (Title, Objectives, Problem Statement, Outcomes, software & Hardware requirements, Date of Completion, Assessment grade/marks and assessor's sign, Theory-Concept in brief, features of tool/framework/language used, Design, test cases, conclusion.

Program codes with sample output of all performed assignments are to be submitted as softcopy.

As a conscious effort and little contribution towards Green IT and environment awareness, attaching printed papers as part of write-ups and program listing to journal may be avoided. Use of DVD containing students programs maintained by lab In-charge is highly encouraged. For reference one or two journals may be maintained with program prints at Laboratory.

### **Guidelines for Assessment**

Continuous assessment of laboratory work is done based on overall performance and lab assignments performance of student. Each lab assignment assessment will assign grade/marks based on parameters with appropriate weightage. Suggested parameters for overall assessment as well as each lab assignment assessment include- timely completion, performance, innovation, efficient codes, punctuality and neatness.

# **Guidelines for Laboratory Conduction**

- 1. The instructor is expected to frame the assignments by understanding the prerequisites, technological aspects, utility and recent trends related to the topic.
- 2. The assignment framing policy need to address the average students and inclusive of an element to attract and promote the intelligent students.
- 3. The instructor may set multiple sets of assignments and distribute among batches of students.
- 4. It is appreciated if the assignments are based on real world problems/applications. Encourage students for appropriate use of Hungarian notation, proper indentation and comments.
- 5. Use of open source software is to be encouraged.
- 6. In addition to these, instructor may assign one real life application in the form of a mini-project based on the concepts learned.
- 7. Instructor may also set one assignment or mini-project that is suitable to respective branch beyond the scope of syllabus.

List of Laboratory Assignments (Any 10 to be Performed)							
1.	Download Install and Configure Eclipse / Android Studio on Linux/windows platform.						
2.	Design a mobile application using implicit intent and explicit intent						
3.	Design a mobile application to create two fragment and pass the data from one fragment to						
	another						
4.	Design a mobile application to create home page using grid layout						
5.	Design a mobile application to create the login page using sqlite / firebase						
6.	Design a mobile application to share data in the app.						
7.	Design a mobile application to create registration application which having spinner (subject),						
	radio button (gender), qualification (check box), first insert the value and then show the data						
	in show activity.						
8.	Design a mobile application to create different dialog boxes and menu ( popup, option ,						
	context)						
9.	Design a mobile application to show list using Recycler View						
10.	Design a mobile application to Show any website using web view						
11.	Design a mobile application to Activity using fragment						
12.	Design a mobile application using imageslider to show images.						
13.	Design a mobile application for media player.						
14.	Design a mobile app to store data using internal or external storage.						
15.	Design a mobile app using Google Map and GPS to trace the location.						

Subject: Embedded System Design (Elective - V)					
Group A (Any 4 to be Performed)					
1.	Interface LED with STM32F4 and Toggle the LED by using delay functions				
2.	Make the LED ON when the input switch interfaced with STM32F4 is pressed				
3.	Interface LCD with STM32F4				
4.	Transmit/Receive a string "SPPU" using interrupt				
5.	Measure period and frequency using capture mode of PWM				
Group B (Any 2 to be Performed)					
1.	Write TIMER drivers using HAL functions				
2.	Write Analog-to-Digital Converter (ADC) drivers using HAL functions				
3.	Write PWM drivers using HAL functions				
4.	Displaying an image/graph on the SPI based LCD				
Group C (Any 2 to be Performed)					
1.	Learn how to Configure FreeRTOS Using CubeMX.				
2.	2. Examine the STM32F4 board thoroughly and prepare a detail report				
3.	5. Study the interfacing of LoRaWAN with STM32F4				
4.	Installation of android packages for embedded application				

# Virtual LAB Links:

- 1. https://docs.simuli.co/getting-started/stm32/using-virtual-lab-and-theia
- 2. <u>https://docs.jumper.io/docs/install.html</u>

List of Experiments (Any 8 to be performed)						
1.	Simulate to elaborate operation of multiple access techniques for CDMA.					
2.	Study of GSM architecture and signaling techniques.					
3.	Study of GPRS services.					
4.	Simulate BER performance over Rayleigh Fading wireless channel with BPSK transmission for SNR 0 to 60 dB.					
5.	Configuring a Cisco Router as a DHCP Server.					
6.	To understand the handover mechanism. http://vlabs.iitkgp.ernet.in/fcmc/exp8/index.html					
7.	To study the outage probability, LCR & ADF in SISO for Selection Combining and MRC (Flat Fading).         http://vlabs.iitkgp.ernet.in/fcmc/exp9/index.html					
8.	To Perform File Transfer in Client & Server Using TCP/IP.					
9.	Case Study on different real time mobile computing services.					

1. <u>http://vlabs.iitkgp.ernet.in/fcmc/</u> (Fading Channels and Mobile Communication Lab.)

Savitribai Phule Pune University Fourth Year of <mark>E &amp; Tc Engineering</mark> (2019 Course)								
404197: Project Phase – II								
Teaching Scheme:		Credit	Examination Scheme:					
Practical: 10 Hrs. / Week		05	Term Work: 100 Marks					
			Oral:	50 Marks				
Project phase 2 is extension of Project phase 1 carried out in seventh semester. The student shall prepare the duly certified Fourth report of project work in standard format preferably in LATEX for satisfactory completion of the work by the concerned guide and head of the Department/Institute.								
1.	The project TW/OR assessment shall be based on Live Project Demonstration and presentation by the students. The assessment parameters shall be Innovative Idea of selected project, literature survey, Depth of understanding, Applications, Individual contributions, presentations, project report, timely completion of work (Project review presentations), participation in project competition, publication of research work in journal/conference, publication in the form of patent and copyright etc. The college can prepare the rubrics based on these parameters							
2.	Certified hard bound project report to be submitted by the students in prescribed format.							
3.	Students must preferably publish at least one technical paper on project work in the conference or peer reviewed Journals or publish patent or copyright or should participate into one of the project competition at university/State/National/International level.							
4.	A log book of work carried out during the semester should be maintained with weekly review remarks by the guide and committee.							
5.	A certified copy of report preferably using LATEX is required to be presented to external							

examiner at the time of Fourth examination.

6. The project report must undergo by plagiarism check and the similarity index must be less than 10%. The plagiarism report should be included in the project report.