

LEDGER FOR VERIFICATION, NOT FOR DISTRIBUTION TO STUDENTS.

DATE : 17 JUNE 2016

CENTRE : AISSMS'S INSTITUTE OF INFORMATION TECHNOLOGY, PUNE

PAGE NO. 01 (251)

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80252501 GAURAW KUMAR		RAMRATI DEVI		, 71107783F		, T80252501		AIIT		, T80252501	
01. ENGG ECONOMICS & MANAGEMENT	PP	100	40	45	P C	12. POWER SYSTEM-II	PP	100	40	46	P C
02. MICRO-CONTROLLER APPLICATIONS	PP	100	40	AA	F	13. POWER SYSTEM-II	TW	25	10	13	P C
03. MICRO-CONTROLLER APPLICATIONS	OR	50	20	35	P C	14. POWER SYSTEM-II	PR	50	20	43	P C
04. ELECTRICAL MACHINE-II	PP	100	40	40	P C	15. ENERGY AUDIT & MANAGEMENT	PP	100	40	40	P C
05. ELECTRICAL MACHINE-II	TW	25	10	16	P C	16. UTILIZATION OF ELECTRICAL ENERGY	PP	100	40	57	P C
06. ELECTRICAL MACHINE-II	PR	50	20	33	P C	17. DESIGN OF ELECTRICAL MACHINES	PP	100	40	41	P C
07. POWER ELECTRONICS	PP	100	40	46	P C	18. DESIGN OF ELECTRICAL MACHINES	TW	25	10	11	P C
08. POWER ELECTRONICS	TW	25	10	14	P C	19. DESIGN OF ELECTRICAL MACHINES	OR	50	20	27	P C
09. POWER ELECTRONICS	PR	50	20	32	P C	20. CONTROL SYSTEM-I	PP	100	40	40	P C
10. ELECTRICAL INSTALL.MAINTEN.& TEST	PP	100	40	40	P C	21. CONTROL SYSTEM-I	TW	50	20	30	P C
11. ELECTRICAL INSTALL.MAINTEN.& TEST	TW	50	20	38	P C	22. SEMINAR	TW	50	20	26	P C

GRAND TOTAL = 713/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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T80253001 AGHOR KEDAR DILIPRAO CHHAYA , 71344963C , T80253001 , AIIT , T80253001

01. CONTROL SYSTEMS	PP	100	40	40	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	17	F
02. DIGITAL COMMUNICATION	PP	100	40	30	F	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	32	P C
03. DIGITAL COMMUNICATION	PR	50	20	32	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	45	P C
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	44	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	35	P C
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	40	P C
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	40	P C
07. MICROCONTROLLERS & APPLICATION	PR	50	20	20	P C	17. WAVE THEORY & ANTENNA	PP	100	40	13	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	44	P C	18. WAVE THEORY & ANTENNA	PR	50	20	20	P C
09. DIGITAL SIGNAL PROCESSING	OR	50	20	27	P C	19. MINI PROJECT & SEMINAR	OR	50	20	30	P C
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	21	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	28	P C

GRAND TOTAL = 636/1500, RESULT: FAILS A.T.K.T. RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

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T80253002 ARATI VASANT HOLE SANJANA , 71344965K , T80253002 , AIIT , T80253002

01. CONTROL SYSTEMS	PP	100	40	69	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	AA	F
02. DIGITAL COMMUNICATION	PP	100	40	08	F	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	38	P C
03. DIGITAL COMMUNICATION	PR	50	20	35	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	42	P C
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	31	P C
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	40	P C
06. MICROCONTROLLERS & APPLICATION	PP	100	40	47	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	45	P C
07. MICROCONTROLLERS & APPLICATION	PR	50	20	28	P C	17. WAVE THEORY & ANTENNA	PP	100	40	43	P C
08. DIGITAL SIGNAL PROCESSING	PP	100	40	41	P C	18. WAVE THEORY & ANTENNA	PR	50	20	27	P C
09. DIGITAL SIGNAL PROCESSING	OR	50	20	21	P C	19. MINI PROJECT & SEMINAR	OR	50	20	30	P C
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	29	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	27	P C

GRAND TOTAL = 680/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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T80253003 JAGADALE VRUSHALI PRAKASH NANDINI , 71052028L , T80253003 , AIIT , T80253003

01. CONTROL SYSTEMS	PP	100	40	63	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	40	P C
02. DIGITAL COMMUNICATION	PP	100	40	49	P	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	25	P C
03. DIGITAL COMMUNICATION	PR	50	20	28	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	47	P C
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	34	P C
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	32	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	40	P C
06. MICROCONTROLLERS & APPLICATION	PP	100	40	45	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	40	P C
07. MICROCONTROLLERS & APPLICATION	PR	50	20	22	P C	17. WAVE THEORY & ANTENNA	PP	100	40	40	P C
08. DIGITAL SIGNAL PROCESSING	PP	100	40	58	P C	18. WAVE THEORY & ANTENNA	PR	50	20	22	P C
09. DIGITAL SIGNAL PROCESSING	OR	50	20	25	P C	19. MINI PROJECT & SEMINAR	OR	50	20	36	P C
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	23	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	26	P C

GRAND TOTAL = 735/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

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CENTRE : AISSMS'S INSTITUTE OF INFORMATION TECHNOLOGY, PUNE

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

SEAT NO.	NAME OF THE CANDIDATE	MOTHER	PERMANENT REG. NO.	PREVIOUS SEAT NO.	COLLEGE	SEAT NO.	HEAD OF PASSING	MAX. MARKS	MIN. PASS MARKS	MARKS OBTAINED	P/F:PASS/FAIL	C:PREVIOUS CARRY OVER	
T80253007	SHARBIDRE YOGESH	SADHANA	, 71236702M	, T80253007	AIIT	, T80253007							
01.	CONTROL SYSTEMS	PP	100	40	40	P C	11.	SIGNAL CODING & ESTIMATION THEORY	PP	100	40	40	P C
02.	DIGITAL COMMUNICATION	PP	100	40	46	P	12.	SIGNAL CODING & ESTIMATION THEORY	PR	50	20	22	P C
03.	DIGITAL COMMUNICATION	PR	50	20	25	P C	13.	SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	40	P C
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P C	14.	SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	39	P C
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P C	15.	COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	40	P C
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	40	P C	16.	INDUSTRIAL MANAGEMENT	PP	100	40	43	P C
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	36	P C	17.	WAVE THEORY & ANTENNA	PP	100	40	40	P C
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	59	P C	18.	WAVE THEORY & ANTENNA	PR	50	20	20	P C
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	22	P C	19.	MINI PROJECT & SEMINAR	OR	50	20	34	P C
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	24	P C	20.	TEST & MEASUREMENT TECHNIQUES	OR	50	20	23	P C

GRAND TOTAL = 711/1500, RESULT: PASS CLASS

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T80253801 AKASH RANA NIRMALA , 71107693G , T80253801 , AIIT , T80253801

01. FEEDBACK CONTROL SYSTEMS	PP	100	40	44	P C	11. DRIVERS & CONTROL	PP	100	40	AA	F
02. DATA COMMUNICATION	PP	100	40	45	P C	12. DRIVERS & CONTROL	PR	50	20	22	P C
03. DATA COMMUNICATION	OR	50	20	23	P C	13. SENSORS & INTERFACES	PP	100	40	58	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	59	P	14. SENSORS & INTERFACES	TW	50	20	21	P C
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	28	P C	15. MICROCOMPUTER BASED SYSTEM	PP	100	40	AA	F
06. MICROCONTROLLER	PP	100	40	51	P C	16. MICROCOMPUTER BASED SYSTEM	PR	50	20	23	P C
07. MICROCONTROLLER	PR	50	20	22	P C	17. INDUSTRIAL MANAGEMENT	PP	100	40	67	P
08. POWER ELECTRONICS	PP	100	40	55	P	18. DISCRETE TIME SIGNAL PROCESSING	PP	100	40	63	P
09. POWER ELECTRONICS	PR	50	20	25	P C	19. MINI PROJECT & SEMINAR	OR	50	20	30	P C
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	25	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	21	P C

GRAND TOTAL = 682/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80253802 BARAVKAR EKNATH ASHOK KRUSHNABAI , 71344967F , T80253802 , AIIT , T80253802

01. FEEDBACK CONTROL SYSTEMS	PP	100	40	47	P C	11. DRIVERS & CONTROL	PP	100	40	49	P C
02. DATA COMMUNICATION	PP	100	40	AA	F	12. DRIVERS & CONTROL	PR	50	20	34	P C
03. DATA COMMUNICATION	OR	50	20	22	P C	13. SENSORS & INTERFACES	PP	100	40	40	P C
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P C	14. SENSORS & INTERFACES	TW	50	20	32	P C
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P C	15. MICROCOMPUTER BASED SYSTEM	PP	100	40	42	P C
06. MICROCONTROLLER	PP	100	40	49	P C	16. MICROCOMPUTER BASED SYSTEM	PR	50	20	21	P C
07. MICROCONTROLLER	PR	50	20	32	P C	17. INDUSTRIAL MANAGEMENT	PP	100	40	58	P C
08. POWER ELECTRONICS	PP	100	40	43	P C	18. DISCRETE TIME SIGNAL PROCESSING	PP	100	40	49	P
09. POWER ELECTRONICS	PR	50	20	30	P C	19. MINI PROJECT & SEMINAR	OR	50	20	44	P C
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	32	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	21	P C

GRAND TOTAL = 727/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80253803 INGLE KOMAL VINOD SUREKHA , 70921869K , T80253803 , AIIT , T80253803

01. FEEDBACK CONTROL SYSTEMS	PP	100	40	AA	F	11. DRIVERS & CONTROL	PP	100	40	43	P C
02. DATA COMMUNICATION	PP	100	40	51	P	12. DRIVERS & CONTROL	PR	50	20	25	P C
03. DATA COMMUNICATION	OR	50	20	21	P C	13. SENSORS & INTERFACES	PP	100	40	42	P C
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	AA	F	14. SENSORS & INTERFACES	TW	50	20	28	P C
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	32	P C	15. MICROCOMPUTER BASED SYSTEM	PP	100	40	49	P C
06. MICROCONTROLLER	PP	100	40	40	P C	16. MICROCOMPUTER BASED SYSTEM	PR	50	20	41	P C
07. MICROCONTROLLER	PR	50	20	30	P C	17. INDUSTRIAL MANAGEMENT	PP	100	40	40	P C
08. POWER ELECTRONICS	PP	100	40	46	P C	18. DISCRETE TIME SIGNAL PROCESSING	PP	100	40	40	P
09. POWER ELECTRONICS	PR	50	20	27	P C	19. MINI PROJECT & SEMINAR	OR	50	20	27	P C
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	20	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	30	P C

GRAND TOTAL = 632/1500, RESULT: FAILS A.T.K.T. [\$ 0.1]

ORDN. 1 MARKS : (18)1,

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80254603 KAPURE AJINKYA SUDHAKR		SHILPA		, 71107827M		, T80254603		, AIIT		, T80254603		
01. INSTRUMENT. FOR CHEMICAL ANALYSISPP	100	40	40	P	C	11. DIGITAL SIGNAL PROCESSING FUNDA.	PP	100	40	41	P	
02. INSTRUMENT. FOR CHEMICAL ANALYSISOR	50	20	22	P	C	12. DIGITAL SIGNAL PROCESSING FUNDA.	TW	50	20	20	P	
03. EMBEDDED SYSTEM DESIGN	PP	100	40	44	P	C	13. POWER PLANT INSTRUMENTATION	PP	100	40	47	P
04. EMBEDDED SYSTEM DESIGN	PR	50	20	20	P	C	14. PROCESS LOOP COMPONENTS	PP	100	40	40	P
05. CONTROL SYSTEM COMPONENTS	PP	100	40	44	P	C	15. PROCESS LOOP COMPONENTS	PR	50	20	33	P
06. CONTROL SYSTEM COMPONENTS	TW	50	20	39	P	C	16. INSTRUMENTATION SYSTEM DESIGN	PP	100	40	40	P
07. ELECTRONIC INSTRUMENTATION	PP	100	40	41	P	C	17. INSTRUMENTATION SYSTEM DESIGN	OR	50	20	30	P
08. ELECTRONIC INSTRUMENTATION	PR	50	20	32	P	C	18. CONTROL SYSTEM DESIGN	PP	100	40	45	P
09. INDUSTRIAL MANAGEMENT	PP	100	40	40	P	C	19. CONTROL SYSTEM DESIGN	TW	50	20	25	P
10. APPLICATION PROGRAMMING	TW	50	20	22	P	C	20. SEMINAR	TW	50	20	20	P

GRAND TOTAL = 685/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

T80254604 MANE PALLAVI UTTAM		SUNITA		, 71072057C		, T80254604		, AIIT		, T80254604		
01. INSTRUMENT. FOR CHEMICAL ANALYSISPP	100	40	42	P	C	11. DIGITAL SIGNAL PROCESSING FUNDA.	PP	100	40	49	P	
02. INSTRUMENT. FOR CHEMICAL ANALYSISOR	50	20	32	P	C	12. DIGITAL SIGNAL PROCESSING FUNDA.	TW	50	20	37	P	
03. EMBEDDED SYSTEM DESIGN	PP	100	40	40	P	C	13. POWER PLANT INSTRUMENTATION	PP	100	40	43	P
04. EMBEDDED SYSTEM DESIGN	PR	50	20	25	P	C	14. PROCESS LOOP COMPONENTS	PP	100	40	51	P
05. CONTROL SYSTEM COMPONENTS	PP	100	40	40	P	C	15. PROCESS LOOP COMPONENTS	PR	50	20	34	P
06. CONTROL SYSTEM COMPONENTS	TW	50	20	38	P	C	16. INSTRUMENTATION SYSTEM DESIGN	PP	100	40	51	P
07. ELECTRONIC INSTRUMENTATION	PP	100	40	40	P	C	17. INSTRUMENTATION SYSTEM DESIGN	OR	50	20	27	P
08. ELECTRONIC INSTRUMENTATION	PR	50	20	22	P	C	18. CONTROL SYSTEM DESIGN	PP	100	40	12	F
09. INDUSTRIAL MANAGEMENT	PP	100	40	41	P	C	19. CONTROL SYSTEM DESIGN	TW	50	20	37	P
10. APPLICATION PROGRAMMING	TW	50	20	39	P	C	20. SEMINAR	TW	50	20	37	P

GRAND TOTAL = 737/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80254605 MANISH KUMAR		MAMTA DEVI		, 71052099K		, T80254605		, AIIT		, T80254605		
01. INSTRUMENT. FOR CHEMICAL ANALYSISPP	100	40	41	P	C	11. DIGITAL SIGNAL PROCESSING FUNDA.	PP	100	40	40	P	
02. INSTRUMENT. FOR CHEMICAL ANALYSISOR	50	20	22	P	C	12. DIGITAL SIGNAL PROCESSING FUNDA.	TW	50	20	28	P	
03. EMBEDDED SYSTEM DESIGN	PP	100	40	30#	P	13. POWER PLANT INSTRUMENTATION	PP	100	40	53	P	
04. EMBEDDED SYSTEM DESIGN	PR	50	20	20	P	C	14. PROCESS LOOP COMPONENTS	PP	100	40	66	P
05. CONTROL SYSTEM COMPONENTS	PP	100	40	44	P	C	15. PROCESS LOOP COMPONENTS	PR	50	20	21	P
06. CONTROL SYSTEM COMPONENTS	TW	50	20	30	P	C	16. INSTRUMENTATION SYSTEM DESIGN	PP	100	40	43	P
07. ELECTRONIC INSTRUMENTATION	PP	100	40	40	P	C	17. INSTRUMENTATION SYSTEM DESIGN	OR	50	20	20	P
08. ELECTRONIC INSTRUMENTATION	PR	50	20	24	P	C	18. CONTROL SYSTEM DESIGN	PP	100	40	51	P
09. INDUSTRIAL MANAGEMENT	PP	100	40	46	P	C	19. CONTROL SYSTEM DESIGN	TW	50	20	32	P
10. APPLICATION PROGRAMMING	TW	50	20	35	P	C	20. SEMINAR	TW	50	20	22	P

GRAND TOTAL = 708/1500, RESULT: PASS CLASS # [0.4]

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SEAT NO.	NAME OF THE CANDIDATE	MOTHER	PERMANENT REG. NO.	PREVIOUS SEAT NO.	COLLEGE	SEAT NO.	HEAD OF PASSING	MAX. MARKS	MIN. PASS MARKS	MARKS OBTAINED	P/F:PASS/FAIL	C:PREVIOUS CARRY OVER
T80254606	SHEVATE MANGESH UTTAM	KUSUM	, 71132342K	, T80254606	AIIT	, T80254606						
01.	INSTRUMENT. FOR CHEMICAL ANALYSIS	PP	100	40	40	P C	11.	DIGITAL SIGNAL PROCESSING FUNDA.	PP	100	40	AA F
02.	INSTRUMENT. FOR CHEMICAL ANALYSIS	OR	50	20	27	P C	12.	DIGITAL SIGNAL PROCESSING FUNDA.	TW	50	20	32 P C
03.	EMBEDDED SYSTEM DESIGN	PP	100	40	44	P C	13.	POWER PLANT INSTRUMENTATION	PP	100	40	49 P C
04.	EMBEDDED SYSTEM DESIGN	PR	50	20	23	P C	14.	PROCESS LOOP COMPONENTS	PP	100	40	45 P C
05.	CONTROL SYSTEM COMPONENTS	PP	100	40	40	P C	15.	PROCESS LOOP COMPONENTS	PR	50	20	37 P C
06.	CONTROL SYSTEM COMPONENTS	TW	50	20	36	P C	16.	INSTRUMENTATION SYSTEM DESIGN	PP	100	40	49 P C
07.	ELECTRONIC INSTRUMENTATION	PP	100	40	40	P C	17.	INSTRUMENTATION SYSTEM DESIGN	OR	50	20	27 P C
08.	ELECTRONIC INSTRUMENTATION	PR	50	20	25	P C	18.	CONTROL SYSTEM DESIGN	PP	100	40	AA F
09.	INDUSTRIAL MANAGEMENT	PP	100	40	52	P C	19.	CONTROL SYSTEM DESIGN	TW	50	20	33 P C
10.	APPLICATION PROGRAMMING	TW	50	20	36	P C	20.	SEMINAR	TW	50	20	37 P C

GRAND TOTAL = 672/1500, RESULT: FAILS A.T.K.T.

RESULT RESERVED FOR BKLG

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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T80254201 BHANDARI ROHIT DILIP REKHA , 71051935E , T80254201 , AIIT , T80254201

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	AA	F
02. DATA COMMUNICATION	PP	100	40	40	P C	13. COMPUTER NETWORKS	PP	100	40	48	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	AA	F
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	51	P C
05. THEORY OF COMPUTATION	PP	100	40	42	P C	16. SOFTWARE ENGINEERING	PP	100	40	26	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P C	17. SOFTWARE LABORATORY	TW	25	10	19	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	20	P C	18. SOFTWARE LABORATORY	PR	50	20	26	P C
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P C	19. COMPUTER NETWORK	TW	25	10	18	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	28	P C	20. COMPUTER NETWORK	OR	50	20	30	P C
10. HARDWARE LABORATORY	TW	25	10	22	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	35	P C
11. HARDWARE LABORATORY	PR	50	20	26	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	36	P C

GRAND TOTAL = 658/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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T80254204 KALE RADHEYA DNYANDEEP ANJALI , 71107820D , T80254204 , AIIT , T80254204

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	49	P C
02. DATA COMMUNICATION	PP	100	40	42	P C	13. COMPUTER NETWORKS	PP	100	40	54	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	44	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	46	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	40	P C
05. THEORY OF COMPUTATION	PP	100	40	53	P C	16. SOFTWARE ENGINEERING	PP	100	40	40	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	44	P C	17. SOFTWARE LABORATORY	TW	25	10	15	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	43	P C	18. SOFTWARE LABORATORY	PR	50	20	32	P C
08. SIGNAL PROCESSING LABORATORY	TW	25	10	23	P C	19. COMPUTER NETWORK	TW	25	10	12	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	24	P C	20. COMPUTER NETWORK	OR	50	20	36	P C
10. HARDWARE LABORATORY	TW	25	10	23	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	35	P C
11. HARDWARE LABORATORY	PR	50	20	20	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	36	P C

GRAND TOTAL = 791/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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T80254205 PATIL DIPAK KASHINATH REKHA , 71348151L , T80254205 , AIIT , T80254205

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	46	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	AA	F
02. DATA COMMUNICATION	PP	100	40	47	P C	13. COMPUTER NETWORKS	PP	100	40	AA	F
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	AA	F	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	AA	F
04. DIGITAL SIGNAL PROCESSING	PP	100	40	AA	F	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	AA	F
05. THEORY OF COMPUTATION	PP	100	40	AA	F	16. SOFTWARE ENGINEERING	PP	100	40	AA	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	36	P C	17. SOFTWARE LABORATORY	TW	25	10	20	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	23	P C	18. SOFTWARE LABORATORY	PR	50	20	30	P C
08. SIGNAL PROCESSING LABORATORY	TW	25	10	17	P C	19. COMPUTER NETWORK	TW	25	10	20	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	35	P C	20. COMPUTER NETWORK	OR	50	20	20	P C
10. HARDWARE LABORATORY	TW	25	10	16	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	40	P C
11. HARDWARE LABORATORY	PR	50	20	21	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	38	P C

GRAND TOTAL = 409/1500, RESULT: FAILS

RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

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LEDGER FOR VERIFICATION, NOT FOR DISTRIBUTION TO STUDENTS.

DATE : 17 JUNE 2016

CENTRE : AISSMS'S INSTITUTE OF INFORMATION TECHNOLOGY, PUNE

PAGE NO. 02 (258)

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80254206 PHADTARE SUJEET DINKAR					BHARTI					, 71208732L , T80254206 , AIIT , T80254206					
01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P	C	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	47	P	C
02.	DATA COMMUNICATION	PP	100	40	64	P		13.	COMPUTER NETWORKS	PP	100	40	45	P	C
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	41	P		14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	50	P	C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	17	F		15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	40	P	C
05.	THEORY OF COMPUTATION	PP	100	40	40	P	C	16.	SOFTWARE ENGINEERING	PP	100	40	52	P	C
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P	C	17.	SOFTWARE LABORATORY	TW	25	10	13	P	C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	20	P	C	18.	SOFTWARE LABORATORY	PR	50	20	24	P	C
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	19	P	C	19.	COMPUTER NETWORK	TW	25	10	11	P	C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	35	P	C	20.	COMPUTER NETWORK	OR	50	20	20	P	C
10.	HARDWARE LABORATORY	TW	25	10	18	P	C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	22	P	C
11.	HARDWARE LABORATORY	PR	50	20	35	P	C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	22	P	C

GRAND TOTAL = 710/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80254207 SALUNKHE NISHANT MADHAVRAO					RANJANA					, 70807643C , T80254207 , AIIT , T80254207					
01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	41	P	C	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	40	P	C
02.	DATA COMMUNICATION	PP	100	40	42	P	C	13.	COMPUTER NETWORKS	PP	100	40	52	P	C
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P	C	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	50	P	C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	AA	F		15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	43	P	C
05.	THEORY OF COMPUTATION	PP	100	40	AA	F		16.	SOFTWARE ENGINEERING	PP	100	40	48	P	C
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P	C	17.	SOFTWARE LABORATORY	TW	25	10	12	P	C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	25	P	C	18.	SOFTWARE LABORATORY	PR	50	20	23	P	C
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	15	P	C	19.	COMPUTER NETWORK	TW	25	10	12	P	C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	22	P	C	20.	COMPUTER NETWORK	OR	50	20	22	P	C
10.	HARDWARE LABORATORY	TW	25	10	18	P	C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	25	P	C
11.	HARDWARE LABORATORY	PR	50	20	23	P	C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	24	P	C

GRAND TOTAL = 612/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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DATE : 17 JUNE 2016

CENTRE : AISSMS'S INSTITUTE OF INFORMATION TECHNOLOGY, PUNE

PAGE NO. 01 (259)

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80258501 AJAY KUMAR		KAMLESH DEVI		, 71208486L		, T80258501		, AIIT		, T80258501	
01. OPERATING SYSTEM	PP	100	40	AA	F	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	44	P C
02. THEORY OF COMPUTATION	PP	100	40	48	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	58	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	40	P C	15. PROGRAMMING PARADIGMS	PP	100	40	AA	F
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	40	P C
05. SOFTWARE ENGINEERING	PP	100	40	01	F	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	40	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	40	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	28	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	41	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	45	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	37	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	36	P C
10. NETWORK LABORATORY	TW	25	10	20	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	46	P C
11. NETWORK LABORATORY	OR	50	20	26	P C						
12. SOFT SKILLS LABORATORY	TW	25	10	21	P C						

GRAND TOTAL = 688/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80258502 BRAHMNE PRADNYA RAMDAS		SADHANA		, 71107732M		, T80258502		, AIIT		, T80258502	
01. OPERATING SYSTEM	PP	100	40	40	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	44	P
02. THEORY OF COMPUTATION	PP	100	40	40	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	50	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	50	P C	15. PROGRAMMING PARADIGMS	PP	100	40	43	P C
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	44	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	53	P
05. SOFTWARE ENGINEERING	PP	100	40	42	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	42	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	13	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	37	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	24	P	19. SOFTWARE DESIGN LABORATORY	PR	50	20	20	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	13	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	29	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	33	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	25	P C
10. NETWORK LABORATORY	TW	25	10	19	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	27	P C
11. NETWORK LABORATORY	OR	50	20	36	P C						
12. SOFT SKILLS LABORATORY	TW	25	10	18	P C						

GRAND TOTAL = 742+08/1500, RESULT: SECOND CLASS [0.2]

ORDN. 1 MARKS :

T80258503 PAGORE MAYUR SUBHASH		RANJANA		, 71208696L		, T80258503		, AIIT		, T80258503	
01. OPERATING SYSTEM	PP	100	40	40	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	53	P C
02. THEORY OF COMPUTATION	PP	100	40	50	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	40	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	50	P C	15. PROGRAMMING PARADIGMS	PP	100	40	47	P C
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	61	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	59	P C
05. SOFTWARE ENGINEERING	PP	100	40	52	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	43	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	36	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	25	P	19. SOFTWARE DESIGN LABORATORY	PR	50	20	28	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	22	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	22	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	38	P C
10. NETWORK LABORATORY	TW	25	10	12	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	27	P C
11. NETWORK LABORATORY	OR	50	20	22	P C						
12. SOFT SKILLS LABORATORY	TW	25	10	19	P C						

GRAND TOTAL = 781/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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DATE : 17 JUNE 2016

CENTRE : AISSMS'S INSTITUTE OF INFORMATION TECHNOLOGY, PUNE

PAGE NO. 02 (260)

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80258505 SHAIKH FAIZAN MUZAFFAR SHAIKH NAHID MUZAFFA , 71351279C , T80258505 , AIIT , T80258505

01. OPERATING SYSTEM	PP	100	40	43	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	40	P C
02. THEORY OF COMPUTATION	PP	100	40	AA	F	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	44	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	40	P C	15. PROGRAMMING PARADIGMS	PP	100	40	52	P C
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	48	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	42	P C
05. SOFTWARE ENGINEERING	PP	100	40	44	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	40	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	25	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	25	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	30	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	27	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	37	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	37	P C
10. NETWORK LABORATORY	TW	25	10	12	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	32	P C
11. NETWORK LABORATORY	OR	50	20	20	P C						
12. SOFT SKILLS LABORATORY	TW	25	10	13	P C						

GRAND TOTAL = 684/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80258506 SHEHJAR KILAM RENU , 70932076M , T80258506 , AIIT , T80258506

01. OPERATING SYSTEM	PP	100	40	40	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	40	P C
02. THEORY OF COMPUTATION	PP	100	40	67	P	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	47	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	54	P C	15. PROGRAMMING PARADIGMS	PP	100	40	44	P C
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	49	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	70	P
05. SOFTWARE ENGINEERING	PP	100	40	50	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	70	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	12	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	23	P	19. SOFTWARE DESIGN LABORATORY	PR	50	20	25	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	29	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	21	P C
10. NETWORK LABORATORY	TW	25	10	12	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	36	P C
11. NETWORK LABORATORY	OR	50	20	26	P C						
12. SOFT SKILLS LABORATORY	TW	25	10	12	P C						

GRAND TOTAL = 800/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

T80258508 SHINDE SONALI GANPAT RAJANI , 71351281E , T80258508 , AIIT , T80258508

01. OPERATING SYSTEM	PP	100	40	41	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	47	P C
02. THEORY OF COMPUTATION	PP	100	40	40	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	46	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	56	P C	15. PROGRAMMING PARADIGMS	PP	100	40	49	P C
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	43	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	40	P C
05. SOFTWARE ENGINEERING	PP	100	40	42	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	AA	F
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	22	P	19. SOFTWARE DESIGN LABORATORY	PR	50	20	35	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	16	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	28	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	35	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	26	P C
10. NETWORK LABORATORY	TW	25	10	17	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	34	P C
11. NETWORK LABORATORY	OR	50	20	AA	F						
12. SOFT SKILLS LABORATORY	TW	25	10	15	P C						

GRAND TOTAL = 670/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

RESULT RESERVED FOR BKLG

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DATE : 17 JUNE 2016

CENTRE : AISSMS'S INSTITUTE OF INFORMATION TECHNOLOGY, PUNE

PAGE NO. 03 (261)

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80258509 UDATE MRUNAL SUDHAKAR		SUNANDA		, 71351282C		, T80258509		AIIT		, T80258509	
01. OPERATING SYSTEM	PP	100	40	50	P	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	AA	F
02. THEORY OF COMPUTATION	PP	100	40	AA	F	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	41	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	41	P C	15. PROGRAMMING PARADIGMS	PP	100	40	43	P C
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	AA	F
05. SOFTWARE ENGINEERING	PP	100	40	53	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	AA	F
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	15	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F	19. SOFTWARE DESIGN LABORATORY	PR	50	20	AA	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	16	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	39	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	32	P C
10. NETWORK LABORATORY	TW	25	10	12	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	35	P C
11. NETWORK LABORATORY	OR	50	20	32	P C						
12. SOFT SKILLS LABORATORY	TW	25	10	15	P C						

GRAND TOTAL = 522/1500, RESULT: FAILS

RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :