

LEDGER FOR VERIFICATION, NOT FOR DISTRIBUTION TO STUDENTS.

DATE : 16 JUNE 2016

CENTRE : AISSMS'S INSTITUTE OF INFORMATION TECHNOLOGY, PUNE

PAGE NO. 01 (186)

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80252502 TILAK RAHUL GAUTAM		MUKTA		, 71052249F		, S80252573		AIIT		, S80252502	
01. POWER PLANT ENGINEERING	PP	100	40	40	P C	11. POWER SYSTEM I	PP	100	40	40	P C
02. POWER PLANT ENGINEERING	TW	50	20	38	P C	12. ELECTRICAL MACHINES I	PP	100	40	40	P C
03. ENGINEERING MATHS III	PP	100	40	40	P C	13. ELECTRICAL MACHINES I	PR	50	20	20	P C
04. MATERIAL SCIENCE	PP	100	40	40	P C	14. NETWORK ANALYSIS	PP	100	40	40	P C
05. MATERIAL SCIENCE	TW	50	20	33	P C	15. NETWORK ANALYSIS	TW	50	20	20	P C
06. ANALOG & DIGITAL ELECTRONICS	PP	100	40	40	P C	16. DIGITAL COMPUTATIONAL TECHNIQUES	PP	100	40	40	P C
07. ANALOG & DIGITAL ELECTRONICS	PR	50	20	22	P C	17. COMPUTER PROGRAMMING	TW	50	20	22	P C
08. ELECTRICAL MEASUREMENTS & INSTRU.	PP	100	40	52	P	18. COMPUTER PROGRAMMING	PR	50	20	20	P C
09. ELECTRICAL MEASUREMENTS & INSTRU.	PR	50	20	22	P C	19. MICROPROCESSOR FUNDAMEN.& APPLI	PP	100	40	40	P C
10. MODERN MANUFACTURING TECHNIQUES	TW	50	20	36	P C	20. MICROPROCESSOR FUNDAMEN.& APPLI	OR	50	20	20	P C

GRAND TOTAL = 665/1500, RESULT: PASS CLASS [\$ 0.1]

ORDN. 1 MARKS : (18)2,

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S80253001		AGHOR KEDAR DILIPRAO				CHHAYA		, 71344963C		, S80253002		AIIT		, S80253001	
01.	SIGNAL AND SYSTEMS	PP	100	40	40	P	C	11.	ENGINEERING MATHEMATICS III	PP	100	40	16	F	
02.	SIGNAL AND SYSTEMS	OR	50	20	29	P	C	12.	ENGINEERING MATHEMATICS III	TW	25	10	22	P	C
03.	SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	40	P	C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P	C
04.	SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	24	P	C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	28	P	C
05.	NETWORK ANALYSIS	PP	100	40	41	P	C	15.	ELECTROMAGNETIC	PP	100	40	40	P	C
06.	DIGITAL LOGIC DESIGN	PP	100	40	40	P	C	16.	ELECTROMAGNETIC	TW	25	10	20	P	C
07.	DIGITAL LOGIC DESIGN	PR	50	20	23	P	C	17.	DATA STRUCTURES	PP	100	40	41	P	C
08.	POWER DEVICES AND MACHINES	PP	100	40	40	P	C	18.	DATA STRUCTURES	PR	50	20	32	P	C
09.	NETWORK AND POWER LAB.	TW	50	20	38	P	C	19.	COMMUNICATION THEORY	PP	100	40	40	P	C
10.	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	43	P	C	20.	COMMUNICATION THEORY	OR	50	20	27	P	C
								21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	P	C

GRAND TOTAL = 702/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S80253002		AKASH RANA				NIRMALA		, 71107693G		, S80253151		AIIT		, S80253002	
01.	SIGNAL AND SYSTEMS	PP	100	40	47	P	C	11.	ENGINEERING MATHEMATICS III	PP	100	40	43	P	C
02.	SIGNAL AND SYSTEMS	OR	50	20	27	P	C	12.	ENGINEERING MATHEMATICS III	TW	25	10	17	P	C
03.	SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	40	P	C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	48	P	C
04.	SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	30	P	C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	27	P	C
05.	NETWORK ANALYSIS	PP	100	40	45	P	C	15.	ELECTROMAGNETIC	PP	100	40	56	P	C
06.	DIGITAL LOGIC DESIGN	PP	100	40	44	P	C	16.	ELECTROMAGNETIC	TW	25	10	14	P	C
07.	DIGITAL LOGIC DESIGN	PR	50	20	26	P	C	17.	DATA STRUCTURES	PP	100	40	50	P	C
08.	POWER DEVICES AND MACHINES	PP	100	40	51	P		18.	DATA STRUCTURES	PR	50	20	37	P	C
09.	NETWORK AND POWER LAB.	TW	50	20	34	P	C	19.	COMMUNICATION THEORY	PP	100	40	42	P	C
10.	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	22	P	C	20.	COMMUNICATION THEORY	OR	50	20	36	P	C
								21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	25	P	C

GRAND TOTAL = 761/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

S80253004		CHAVAN ANIRUDH GOVINDRAO				NIRMALA		, 71344971D		, S80253019		AIIT		, S80253004	
01.	SIGNAL AND SYSTEMS	PP	100	40	40	P	C	11.	ENGINEERING MATHEMATICS III	PP	100	40	40	P	
02.	SIGNAL AND SYSTEMS	OR	50	20	26	P	C	12.	ENGINEERING MATHEMATICS III	TW	25	10	14	P	C
03.	SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	40	P	C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P	C
04.	SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	30	P	C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	22	P	C
05.	NETWORK ANALYSIS	PP	100	40	41	P	C	15.	ELECTROMAGNETIC	PP	100	40	44	P	C
06.	DIGITAL LOGIC DESIGN	PP	100	40	40	P	C	16.	ELECTROMAGNETIC	TW	25	10	18	P	C
07.	DIGITAL LOGIC DESIGN	PR	50	20	28	P	C	17.	DATA STRUCTURES	PP	100	40	40	P	C
08.	POWER DEVICES AND MACHINES	PP	100	40	40	P	C	18.	DATA STRUCTURES	PR	50	20	20	P	C
09.	NETWORK AND POWER LAB.	TW	50	20	30	P	C	19.	COMMUNICATION THEORY	PP	100	40	40	P	C
10.	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	28	P	C	20.	COMMUNICATION THEORY	OR	50	20	25	P	C
								21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	40	P	C

GRAND TOTAL = 686/1500, RESULT: PASS CLASS [\$ 0.1]

ORDN. 1 MARKS : (11)3,

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80253007 KAMBLE DEEPALI DIGAMBAR					SUKUMARI					, 71344991J , S80253059 , AIIT , S80253007					
01.	SIGNAL AND SYSTEMS	PP	100	40	40	P	C	11.	ENGINEERING MATHEMATICS III	PP	100	40	52	P	
02.	SIGNAL AND SYSTEMS	OR	50	20	23	P	C	12.	ENGINEERING MATHEMATICS III	TW	25	10	19	P	C
03.	SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	40	P	C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P	C
04.	SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	34	P	C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	P	C
05.	NETWORK ANALYSIS	PP	100	40	45	P	C	15.	ELECTROMAGNETIC	PP	100	40	40	P	C
06.	DIGITAL LOGIC DESIGN	PP	100	40	44	P	C	16.	ELECTROMAGNETIC	TW	25	10	20	P	C
07.	DIGITAL LOGIC DESIGN	PR	50	20	29	P	C	17.	DATA STRUCTURES	PP	100	40	40	P	C
08.	POWER DEVICES AND MACHINES	PP	100	40	40	P	C	18.	DATA STRUCTURES	PR	50	20	24	P	C
09.	NETWORK AND POWER LAB.	TW	50	20	38	P	C	19.	COMMUNICATION THEORY	PP	100	40	40	P	C
10.	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	41	P	C	20.	COMMUNICATION THEORY	OR	50	20	28	P	C
								21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	39	P	C

GRAND TOTAL = 746+04/1500, RESULT: SECOND CLASS [0.2]

ORDN. 1 MARKS :

S80253008 KUMAR GAURAV					GEETA					, 71107857C , S80253238 , AIIT , S80253008					
01.	SIGNAL AND SYSTEMS	PP	100	40	48	P		11.	ENGINEERING MATHEMATICS III	PP	100	40	10	F	
02.	SIGNAL AND SYSTEMS	OR	50	20	34	P	C	12.	ENGINEERING MATHEMATICS III	TW	25	10	11	P	C
03.	SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	40	P	C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P	C
04.	SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	32	P	C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	22	P	C
05.	NETWORK ANALYSIS	PP	100	40	00	F		15.	ELECTROMAGNETIC	PP	100	40	AA	F	
06.	DIGITAL LOGIC DESIGN	PP	100	40	40	P	C	16.	ELECTROMAGNETIC	TW	25	10	12	P	C
07.	DIGITAL LOGIC DESIGN	PR	50	20	25	P	C	17.	DATA STRUCTURES	PP	100	40	42	P	C
08.	POWER DEVICES AND MACHINES	PP	100	40	40	P	C	18.	DATA STRUCTURES	PR	50	20	30	P	C
09.	NETWORK AND POWER LAB.	TW	50	20	24	P	C	19.	COMMUNICATION THEORY	PP	100	40	40	P	C
10.	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	20	P	C	20.	COMMUNICATION THEORY	OR	50	20	28	P	C
								21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	22	P	C

GRAND TOTAL = 560/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S80253009 NAIK PUSHPAK NITIN					MANISHA					, 71208679L , S80253096 , AIIT , S80253009					
01.	SIGNAL AND SYSTEMS	PP	100	40	40	P	C	11.	ENGINEERING MATHEMATICS III	PP	100	40	34#	P	
02.	SIGNAL AND SYSTEMS	OR	50	20	25	P	C	12.	ENGINEERING MATHEMATICS III	TW	25	10	18	P	C
03.	SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	40	P	C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P	C
04.	SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	21	P	C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	22	P	C
05.	NETWORK ANALYSIS	PP	100	40	43	P	C	15.	ELECTROMAGNETIC	PP	100	40	40	P	C
06.	DIGITAL LOGIC DESIGN	PP	100	40	40	P	C	16.	ELECTROMAGNETIC	TW	25	10	12	P	C
07.	DIGITAL LOGIC DESIGN	PR	50	20	35	P	C	17.	DATA STRUCTURES	PP	100	40	42	P	C
08.	POWER DEVICES AND MACHINES	PP	100	40	40	P	C	18.	DATA STRUCTURES	PR	50	20	31	P	C
09.	NETWORK AND POWER LAB.	TW	50	20	21	P	C	19.	COMMUNICATION THEORY	PP	100	40	40	P	C
10.	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	20	P	C	20.	COMMUNICATION THEORY	OR	50	20	20	P	C
								21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	22	P	C

GRAND TOTAL = 646/1500, RESULT: PASS CLASS # [0.4]

ORDN. 1 MARKS :

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S80253011 SADIGALE AKASH ASHOK		KALPANA		, 71107940E		, S80253123		, AIIT		, S80253011					
01.	SIGNAL AND SYSTEMS	PP	100	40	40	P	C	11.	ENGINEERING MATHEMATICS III	PP	100	40	40	P	C
02.	SIGNAL AND SYSTEMS	OR	50	20	21	P	C	12.	ENGINEERING MATHEMATICS III	TW	25	10	19	P	C
03.	SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	40	P	C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P	C
04.	SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	25	P	C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	20	P	
05.	NETWORK ANALYSIS	PP	100	40	AA	F		15.	ELECTROMAGNETIC	PP	100	40	40	P	C
06.	DIGITAL LOGIC DESIGN	PP	100	40	40	P	C	16.	ELECTROMAGNETIC	TW	25	10	16	P	C
07.	DIGITAL LOGIC DESIGN	PR	50	20	21	P	C	17.	DATA STRUCTURES	PP	100	40	AA	F	
08.	POWER DEVICES AND MACHINES	PP	100	40	AA	F		18.	DATA STRUCTURES	PR	50	20	22	P	C
09.	NETWORK AND POWER LAB.	TW	50	20	21	P	C	19.	COMMUNICATION THEORY	PP	100	40	40	P	C
10.	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	20	P	C	20.	COMMUNICATION THEORY	OR	50	20	26	P	C
								21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	30	P	C

GRAND TOTAL = 521/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S80253012 SINNARKAR PARAG DEEPAK		RUPALI		, 70932087G		, S80253287		, AIIT		, S80253012					
01.	SIGNAL AND SYSTEMS	PP	100	40	40	P	C	11.	ENGINEERING MATHEMATICS III	PP	100	40	25	F	
02.	SIGNAL AND SYSTEMS	OR	50	20	30	P	C	12.	ENGINEERING MATHEMATICS III	TW	25	10	16	P	C
03.	SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	40	P	C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P	C
04.	SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	20	P	C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	21	P	C
05.	NETWORK ANALYSIS	PP	100	40	40	P	C	15.	ELECTROMAGNETIC	PP	100	40	45	P	C
06.	DIGITAL LOGIC DESIGN	PP	100	40	41	P	C	16.	ELECTROMAGNETIC	TW	25	10	17	P	C
07.	DIGITAL LOGIC DESIGN	PR	50	20	24	P	C	17.	DATA STRUCTURES	PP	100	40	42	P	C
08.	POWER DEVICES AND MACHINES	PP	100	40	40	P	C	18.	DATA STRUCTURES	PR	50	20	32	P	C
09.	NETWORK AND POWER LAB.	TW	50	20	39	P	C	19.	COMMUNICATION THEORY	PP	100	40	40	P	C
10.	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P	C	20.	COMMUNICATION THEORY	OR	50	20	20	P	C
								21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	25	P	C

GRAND TOTAL = 675/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S80253014 TRIVEDI NITISH ASHWINKUMAR		DEEPA		, 71345017H		, S80253141		, AIIT		, S80253014					
01.	SIGNAL AND SYSTEMS	PP	100	40	40	P	C	11.	ENGINEERING MATHEMATICS III	PP	100	40	19	F	
02.	SIGNAL AND SYSTEMS	OR	50	20	27	P	C	12.	ENGINEERING MATHEMATICS III	TW	25	10	12	P	C
03.	SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	40	P	C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	52	P	C
04.	SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	25	P	C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	21	P	C
05.	NETWORK ANALYSIS	PP	100	40	41	P	C	15.	ELECTROMAGNETIC	PP	100	40	26	F	
06.	DIGITAL LOGIC DESIGN	PP	100	40	40	P	C	16.	ELECTROMAGNETIC	TW	25	10	11	P	C
07.	DIGITAL LOGIC DESIGN	PR	50	20	31	P	C	17.	DATA STRUCTURES	PP	100	40	40	P	C
08.	POWER DEVICES AND MACHINES	PP	100	40	40	P	C	18.	DATA STRUCTURES	PR	50	20	24	P	C
09.	NETWORK AND POWER LAB.	TW	50	20	34	P	C	19.	COMMUNICATION THEORY	PP	100	40	40	P	C
10.	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	24	P	C	20.	COMMUNICATION THEORY	OR	50	20	30	P	C
								21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	20	P	C

GRAND TOTAL = 637/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S80254601		BADADE ROHIT SURESH				SHARDA		, 71347666E		, S80254605		AIIT		, S80254601	
01.	ENGINEERING MATHEMATICS III	PP	100	40	40	P	C	12.	DIGITAL TECHNIQUES	PP	100	40	40	P	C
02.	FUNDAMENTALS OF INSTRUMENTATION	PP	100	40	41	P	C	13.	DIGITAL TECHNIQUES	PR	50	20	22	P	C
03.	FUNDAMENTALS OF INSTRUMENTATION	TW	50	20	32	P	C	14.	APPLIED ELECTRONICS	PP	100	40	40	P	C
04.	LINEAR INTEGRATED CIRCUIT I	PP	100	40	55	P	C	15.	APPLIED ELECTRONICS	PR	50	20	27	P	C
05.	LINEAR INTEGRATED CIRCUIT I	PR	50	20	23	P	C	16.	TRANSDUCERS & SIGNAL CONDITIONING	PP	100	40	47	P	C
06.	PRINCIPLES SENSORS & TRANSDUCERS	PP	100	40	50	P	C	17.	TRANSDUCERS & SIGNAL CONDITIONING	TW	25	10	18	P	C
07.	PRINCIPLES SENSORS & TRANSDUCERS	TW	25	10	16	P	C	18.	TRANSDUCERS & SIGNAL CONDITIONING	PR	50	20	21	P	C
08.	PRINCIPLES SENSORS & TRANSDUCERS	PR	50	20	20	P	C	19.	PHONOTICS & INSTRUMENTATION	PP	100	40	43	P	C
09.	AUTOMATIC CONTROL SYSTEM	PP	100	40	42	P	C	20.	PHONOTICS & INSTRUMENTATION	TW	50	20	35	P	C
10.	AUTOMATIC CONTROL SYSYTEM	TW	50	20	35	P	C	21.	DRIVES AND CONTROLS	PP	100	40	13	F	
11.	SOFT SKILL	TW	25	10	17	P	C	22.	DRIVES AND CONTROLS	TW	25	10	12	P	C

GRAND TOTAL = 689/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S80254602		DAIVADNYA SANKET KISHOR				MANISHA		, 71051964J		, S80254615		AIIT		, S80254602	
01.	ENGINEERING MATHEMATICS III	PP	100	40	34#	P		12.	DIGITAL TECHNIQUES	PP	100	40	40	P	C
02.	FUNDAMENTALS OF INSTRUMENTATION	PP	100	40	40	P	C	13.	DIGITAL TECHNIQUES	PR	50	20	28	P	C
03.	FUNDAMENTALS OF INSTRUMENTATION	TW	50	20	35	P	C	14.	APPLIED ELECTRONICS	PP	100	40	40	P	C
04.	LINEAR INTEGRATED CIRCUIT I	PP	100	40	41	P	C	15.	APPLIED ELECTRONICS	PR	50	20	44	P	C
05.	LINEAR INTEGRATED CIRCUIT I	PR	50	20	27	P	C	16.	TRANSDUCERS & SIGNAL CONDITIONING	PP	100	40	40	P	C
06.	PRINCIPLES SENSORS & TRANSDUCERS	PP	100	40	44	P	C	17.	TRANSDUCERS & SIGNAL CONDITIONING	TW	25	10	17	P	C
07.	PRINCIPLES SENSORS & TRANSDUCERS	TW	25	10	16	P	C	18.	TRANSDUCERS & SIGNAL CONDITIONING	PR	50	20	22	P	C
08.	PRINCIPLES SENSORS & TRANSDUCERS	PR	50	20	30	P	C	19.	PHONOTICS & INSTRUMENTATION	PP	100	40	40	P	C
09.	AUTOMATIC CONTROL SYSTEM	PP	100	40	40	P	C	20.	PHONOTICS & INSTRUMENTATION	TW	50	20	37	P	C
10.	AUTOMATIC CONTROL SYSYTEM	TW	50	20	34	P	C	21.	DRIVES AND CONTROLS	PP	100	40	40	P	C
11.	SOFT SKILL	TW	25	10	19	P	C	22.	DRIVES AND CONTROLS	TW	25	10	12	P	C

GRAND TOTAL = 720/1500, RESULT: PASS CLASS # [0.4]

ORDN. 1 MARKS :

S80254603		IRMALWAR SHRIKANT RAMESHRAO				JAYSHRI		, 71208572G		, S80254626		AIIT		, S80254603	
01.	ENGINEERING MATHEMATICS III	PP	100	40	15	F		12.	DIGITAL TECHNIQUES	PP	100	40	40\$	P	
02.	FUNDAMENTALS OF INSTRUMENTATION	PP	100	40	40	P	C	13.	DIGITAL TECHNIQUES	PR	50	20	23	P	C
03.	FUNDAMENTALS OF INSTRUMENTATION	TW	50	20	35	P	C	14.	APPLIED ELECTRONICS	PP	100	40	42	P	C
04.	LINEAR INTEGRATED CIRCUIT I	PP	100	40	51	P	C	15.	APPLIED ELECTRONICS	PR	50	20	22	P	C
05.	LINEAR INTEGRATED CIRCUIT I	PR	50	20	32	P	C	16.	TRANSDUCERS & SIGNAL CONDITIONING	PP	100	40	40	P	C
06.	PRINCIPLES SENSORS & TRANSDUCERS	PP	100	40	40	P	C	17.	TRANSDUCERS & SIGNAL CONDITIONING	TW	25	10	18	P	C
07.	PRINCIPLES SENSORS & TRANSDUCERS	TW	25	10	20	P	C	18.	TRANSDUCERS & SIGNAL CONDITIONING	PR	50	20	28	P	C
08.	PRINCIPLES SENSORS & TRANSDUCERS	PR	50	20	28	P	C	19.	PHONOTICS & INSTRUMENTATION	PP	100	40	40	P	C
09.	AUTOMATIC CONTROL SYSTEM	PP	100	40	40	P	C	20.	PHONOTICS & INSTRUMENTATION	TW	50	20	32	P	C
10.	AUTOMATIC CONTROL SYSYTEM	TW	50	20	38	P	C	21.	DRIVES AND CONTROLS	PP	100	40	40	P	C
11.	SOFT SKILL	TW	25	10	20	P	C	22.	DRIVES AND CONTROLS	TW	25	10	14	P	C

GRAND TOTAL = 698/1500, RESULT: FAILS A.T.K.T. [\$ 0.1]

ORDN. 1 MARKS : (12)2,

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OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80254605 PANDIT SAYALI SUDHAKAR AMRUTA , 71347679G , S80254646 , AIIT , S80254605

01. ENGINEERING MATHEMATICS III	PP	100	40	09	F	12. DIGITAL TECHNIQUES	PP	100	40	40	P	C	
02. FUNDAMENTALS OF INSTRUMENTATION	PP	100	40	40	P	C	13. DIGITAL TECHNIQUES	PR	50	20	29	P	C
03. FUNDAMENTALS OF INSTRUMENTATION	TW	50	20	32	P	C	14. APPLIED ELECTRONICS	PP	100	40	40	P	C
04. LINEAR INTEGRATED CIRCUIT I	PP	100	40	40	P	C	15. APPLIED ELECTRONICS	PR	50	20	24	P	C
05. LINEAR INTEGRATED CIRCUIT I	PR	50	20	22	P	C	16. TRANSDUCERS & SIGNAL CONDITIONING	PP	100	40	40	P	C
06. PRINCIPLES SENSORS & TRANSDUCERS	PP	100	40	40	P	C	17. TRANSDUCERS & SIGNAL CONDITIONING	TW	25	10	17	P	C
07. PRINCIPLES SENSORS & TRANSDUCERS	TW	25	10	18	P	C	18. TRANSDUCERS & SIGNAL CONDITIONING	PR	50	20	22	P	C
08. PRINCIPLES SENSORS & TRANSDUCERS	PR	50	20	23	P	C	19. PHONOTICS & INSTRUMENTATION	PP	100	40	40	P	C
09. AUTOMATIC CONTROL SYSTEM	PP	100	40	11	F		20. PHONOTICS & INSTRUMENTATION	TW	50	20	35	P	C
10. AUTOMATIC CONTROL SYSTEM	TW	50	20	36	P	C	21. DRIVES AND CONTROLS	PP	100	40	40	P	C
11. SOFT SKILL	TW	25	10	18	P	C	22. DRIVES AND CONTROLS	TW	25	10	14	P	C

GRAND TOTAL = 630/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S80254606 SHERIKAR SHAHED MAHAMMAD SANJIDA , 71347683E , S80254658 , AIIT , S80254606

01. ENGINEERING MATHEMATICS III	PP	100	40	12	F	12. DIGITAL TECHNIQUES	PP	100	40	40	P	C	
02. FUNDAMENTALS OF INSTRUMENTATION	PP	100	40	40	P	C	13. DIGITAL TECHNIQUES	PR	50	20	22	P	C
03. FUNDAMENTALS OF INSTRUMENTATION	TW	50	20	38	P	C	14. APPLIED ELECTRONICS	PP	100	40	43	P	C
04. LINEAR INTEGRATED CIRCUIT I	PP	100	40	44	P	C	15. APPLIED ELECTRONICS	PR	50	20	22	P	C
05. LINEAR INTEGRATED CIRCUIT I	PR	50	20	29	P	C	16. TRANSDUCERS & SIGNAL CONDITIONING	PP	100	40	40	P	C
06. PRINCIPLES SENSORS & TRANSDUCERS	PP	100	40	45	P	C	17. TRANSDUCERS & SIGNAL CONDITIONING	TW	25	10	19	P	C
07. PRINCIPLES SENSORS & TRANSDUCERS	TW	25	10	19	P	C	18. TRANSDUCERS & SIGNAL CONDITIONING	PR	50	20	21	P	C
08. PRINCIPLES SENSORS & TRANSDUCERS	PR	50	20	23	P	C	19. PHONOTICS & INSTRUMENTATION	PP	100	40	40	P	C
09. AUTOMATIC CONTROL SYSTEM	PP	100	40	40	P	C	20. PHONOTICS & INSTRUMENTATION	TW	50	20	32	P	C
10. AUTOMATIC CONTROL SYSTEM	TW	50	20	38	P	C	21. DRIVES AND CONTROLS	PP	100	40	48	P	C
11. SOFT SKILL	TW	25	10	18	P	C	22. DRIVES AND CONTROLS	TW	25	10	12	P	C

GRAND TOTAL = 685/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S80254607 SHEVATE MANGESH UTTAM KUSUM , 71132342K , S80254722 , AIIT , S80254607

01. ENGINEERING MATHEMATICS III	PP	100	40	13	F	12. DIGITAL TECHNIQUES	PP	100	40	40	P	C	
02. FUNDAMENTALS OF INSTRUMENTATION	PP	100	40	41	P	C	13. DIGITAL TECHNIQUES	PR	50	20	20	P	C
03. FUNDAMENTALS OF INSTRUMENTATION	TW	50	20	35	P	C	14. APPLIED ELECTRONICS	PP	100	40	50	P	C
04. LINEAR INTEGRATED CIRCUIT I	PP	100	40	49	P	C	15. APPLIED ELECTRONICS	PR	50	20	23	P	C
05. LINEAR INTEGRATED CIRCUIT I	PR	50	20	20	P	C	16. TRANSDUCERS & SIGNAL CONDITIONING	PP	100	40	46	P	C
06. PRINCIPLES SENSORS & TRANSDUCERS	PP	100	40	40	P	C	17. TRANSDUCERS & SIGNAL CONDITIONING	TW	25	10	20	P	C
07. PRINCIPLES SENSORS & TRANSDUCERS	TW	25	10	16	P	C	18. TRANSDUCERS & SIGNAL CONDITIONING	PR	50	20	25	P	C
08. PRINCIPLES SENSORS & TRANSDUCERS	PR	50	20	22	P	C	19. PHONOTICS & INSTRUMENTATION	PP	100	40	40	P	C
09. AUTOMATIC CONTROL SYSTEM	PP	100	40	45	P	C	20. PHONOTICS & INSTRUMENTATION	TW	50	20	31	P	C
10. AUTOMATIC CONTROL SYSTEM	TW	50	20	30	P	C	21. DRIVES AND CONTROLS	PP	100	40	44	P	C
11. SOFT SKILL	TW	25	10	17	P	C	22. DRIVES AND CONTROLS	TW	25	10	19	P	C

GRAND TOTAL = 686/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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CENTRE : AISSMS'S INSTITUTE OF INFORMATION TECHNOLOGY, PUNE

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80254608 THORAT MAYUR PRADIP		ALAKA		, 71347686K		, S80254664		, AIIT		, S80254608	
01. ENGINEERING MATHEMATICS III	PP	100	40	07	F	12. DIGITAL TECHNIQUES	PP	100	40	40	P C
02. FUNDAMENTALS OF INSTRUMENTATION	PP	100	40	40	P C	13. DIGITAL TECHNIQUES	PR	50	20	27	P C
03. FUNDAMENTALS OF INSTRUMENTATION	TW	50	20	34	P C	14. APPLIED ELECTRONICS	PP	100	40	40	P C
04. LINEAR INTEGRATED CIRCUIT I	PP	100	40	40	P C	15. APPLIED ELECTRONICS	PR	50	20	29	P C
05. LINEAR INTEGRATED CIRCUIT I	PR	50	20	22	P C	16. TRANSDUCERS & SIGNAL CONDITIONING	PP	100	40	40	P C
06. PRINCIPLES SENSORS & TRANSDUCERS	PP	100	40	40	P C	17. TRANSDUCERS & SIGNAL CONDITIONING	TW	25	10	17	P C
07. PRINCIPLES SENSORS & TRANSDUCERS	TW	25	10	18	P C	18. TRANSDUCERS & SIGNAL CONDITIONING	PR	50	20	24	P C
08. PRINCIPLES SENSORS & TRANSDUCERS	PR	50	20	23	P C	19. PHONOTICS & INSTRUMENTATION	PP	100	40	48	P C
09. AUTOMATIC CONTROL SYSTEM	PP	100	40	09	F	20. PHONOTICS & INSTRUMENTATION	TW	50	20	32	P C
10. AUTOMATIC CONTROL SYSYTEM	TW	50	20	37	P C	21. DRIVES AND CONTROLS	PP	100	40	40	P C
11. SOFT SKILL	TW	25	10	18	P C	22. DRIVES AND CONTROLS	TW	25	10	14	P C

GRAND TOTAL = 639/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S80254609 VALSANGE PRADNYA ASHOK		SULOCHANA		, 71347687H		, S80254665		, AIIT		, S80254609	
01. ENGINEERING MATHEMATICS III	PP	100	40	AA	F	12. DIGITAL TECHNIQUES	PP	100	40	40	P C
02. FUNDAMENTALS OF INSTRUMENTATION	PP	100	40	40	P C	13. DIGITAL TECHNIQUES	PR	50	20	21	P C
03. FUNDAMENTALS OF INSTRUMENTATION	TW	50	20	40	P C	14. APPLIED ELECTRONICS	PP	100	40	AA	F
04. LINEAR INTEGRATED CIRCUIT I	PP	100	40	40	P C	15. APPLIED ELECTRONICS	PR	50	20	22	P C
05. LINEAR INTEGRATED CIRCUIT I	PR	50	20	22	P C	16. TRANSDUCERS & SIGNAL CONDITIONING	PP	100	40	AA	F
06. PRINCIPLES SENSORS & TRANSDUCERS	PP	100	40	18	F	17. TRANSDUCERS & SIGNAL CONDITIONING	TW	25	10	16	P C
07. PRINCIPLES SENSORS & TRANSDUCERS	TW	25	10	20	P C	18. TRANSDUCERS & SIGNAL CONDITIONING	PR	50	20	23	P C
08. PRINCIPLES SENSORS & TRANSDUCERS	PR	50	20	22	P C	19. PHONOTICS & INSTRUMENTATION	PP	100	40	AA	F
09. AUTOMATIC CONTROL SYSTEM	PP	100	40	AA	F	20. PHONOTICS & INSTRUMENTATION	TW	50	20	32	P C
10. AUTOMATIC CONTROL SYSYTEM	TW	50	20	40	P C	21. DRIVES AND CONTROLS	PP	100	40	17	F
11. SOFT SKILL	TW	25	10	19	P C	22. DRIVES AND CONTROLS	TW	25	10	12	P C

GRAND TOTAL = 444/1500, RESULT: FAILS [\$ 0.1]

ORDN. 1 MARKS : (12)1,

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OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80254202 MUNDHE SACHIN RAMKRUSHNA GEETA , 71208674K , S80254237 , AIIT , S80254202

01. DISCRETE STRUCTURES	PP	100	40	45	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P C
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	09	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C	13. DATA STRUCTURES	PP	100	40	40	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	45	P C	14. COMPUTER GRAPHICS	PP	100	40	40	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	09	F
06. PROGRAMMING LABORATORY	TW	25	10	15	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	23	P C
07. PROGRAMMING LABORATORY	PR	50	20	31	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	39	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	24	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	24	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	20	P C
10. SOFT SKILLS	TW	50	20	35	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	25	P C
						21. DATA STRUCTURES LABORATORY	PR	50	20	27	P C

GRAND TOTAL = 627/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S80254203 PADVI SOHAMNIRANJAN MOHANSING SHAKUNTALA , 70807599B , S80254318 , AIIT , S80254203

01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	22	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P C
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C	13. DATA STRUCTURES	PP	100	40	40	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	43	P C	14. COMPUTER GRAPHICS	PP	100	40	40	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P C
06. PROGRAMMING LABORATORY	TW	25	10	17	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	30	P C
07. PROGRAMMING LABORATORY	PR	50	20	22	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	23	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	29	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	21	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	20	P C
10. SOFT SKILLS	TW	50	20	34	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	29	P C
						21. DATA STRUCTURES LABORATORY	PR	50	20	30	P C

GRAND TOTAL = 658/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S80254204 PATIL DIPAK KASHINATH REKHA , 71348151L , S80254247 , AIIT , S80254204

01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	14	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	45	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	50	P C
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	46	P C	13. DATA STRUCTURES	PP	100	40	40	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	40	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	48	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P C
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	39	P C
07. PROGRAMMING LABORATORY	PR	50	20	41	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	30	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	38	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	29	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	21	P C
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	39	P C
						21. DATA STRUCTURES LABORATORY	PR	50	20	30	P C

GRAND TOTAL = 749/1500, RESULT: FAILS A.T.K.T.

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OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80258501 AJAY KUMAR		KAMLESH DEVI		, 71208486L		, S80258503		, AIIT		, S80258501	
01. DISCRETE STRUCTURES	PP	100	40	46	P C	11. ENG MATHS III	PP	100	40	40	P C
02. COMPUTER ORGANIZATION	PP	100	40	47	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	52	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	46	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	43	P C	14. DATA STRUCTURES AND FILES	PP	100	40	51	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	40	P C
06. DIGITAL LABORATORY	TW	50	20	45	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P C
07. DIGITAL LABORATORY	PR	50	20	37	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	38	P C
08. PROGRAMMING LABORATORY	TW	50	20	47	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P C
09. PROGRAMMING LABORATORY	PR	50	20	47	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	42	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	47	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	32	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	38	P C

GRAND TOTAL = 856/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S80258502 BHALERAO PRASHANT VIJAY		SARALA		, 70931805H		, S80258507		, AIIT		, S80258502	
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	40	P C
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	46	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	44	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	42	P C	15. DATA COMMUNICATION	PP	100	40	58	P C
06. DIGITAL LABORATORY	TW	50	20	32	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	13	P C
07. DIGITAL LABORATORY	PR	50	20	22	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	20	P C
08. PROGRAMMING LABORATORY	TW	50	20	30	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	12	P C
09. PROGRAMMING LABORATORY	PR	50	20	20	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	21	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	22	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	30	P C

GRAND TOTAL = 687/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

S80258503 KAMBLE SNEHAL ANIL		LATA		, 70931915M		, AIIT		, S80258503			
01. DISCRETE STRUCTURES	PP	100	40	44	P C	11. ENG MATHS III	PP	100	40	21	F
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	43	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	48	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	43	P C	14. DATA STRUCTURES AND FILES	PP	100	40	42	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	50	P C	15. DATA COMMUNICATION	PP	100	40	49	P C
06. DIGITAL LABORATORY	TW	50	20	33	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P C
07. DIGITAL LABORATORY	PR	50	20	34	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	35	P C
08. PROGRAMMING LABORATORY	TW	50	20	32	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	18	P C
09. PROGRAMMING LABORATORY	PR	50	20	20	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	22	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	33	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	24	P C

GRAND TOTAL = 733/1500, RESULT: FAILS A.T.K.T.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80258504 KATRE SHIVKUMAR BABULAL VANITA , 71208605G , S80258533 , AIIT , S80258504

01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	AA	F
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	45	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	54	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	49	P C	14. DATA STRUCTURES AND FILES	PP	100	40	47	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C	15. DATA COMMUNICATION	PP	100	40	40	P C
06. DIGITAL LABORATORY	TW	50	20	30	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	15	P C
07. DIGITAL LABORATORY	PR	50	20	32	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	20	P C
08. PROGRAMMING LABORATORY	TW	50	20	30	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	14	P C
09. PROGRAMMING LABORATORY	PR	50	20	30	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	20	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	32	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	21	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	29	P C

GRAND TOTAL = 672/1500, RESULT: FAILS A.T.K.T. [\$ 0.1]

ORDN. 1 MARKS : (19)2,

S80258506 LAD PRIYANKA VIJAY VARSHA , 71351273D , S80258538 , AIIT , S80258506

01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	46	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	49	P C	15. DATA COMMUNICATION	PP	100	40	54	P C
06. DIGITAL LABORATORY	TW	50	20	36	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P C
07. DIGITAL LABORATORY	PR	50	20	38	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	27	P C
08. PROGRAMMING LABORATORY	TW	50	20	35	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P C
09. PROGRAMMING LABORATORY	PR	50	20	25	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	42	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	37	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	39	P C

GRAND TOTAL = 785/1500, RESULT: SECOND CLASS [\$ 0.1]

ORDN. 1 MARKS : (11)3,

S80258507 NAGARGOJE RAMCHANDRA BALASAHEB SAGNYAN , 71351274B , S80258547 , AIIT , S80258507

01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	57	P
02. COMPUTER ORGANIZATION	PP	100	40	43	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	43	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	50	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	45	P C
06. DIGITAL LABORATORY	TW	50	20	37	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P C
07. DIGITAL LABORATORY	PR	50	20	30	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	37	P C
08. PROGRAMMING LABORATORY	TW	50	20	36	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P C
09. PROGRAMMING LABORATORY	PR	50	20	25	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	23	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	33	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	32	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	26	P C

GRAND TOTAL = 756/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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DATE : 16 JUNE 2016

CENTRE : AISSMS'S INSTITUTE OF INFORMATION TECHNOLOGY, PUNE

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80258508 PATNE VIHAR CHANDAN		CHITRA		, 71052145G		, S80258629		AIIT		, S80258508	
01. DISCRETE STRUCTURES	PP	100	40	AA	F	11. ENG MATHS III	PP	100	40	AA	F
02. COMPUTER ORGANIZATION	PP	100	40	AA	F	12. COMPUTER GRAPHICS	PP	100	40	AA	F
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	AA	F	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	AA	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	AA	F	14. DATA STRUCTURES AND FILES	PP	100	40	AA	F
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	AA	F
06. DIGITAL LABORATORY	TW	50	20	20	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	18	P C
07. DIGITAL LABORATORY	PR	50	20	27	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	AA	F
08. PROGRAMMING LABORATORY	TW	50	20	25	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P C
09. PROGRAMMING LABORATORY	PR	50	20	39	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	30	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	20	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	35	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	AA	F

GRAND TOTAL = 271/1500, RESULT: FAILS

RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

S80258509 SHAIKH FAIZAN MUZAFFAR		NAHID		, 71351279C		, S80258565		AIIT		, S80258509	
01. DISCRETE STRUCTURES	PP	100	40	41	P C	11. ENG MATHS III	PP	100	40	54	P
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	49	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	41	P C	15. DATA COMMUNICATION	PP	100	40	43	P C
06. DIGITAL LABORATORY	TW	50	20	36	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	12	P C
07. DIGITAL LABORATORY	PR	50	20	30	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	26	P C
08. PROGRAMMING LABORATORY	TW	50	20	38	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	13	P C
09. PROGRAMMING LABORATORY	PR	50	20	36	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	29	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	21	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	30	P C

GRAND TOTAL = 737/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

S80258510 SHINDE SONALI GANPAT		RAJANI		, 71351281E		, S80258568		AIIT		, S80258510	
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	24	F
02. COMPUTER ORGANIZATION	PP	100	40	58	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	57	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	49	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	54	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C	15. DATA COMMUNICATION	PP	100	40	60	P C
06. DIGITAL LABORATORY	TW	50	20	35	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	18	P C
07. DIGITAL LABORATORY	PR	50	20	28	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	27	P C
08. PROGRAMMING LABORATORY	TW	50	20	35	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	21	P C
09. PROGRAMMING LABORATORY	PR	50	20	27	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	21	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	22	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	29	P C

GRAND TOTAL = 764/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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CENTRE : AISSMS'S INSTITUTE OF INFORMATION TECHNOLOGY, PUNE

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80258511 UDATE MRUNAL SUDHAKAR		SUNANDA		, 71351282C		, S80258572		AIIT		, S80258511	
01. DISCRETE STRUCTURES	PP	100	40	42	P C	11. ENG MATHS III	PP	100	40	08	F
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	44	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	AA	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	41	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C	15. DATA COMMUNICATION	PP	100	40	43	P C
06. DIGITAL LABORATORY	TW	50	20	37	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	13	P C
07. DIGITAL LABORATORY	PR	50	20	32	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	36	P C
08. PROGRAMMING LABORATORY	TW	50	20	36	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P C
09. PROGRAMMING LABORATORY	PR	50	20	31	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	35	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	21	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	31	P C

GRAND TOTAL = 666/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :